Open Architecture Tester

What is a key issue of OAT ?

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Abstract - This paper summarizes an outline of OAT(Open Architecture Tester), and a positioning of OAT in an LSI manufacturing process is clarified. A mass-production test can be prepared synchronizing with the next SoC generation development & production by the OAT introduction. The panel will be discussed the key issues required in order to make this advantage of the OAT.

Introduction

OAT is introduced in a SoC test. A Semiconductor Test Consortium (STC) is established in order to realize OAT. The mission of the consortium are as follows [1].
The mission of the STC will be to support the development and long-term success of OPENSTAR™ system architecture. This architecture delivers unparalleled technical and economic performance; is truly open and enabled for solution development; and provides true multi vendor interoperability both at the hardware and software level.

Consortium Goals

1.Architectural Development. Support driving the direction of the development of the platform architecture consistent with the mission statement.
2.Truly Open Architecture. Ensure a truly open architecture by publishing the architecture and promoting understanding of the architectures with training programs and workshops.
3.Full Third Party Enabling. Ensure full interoperability by identifying requirements (developers kits, etc.) and driving solutions to meet these requirements. Define and manage validation procedures to insure complete solution interoperability.
4.Semiconductor Test Open Architecture Marketing and Promotion. The consortium will engage in marketing and promotional activities to ensure the successful implementation of the Open Architecture

Key issues

The concept of OAT is excellent. The approach is good. Figure 1 is an example of a test solution development on the OAT approach. The test solution can be prepared just in time on a new SoC production line. Key word is “Introduction support !!! Particulars contrary ???”. What to know is to meet successful examples (including future models).

References

[1] STC Web Site http://www.semitest.org

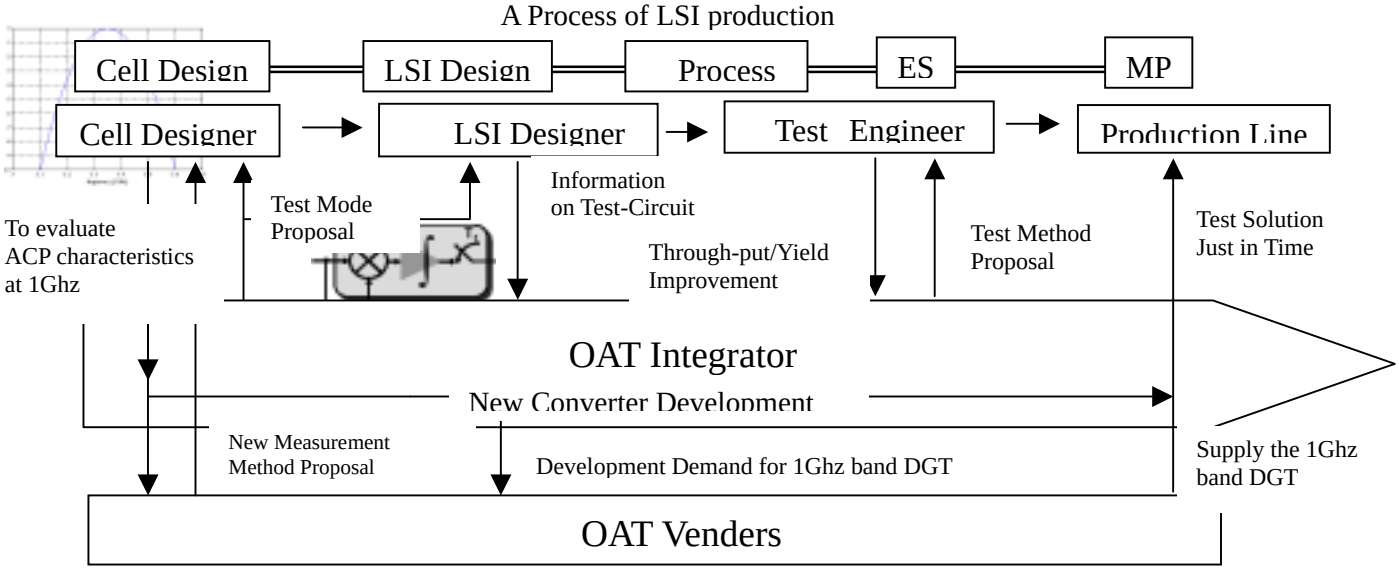

Fig1. Test Solution development on the OAT approach