

ECRTS05

Final WCET05 workshop programme

5th Intl. Workshop on Worst-Case Execution Time Analysis. WCET 2005. Chair: Reinhard Wilhelm.

- 09:00 Bus to Workshop venue departs from Hotel Saratoga.
- 09:30 - 10:00 Registration.
- 10:00 - 11:30 Session 1: Measurement-based methods for WCET determination. Chair: Reinhard Wilhelm.
- Issues using the Nexus Interface for Measurement-Based WCET Analysis.**
A. Betts, G. Bernat, University of York, UK.
- Safe Measurement-based WCET Estimation.**
J. F. Deverge, I. Puaut, Université de Rennes, France.
- WCET Measurement using modified path testing.**
N. Williams, Commissariat à l'Energie Atomique, CEA, France.
- 11:30 - 12:00 Coffee break.
- 12:00 - 13:30 Session 2: Industrial Experience and Education. Chair: Lothar Thiele, ETH Zürich.
- Computing the WCET of an Avionics Program by Abstract Interpretation.**
J. Souyris, E. le Pavec, G. Himbert, V. Jégu, G. Borios and R. Heckmann, Airbus France, Atos Origin Integration and AbsInt GmbH, Germany.
- Experiences from Industrial WCET Analysis Case Studies.**
A. Ermedahl, J. Gustafsson, B. Lisper, Mälardalen University, Sweden.
- Using a WCET Analysis Tool in Real-Time Systems Education.**
S. Petersson, A. Ermedahl, A. Pettersson, D. Sundmark and N. Holsti, Mälardalen University, Sweden and Tidorium Ltd, Helsinki, Finland.
- 13:30 - 15:00 Lunch break.
- 15:00 - 16:45 Session 3: Modeling and Compiler Support. Chair: Björn Lisper.
- Analysis of Memory Latencies in Multi-Processor Systems.**
J. Staschulat, S. Schiecker, M. Ivers, R. Ernst, Technical University of Braunschweig, Germany.
- Efficient Analysis of Pipeline Models for WCET Computation.**
S. Wilhelm, AbsInt GmbH and Saarland University, Germany.
- Compiler-Support for Worst-Case Execution Time Analysis of Optimized Code.**
Raimund Kirner, TU Wien, Austria.
- Exploiting Branch Constraints without Explicit Path Enumeration.**
T. Chen, T. Mitra, A. Roychoudhury, V. Suhendra, School of Computing, National University of Singapore.
- 16:45 - 17:15 Coffee break.
- 17:15 - 18:15 Invited Talk.
- Composable Real-Time Analysis.**
Lothar Thiele, ETH Zürich.
- 19:00 - Bus departs from *Edifici Jovellanos* back to Hotel Saratoga.