# Refinement of the Parallel $\mathsf{CD}_x$

# Technical Report

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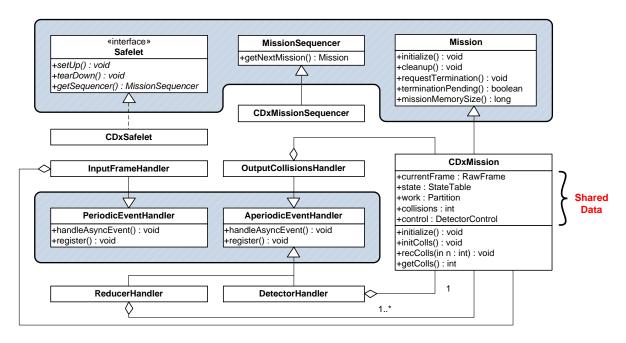


Figure 1: UML diagram for the concurrent  $CD_x$  program

## 1 Introduction

The purpose of the  $\mathsf{CD}_x$  is to detect potential collisions of aircraft located by a radar device. We take the program discussed in [8] as a basis for the definition of our requirements. It uses a cyclic executive, and embeds the assumption that the radar collects (and buffers) a frame of aircraft positions that becomes available for input periodically. In each iteration, the  $\mathsf{CD}_x$ : (1) reads a frame; (2) carries out a voxel-hashing step that maps aircraft to voxels; (3) checks for collisions in each voxel; and (4) records and reports the number of detected collisions. Unlike [8], we allow aircraft to enter or leave the radar frame.

Since the majority of the computation burden is in the checking for collisions in step (3), we propose a version of the  $CD_x$  where this task is parallelised. As a result, we obtain an SCJ program that illustrates the features of SCJ Level 1. Our aim with the concurrent  $CD_x$  is, most of all, to provide a genuine and more representative Level 1 application. Due to the novelty of the SCJ paradigm and technology, such applications are still difficult to come by in the public domain. On the other hand, even though we are not specifying a particular radar system, concurrent collision detection is a reasonable target to improve the performance of such an application. The program code is available via http://www.cs.york.ac.uk/circus/hijac/.

A voxel is a volumetric element; all voxels together subdivide the entire space. The voxels in the  $\mathsf{CD}_x$  superimpose a coarse 2-dimensional grid on the x-y plane with the height of a voxel extending along the entire z-axis. Thus, the altitude of aircraft is abstracted away. This reduces the number of necessary collision tests: after mapping aircraft to the voxels that are intersected by their interpolated trajectories, it is sufficient to test for possible collisions within each voxel. Details of the algorithm can be found in [8].

The concurrent  $CD_x$  consists of a single mission that instantiates seven handlers. Figure 1 presents a UML class diagram that illustrates the design. The classes shaded are part of the SCJ API. The classes CDxSafelet, CDxMissionSequencer and CDxMission implement the safelet, the mission sequencer, and the mission. The behaviour of the setUp() and tearDown() methods of CDxSafelet is void, and getSequencer() simply returns an instance of CDxMissionSequencer. Likewise, getNextMission() returns an instance of CDxMission when called for the first time. Since the mission does not terminate, getNextMission() is not called again.

In the mission execution, first the initialize() method of CDxMission is called. It creates the handler objects and shared data in mission memory. The handler classes are InputFrameHandler, "OutputCollisions-

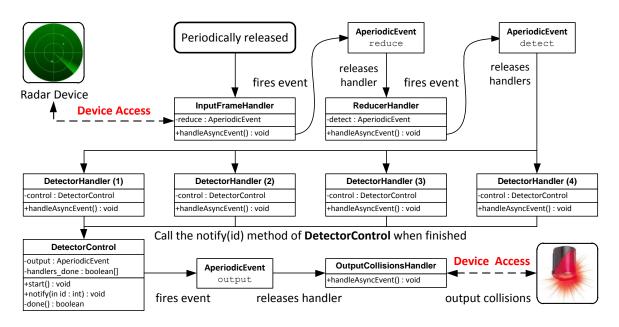


Figure 2: Parallel  $\mathsf{CD}_x$  control flow

Handler", ReducerHandler, and DetectorHandler. We choose to create four instances of DetectorHandler, possibly corresponding to a scenario in which we have four processors. The refinement in the remainder of the report can, however, proceed without significant changes in the presence of a different number of instances. A more general design for the  $\mathsf{CD}_x$  could allow the configuration of the number of instances; our program, however, is enough to illustrate the main aspects of our technique.

The shared data is held by public fields of CDxMission. The currentFrame and state fields record the current and previous frame of aircraft positions; recording previous positions is important for calculating their predicted motions. As we divide and distribute the computational work, work holds the partitions of voxels to be checked by each of the detection handlers, and collisions is used to accumulate the result of the detection. Another shared object control plays a crucial part in orchestrating the execution of handlers.

Figure 2 summarises the control mechanism of the SCJ application. The three software events, reduce, detect and output, are used to control execution of the handlers. The program design ensures that the handlers effectively execute sequentially in each cycle, apart from the four instances of DetectorHandler, which carry out their work concurrently.

The InputFrameHandler is the only periodic handler. It is released at the beginning of each cycle to interact with the hardware to read the frame into currentFrame and update state accordingly. Afterwards, it releases the ReducerHandler, via the reduce software event, to carry out the voxel-based reduction step. This handler also partitions and distributes the work among the detector handlers by populating work. Once this is done, it concurrently releases all DetectorHandler instances by firing the detect event. These handlers carry out the actual detection work and store their result in collisions. The mechanism for releasing OutputCollisionsHandler, which outputs the number of collisions to an external device, uses the shared object control. Its class type DetectorControl provides a method notify(int id), which is called by the detector handlers at the end of each release. It fires the event output when all detection work is done. This illustrates that sharing may occur not only to exchange data between handlers, but also in the design of execution control, and our refinement strategy will have to cater for this.

Our program highlights various features of the SCJ mission framework: the subdivision of a mission into handlers, the control of handlers via software events, and the sharing of data for both data communication and control purposes. The verification of this program not only has to address functional correctness, but also must show that the flow of activities in Figure 2 can be executed within the duration of a cycle.

## 2 Preliminaries

In this section, we present preliminary definitions of types, operators and functions that are used later on in the models.

#### 2.1 Extensions

We have already introduced a nondeterministic wait statement with the following semantics.

wait 
$$S =_{df} \prod t : S \bullet \text{ wait } t$$

It turns out to be useful to have an alternative construct that allows us to refer to the actual time waited via a bound identifier.

wait 
$$t: S \bullet A(t) =_{\mathrm{df}} \prod t: S \bullet \text{ wait } t; A(t)$$

With this, A can obtain information regarding the delay resulting from the **wait**; this is used in a few places in specifying the models, in particular for the  $\mathsf{E}$  anchor.

In addition to the above, we introduce several further extensions to the SCJ Circus language.

- 1. A generic Array class to model one-dimensional Java arrays of a given type.
- 2. Methods to get and set the elements of an array as well as obtain its size:
  - getA(index:int):T
  - setA(index : int, value : T)
  - length(): int
- 3. Support for simple for loops. This is via the action construct for  $i = n_0$  to  $n_1 \bullet A(i)$ .
- 4. Support for the creation of software events. For this we have the **newEvent** construct.
- 5. Software events are fired using the **fire** construct.

#### 2.2 Reals

We postulate the existence of a type  $\mathbb{R}$  for real numbers.

$$\mathbb{R}: \mathbb{P} \mathbb{A}$$
  $\mathbb{Z} \subset \mathbb{R}$ 

By introducing  $\mathbb{R}$  as a subset of  $\mathbb{A}$  (arithmos), we can immediately reuse all arithmetic and relational operators on numbers. Formally, we have to elaborate the semantics of those operators for elements of  $\mathbb{R}$ . Here, however, we content ourselves that this can be done in principle, rather than providing an axiomatisation of the reals. Such an axiomatisation has been developed in  $\mathbb{Z}$ , for instance, in [1] and is illustrated by the ProofPower-Z theorem prover [7]. Real numbers are require in the sequel to define the vector schema type used to characterise positions and motions of aircrafts in 3-dimensional space.

#### 2.3 Vectors

Vectors are used to represent the positions and motions of an aircraft.

```
Vector \_\_\_\_
x: \mathbb{R}
y: \mathbb{R}
z: \mathbb{R}
```

We characterise vectors by a schema binding (record) with three real components, x, y and z.

**Construction** The following function constructs a vector from scratch.

$$\frac{MkVector : \mathbb{R} \times \mathbb{R} \times \mathbb{R} \to Vector}{\forall x, y, z : \mathbb{R} \bullet MkVector(x, y, z) = \langle x == x, y == y, z == z \rangle}$$

**Zero and unit vector** These are defined by explicitly giving the underlying coordinates.

$$Zero V == MkVector(0,0,0)$$
  
 $Unit V == MkVector(1,1,1)$ 

Addition and subtraction Addition and subtraction are defined component-wise.

function 30 leftassoc ( $\_+_V\_$ )

function 30 leftassoc  $(_- -_V _-)$ 

Scalar product The scalar product multiplies a vector with a real number.

**Dot product** The dot (or inner) product multiplies two vectors.

function 50 leftassoc ( $\_\cdot V\_$ )  $\begin{array}{c|c}
& -\cdot V =: Vector \times Vector \rightarrow \mathbb{R} \\
\hline
& \forall v_1, v_2 : Vector \bullet v_1 \cdot_V v_2 = (v_1.x * v_2.x) + (v_1.y * v_2.y) + (v_1.z * v_2.z)
\end{array}$ 

We subsequently use the dot product to introduce the length of a vector.

Square of a vector The square multiplies a vector with itself.

**Length of a vector** We use the common definition  $|v| = \sqrt{v^2}$ .

$$\begin{split} & \mathbf{function} \; (|\; \_|\;) \\ & \underline{ \quad |\; \_| : \mathit{Vector} \to \mathbb{R} } \\ & \overline{ \quad \forall \, v : \mathit{Vector} \; \bullet \; |\; v \; | * |\; v \; | = v^2 } \end{split}$$

## 2.4 Trajectories

Trajectories are modelled by a pair consisting of a position and a motion vector.

```
Trajectory == Vector \times Vector
```

The points on a trajectory t are given by the formula  $t.1 + v \times v t.2$  where x ranges over the interval [0, 1].

**Distance of Trajectories** The notion of distance between trajectories is introduced below.

```
distance: Trajectory \times Trajectory \rightarrow \mathbb{R}
\forall t_1, t_2: Trajectory \bullet distance(t_1, t_2) =
\begin{pmatrix} \mu \ d : \mathbb{R} \mid \\ \exists \ x : \mathbb{R} \mid 0 \le x \le 1 \bullet d = |(t_2.1 +_V \ x *_V \ t_2.2) -_V \ (t_1.1 +_V \ x *_V \ t_1.2)| \land \\ \forall \ x : \mathbb{R} \mid 0 \le x \le 1 \bullet d \le |(t_2.1 +_V \ x *_V \ t_2.2) -_V \ (t_1.1 +_V \ x *_V \ t_1.2)| \end{pmatrix}
```

We note that this is not the minimal distance between 'any' two points on each trajectory. The  $\mathsf{CD}_x$  makes the simplifying assumption that aircrafts move at constant speed, so the result is an approximation of the actual minimal distance. In terms of the computation, we calculate the smallest distance between two points that simultaneously traverse each trajectory.

**Collisions** The threshold distance between two trajectories to flag a collision is given by the constant below. We leave its precise value implicit.

```
THRESHOLD: \mathbb{R}
0 \le THRESHOLD
```

The relation below determines whether two trajectories collide.

#### 2.5 Miscellaneous

The following operation calculates the sum of all elements in a sequence.

```
\frac{\Sigma : (\operatorname{seq} \mathbb{R}) \to \mathbb{R}}{\forall s : \operatorname{seq} \mathbb{R} \bullet \Sigma s = \text{if } s = \langle \rangle \text{ then } 0 \text{ else } head(s) + \Sigma (tail(s))}
```

## 3 Anchor A

In this section, we present the abstract model of the parallel  $CD_x$ .

#### 3.1 Aircrafts

The type Aircraft represents aircrafts as they may enter the radar.

$$Aircraft == seq_1 byte$$

We identify aircrafts by their call sign which consists of a non-empty sequence of bytes.

### 3.2 Frames

A frame records the positions all of aircrafts in a radar frame. It is modelled by virtue of a (finite) partial function that maps aircrafts to vectors representing their positions in airspace. The number of aircrafts in a radar frame is restricted by a constant  $MAX\_AIRCRAFTS$ .

```
 | MAX\_AIRCRAFT : \mathbb{N}_1 
Frame == \{f : Aircraft \Rightarrow Vector \mid \#f \leq MAX\_AIRCRAFT\}
```

The domain of a function implicitly determines the aircrafts that are currently in view of the radar.

#### 3.3 Collision Sets

We introduce a utility function that calculates the collision set for a frame of aircraft positions and motions. This is the set of all colliding aircrafts.

```
CalcCollisionSet: (Frame \times Frame) \rightarrow \mathbb{F} (Aircraft \times Aircraft)
\forall posns, motions: Frame \mid \text{dom } posns = \text{dom } motions \bullet
CalcCollisionSet(posns, motions) =
\left\{ a_1, a_2: Aircraft \mid a_1 \in \text{dom } posns \wedge a_2 \in \text{dom } posns \wedge \\ collide((posns \ a_1, motions \ a_1), (posns \ a_2, motions \ a_2)) \right\}
```

By definition, this set is symmetric:  $(a_1, a_2) \in colldide(t_1, t_2) \Leftrightarrow (a_2, a_1) \in colldide(t_1, t_2)$ .

#### 3.4 Channels

We require two channels for external interactions: one channel next\_frame to input the next radar frame and another channel output\_collisions to output the number of collisions at the end of the cycle.

```
channel next\_frame : Frame

channel output\_collisions : \mathbb{N}
```

We note that the value communicated by  $output\_collisions$  is an upper bound for the exact number of collisions modulo symmetry. (Symmetry means  $(a_1, a_2)$  and  $(a_2, a_1)$  are not viewed as separate collisions.) This is due to efficiency and the voxel algorithm we use. That is, voxel hashing in certain cases may record colliding aircraft pairs in more than one voxel, namely if both aircrafts are close to the boundary between those voxels. The original  $\mathsf{CD}_x$  manually removes such duplicates but we decided not to do so in our parallel implementation to simplify the program (another handler would be required for the removal step).

#### 3.5 Constants

The following three constants specify the duration of a detection cycle  $(FRAME\_PERIOD)$  as well as deadlines for the input  $(INT\_DL)$  and output  $(OUT\_DL)$  communications.

```
FRAME\_PERIOD: TIME \\ INP\_DL: TIME \\ OUT\_DL: TIME \\ \hline INP\_DL + OUT\_DL \leq FRAME\_PERIOD
```

We leave the precise values of the constants implicit. The paper [8] specifies them in terms of frames per seconds rather than periods, but this is just a technicality as we can convert between these measurements.

## 3.6 System

Below is the process for the behavioural requirements of the parallel  $\mathsf{CD}_x$ .

```
process ABReqsCDx \stackrel{\frown}{=} \mathbf{begin}
   state AStateCDx
    posns: Frame
    motions: Frame \\
    dom\ posns = dom\ motions
   Init.
    AStateCDx'
    posns' = \emptyset \land motions' = \emptyset
   RecordFrame.
    \Delta AStateCDx
    frame?: Frame
    posns' = frame?
    motions' = \{a : \text{dom } posns' \bullet a \mapsto \text{if } a \in \text{dom } posns \text{ then } (posns' a) -_V (posns a) \text{ else } ZeroV\}
   {\it Calc Collisions}
    \Xi A State CDx
    colls! : \mathbb{N}
    \exists collset : \mathbb{F}(Aircraft \times Aircraft) \mid collset = CalcCollisionSet(posns, motions) \bullet
         (\# collset = 0 \land colls! = 0) \lor (\# collset > 0 \land colls! \ge (\# collset) \text{ div } 2)
BReq1 \stackrel{\frown}{=} next\_frame? frame \longrightarrow
       RecordFrame;
     \bigvee var colls: \mathbb{N} \bullet CalcCollisions; output\_collisions! colls <math>\longrightarrow BReq1,
• Init; BReq1
end
```

The process for the timing requirements of the parallel  $\mathsf{CD}_x$  is as follows.

```
process ATReqsCDx \cong \mathbf{begin}

TReq1 \cong (TReqCycle \blacktriangleright FRAME\_PERIOD \parallel \mathbf{wait}\ FRAME\_PERIOD); TReq1

TReqCycle \cong

\begin{pmatrix} (next\_frame?frame@t \longrightarrow \\ \mathbf{wait}\ 0..(FRAME\_PERIOD - t - OUT\_DL) \end{pmatrix} \blacktriangleleft INP\_DL; \\ (output\_collisions?c \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL \end{pmatrix}

• TReq1

end
```

The requirements of the abstract system are specified by the **system** process below.

```
system CDx \triangleq ABRegsCDx [ {| next\_frame, output\_collisions } |] ATRegsCDx
```

We have one behavioural requirement (BReq1) and one timing requirement (TReq1). The behavioural

requirement specifies the result of the collision detection. It is defined by a recursion that inputs the next frame via a synchronisation on next\_frame, updates the process state while calculating the new aircraft motions (RecordFrame), computes the collisions and deposits them in the local variable colls (CalcCollisions), and outputs them on the channel output\_collisions. Any number greater than the precise number of collisions may be output unless there are no collisions (in that case 0 must be output). Since the collision set is symmetric, the actual number of collisions is obtained by dividing the cardinality of the set by 2. The calculation of the collision set makes use of the CalcCollisionSet function defined earlier on in Section 3.2.

The time-wise behaviour in each cycle is captured by the local action TReqCycle. It is used in defining the overall timing requirement TReq1. Its specification states that TReqCycle is executed once in each cycle and has to terminate within the period  $(... \triangleright FRAME\_PERIOD)$ . The interleaving with wait  $FRAME\_PERIOD$  ensures that moreover we do not terminate before the period expires.

The specification of TReqCycle imposes a deadline, too,  $(INP\_DL)$ , namely on the input communication on  $next\_frame$ . This is an imposition on the environment to make the next frame available within a certain period of time once the program is ready to accept it. We record in t the time it took to communicate the next frame from when the communication was first offered. The subsequent nondeterministic wait provides freedom to the implementation to use up to  $FRAME\_PERIOD - t - OUT\_DL$  units of time to calculate the collisions and then output the result on the  $output\_collisions$  channel. The environment has to accept the output within  $OUT\_DL$  time units from when it is offered by the program. This can prevent a situation in which the environment delays the communication on  $output\_collisions$  right to the end of the cycle, and the infrastructure may thus not have time to initiate the next cycle.

We note that using wait  $FRAME\_PERIOD - t - OUT\_DL$  rather than wait  $FRAME\_PERIOD - t$  is a modelling decision to ensure that the environment is potentially given not less that  $OUT\_DL$  time units to accept the output, and that an implementation cannot restrict this allowance.

The CDx system process yields the specification of the entire system. This is a parallelism between the two requirement processes ABReqsCDx and ATReqsCDx. The processes synchronise on both external channels  $next\_frame$  and  $output\_collisions$ .

Field	Type	Location	Access Mode	Memory Area
simulator	Simulator	CDxMission	shared	immortal
frameBuffer	FrameBuffer	Simulation	shared	immortal
currentFrame	RawFrame	CDxMission	shared	mission
state	StateTable	CDxMission	shared	mission
voxel_map	HashMap	ReducerHandler	local	per release
work	Partition	CDxMission	shared	mission
collisions	int	CDxMission	shared	mission
control	DetectorControl	CDxMission	shared	mission
factories	PersistentData	ReducerHandler	local	mission

Table 1: Analysis of relevant shared and local data in the parallel  $\mathsf{CD}_x$ .

## 4 Anchor O

In this section, we discuss the construction of the O anchor. This is done in three refinement phases as explained in [6]. They are namely CS (concrete state), SD (shared data) and EL (elimination). We first examine the shared data in the  $CD_x$  program and then proceed with the refinement model(s) for each phase.

#### 4.1 Shared Data

To guide the data refinements in this step, Table 1 summarises the relevant shared and local fields of the parallel  $CD_x$  SCJ program. All data resides in either mission or immortal memory except for  $voxel_map$ , which is local to ReducerHandler; we have to consider  $voxel_map$  in the O anchor data refinements though because other shared data depend on its presence in the model; this is in particular to formulate a suitable retrieve relation for  $voxel_map$ .

Below we give a brief explanation of the purpose of each variable.

- The simulator and frameBuffer objects are part of the simulation. The FrameBuffer class provides the mechanism for reading the next radar frame from the hardware. In the original  $CD_x$ , frameBuffer was located in a class ImmortalEntry whose name we changed to Simulator. The frameBuffer object is not directly represented as part of the model since we abstract from the details of the mechanism that interact with the hardware, as well as the storage to buffer device data for radar frames.
- The currentFrame variable corresponds to the *posns* component of the abstract model. Its type RawFrame records this data by virtue of various arrays of primitive types: int[], byte[] and float[]. These hold the call signs and positions of the aircrafts.
- The state variable of type StateTable holds the previous positions of aircrafts. It thus does not encode the *motions* vectors directly, but we can construct them from the content of currentFrame and state. Unlike RawFrame, StateTable records the positions by way of a (customised) CHashMap. It also manages the allocation of Vector3d objects for positions as to avoid memory leaks.
- The voxel\_map field is local to ReducerHandler and thus not shared. It records the result of the voxel hashing operation and is needed to specify essential properties of the algorithm as well as the retrieve relation for work (which is shared). It thus becomes relevant to the O anchor.
- The work variable of type Partition is used to divide and record the computational work assigned to each detection handler. The Partition class provides some methods that facilitate this. This object is shared between the DetectorHandler classes as well as ReducerHandler who initialises it.

- The collisions variable accumulates the number of collisions detected by the parallel detection handlers. It is concurrently accessed by them via synchronised methods to avoid data races.
- The control variable holds an object of type DetectorControl which is used to orchestrate the execution of the detector handlers and the output handler.
- The factories variable resides in mission memory despite being local. It is an artifacts of our program design to pre-allocate shared objects. This is important to avoid dynamic allocations in mission memory while the mission executes. We ignore it in the O anchor as it is introduced during algorithmic refinement.

The above analysis yields the following correspondence between abstract model variables and concrete program variables. The *posns* and *motions* state components of *ABReqsCDx* are exactly represented by the currentFrame and state variables in the program (CS phase). The other variables refer to shared and local data that has to be introduced as part of the SD phase of the O anchor, apart from the simulation-related classes and factories. The control object is also not considered in the O anchor as it will be introduced later on in the design, namely in the E anchor when refining the control behaviour.

We have omitted the shared objects for SCJ events in Table 1; this is because they are not considered as data objects. We next examine the refinements that introduce the aforementioned class objects for shared data into the model. For the CS and EL phases, we have a single (data) refinement, whereas for the SD phase we carry out the refinement in two incremental steps. Apart from the finalising EL model, none of the models discard existing state components but merely extend the state of the previous process. To disambiguate the names of state components inside the retrieve relations, we use subscripts.

## 4.2 Phase CS

In the first phase of the O anchor, we data-refine the abstract model variables *posns* and *motions* into their concrete representations in the program. This is via the shared objects *currentFrame* and *state* of class type *RawFrame* and *StateTable*. They are used to record the current and previous positions of aircrafts.

#### Refining State

The state of the refining process is given by the following state schema.

```
OCSStateCDx posns_1: Frame motions_1: Frame currentFrame_1: RawFrame state_1: StateTable
```

The class types *RawFrame* and *StateTable* are specified in Appendix A.1 and A.2. We note that we retain the abstract state components as auxiliary variables following Morgan's approach [9].

#### Retrieve Relation

The retrieve relation is given by the following schema that associates abstract and concrete states.

```
OCSRetrCDx \\ OCSStateCDx \\ OCSStateCDx \\ posns_1 = posns \land motions_1 = motions \\ currentFrame_1 \neq \mathbf{null} \land state_1 \neq \mathbf{null} \\ \begin{cases} \lambda \ a : Aircraft \mid currentFrame_1 . find(a) \neq -1 \bullet \\ \left( \begin{array}{c} \mathbf{let} \ i == currentFrame_1 . find(a) \bullet \\ currentFrame_1 . positions . getA(3*i), \\ currentFrame_1 . positions . getA(3*i+1), \\ currentFrame_1 . positions . getA(3*i+2) \end{array} \right) \\ motions = \begin{pmatrix} \lambda \ a : Aircraft \mid currentFrame_1 . find(a) \neq -1 \bullet \\ \left( \begin{array}{c} \mathbf{let} \ prev == state_1 . position\_map . get(MkCallSign(a)) \bullet \\ \mathbf{if} \ prev \neq \mathbf{null} \\ \mathbf{then} \ posns(a) -_V \ MkVector(prev . x, prev . y, prev . z) \\ \mathbf{else} \ Zero V \end{pmatrix} \end{cases}
```

For reasons of definedness, we require  $currentFrame_1$  and  $state_1$  not to be **null**. We observe that the abstract state is expressed as a function of the concrete state. This enables a calculations approach to derive the refining data operations. Here, we do not fully simplify the calculated refining operations though.

#### **Refining Process**

The process for the first data refinement of the Anchor O is given below. We target the refinement of ABReqsCDx, that is the abstract behavioural requirements. The new state invariant and refined data operations have been calculated; as mentioned before, they are not fully simplified yet.

```
process OCSBReqsCDx \stackrel{\frown}{=} \mathbf{begin}
```

```
state OCSStateCDx_
 posns_1 : Frame
 motions_1 : Frame
 currentFrame_1: RawFrame
 state_1: State Table
 dom posns_1 = dom motions_1
 currentFrame_1 \neq \mathbf{null} \land state_1 \neq \mathbf{null}
                         \lambda a : Aircraft \mid currentFrame_1 . find(a) \neq -1 \bullet
posns_{1} = \begin{pmatrix} \text{let } i == currentFrame_{1} \cdot find(a) \bullet \\ MkVector \begin{pmatrix} currentFrame_{1} \cdot positions \cdot getA(3*i), \\ currentFrame_{1} \cdot positions \cdot getA(3*i+1), \\ currentFrame_{1} \cdot positions \cdot getA(3*i+2) \end{pmatrix}
                              \lambda a : Aircraft \mid currentFrame_1 . find(a) \neq -1 \bullet
                                    (let prev == state_1 \cdot position\_map \cdot get(MkCallSign(a)) \bullet 
                                                if prev \neq \mathbf{null}

then posns_1(a) -_V MkVector(prev.x, prev.y, prev.z)
Init_
 OCSStateCDx'
 posns'_1 = \emptyset \land motions'_1 = \emptyset
 currentFrame'_1 = \mathbf{new} \ RawFrame \land state'_1 = \mathbf{new} \ State Table
RecordFrame
 \Delta OCSStateCDx
 frame?: Frame
 posns'_1 = frame?
 motions'_1 = \{a : \text{dom } posns'_1 \bullet a \mapsto \text{if } a \in \text{dom } posns_1 \text{ then } (posns'_1 a) -_V (posns_1 a) \text{ else } ZeroV\}
                          \lambda \ a : Aircraft \mid currentFrame_1 . find(a) \neq -1 \bullet
posns_1 = \begin{pmatrix} \text{let } i == currentFrame_1 \cdot find(a) \bullet \\ MkVector \begin{pmatrix} currentFrame_1 \cdot positions \cdot getA(3*i), \\ currentFrame_1 \cdot positions \cdot getA(3*i+1), \\ currentFrame_1 \cdot positions \cdot getA(3*i+2) \end{pmatrix}
                       \bigwedge \lambda \ a : Aircraft \mid currentFrame'_1 . find(a) \neq -1 \bullet
                          \begin{pmatrix} \mathbf{let} \ i == \ currentFrame_1' \ . \ find(a) \bullet \\ MkVector \begin{pmatrix} currentFrame_1' \ . \ positions \ . \ getA(3*i), \\ currentFrame_1' \ . \ positions \ . \ getA(3*i+1), \\ currentFrame_1' \ . \ positions \ . \ getA(3*i+2) \end{pmatrix} 
                           \lambda a : Aircraft \mid currentFrame'_1 . find(a) \neq -1 \bullet
                                     \begin{cases} \textbf{let } \textit{prev} == \textit{state}_1' . \textit{position\_map} . \textit{get}(\textit{MkCallSign}(a)) \bullet \\ \textbf{if } \textit{prev} \neq \textbf{null} \\ \textbf{then } \textit{posns}_1'(a) -_V \textit{MkVector}(\textit{prev} . x, \textit{prev} . y, \textit{prev} . z) \end{cases}
```

The local action *BReq1* and the main action remain exactly as in *ABReqsCDx*. Simulation laws in *Circus* [4] establish that this yields a valid process refinement of *ABReqsCDx*. A detailed proof of this is omitted but not difficult. Regarding the data operations, we expect that further refinement later on in the AR phase of the E anchor transforms them into executable code, so simplification of the refined data operations can (an probably should) be postponed in this anchor. Automatic tools can in principle assist the simplification and enable the developer to take full advantage of the calculational approach. This single refinement concludes the CS phase. We next turn to the SD phase where shared data is introduced.

## 4.3 Phase SD

end

This phase is divided into two incremental refinements to leverage the proof effort.

- The first refinement introduces the shared variable *collisions* whose purpose is to hold the detected number of collisions after each detection cycle.
- The second refinement introduces the shared variable work, concurrently with the local variable voxel\_map. Whereas the work component divides the computational work, voxel\_map determines the result of the voxel hashing operation and is required to specify the retrieve relation for work.

We present the process model for each refinement step.

#### Refinement 1

In the first refinement of the SD phase, we introduce the shared *collisions* variable. It holds the result of the collision detection at the end of each cycle.

#### Refining State

The state of the refining process is given by the following state schema.

```
OSD1StateCDx \_
posns_2: Frame
motions_2: Frame
currentFrame_2: RawFrame
state_2: StateTable
collisions_2: int
```

Again, all state components of the previous model are retained. Subscripts are used for disambiguation.

#### **Retrieve Relation**

The retrieve relation of the first refinement of SD is specified below.

```
OSD1RetrCDx \\ OCSStateCDx \\ OSD1StateCDx \\ \hline posns_2 = posns_1 \land motions_2 = motions_1 \\ currentFrame_2 = currentFrame_1 \land state_2 = state_1 \\ \exists \ collset : \mathbb{F} \left( Aircraft \times Aircraft \right) \mid collset = CalcCollisionSet(posns_2, motions_2) \bullet \\ \left( \# \ collset = 0 \land collisions_2 = 0 \right) \lor \left( \# \ collset > 0 \land collisions_2 \ge \left( \# \ collset \right) \text{ div } 2 \right)
```

We constrain the value of  $collisions_2$  similar to colls! in the CalcCollisions action. This reflects the intention of refining CalcCollisions by an assignment  $colls := collisions_2$ .

#### Refining Process

The process for the first refinement of the SD phase is presented in the sequel.

```
process OSD1BReqsCDx = begin
```

```
state \ OSD1StateCDx \\ posns_2 : Frame \\ motions_2 : Frame \\ currentFrame_2 : RawFrame \\ state_2 : StateTable \\ collisions_2 : int \\ \\ dom \ posns_2 = dom \ motions_2 \\ currentFrame_2 \neq \mathbf{null} \land state_2 \neq \mathbf{null} \\ \\ posns_2 = \begin{pmatrix} \lambda \ a : Aircraft \mid currentFrame_2 \cdot find(a) \neq -1 \bullet \\ \\ (let \ i == currentFrame_2 \cdot find(a) \bullet \\ \\ MkVector \begin{pmatrix} currentFrame_2 \cdot positions \cdot getA(3*i), \\ currentFrame_2 \cdot positions \cdot getA(3*i+1), \\ currentFrame_2 \cdot positions \cdot getA(3*i+2) \end{pmatrix} \end{pmatrix} \\ \\ motions_2 = \begin{pmatrix} \lambda \ a : Aircraft \mid currentFrame_2 \cdot find(a) \neq -1 \bullet \\ \\ (let \ prev == state_2 \cdot position\_map \cdot get(MkCallSign(a)) \bullet \\ \\ if \ prev \neq \mathbf{null} \\ \\ then \ posns_2(a) - V \ MkVector(prev \cdot x, prev \cdot y, prev \cdot z) \\ \\ else \ ZeroV \end{pmatrix} \\ \\ \exists \ collset : \mathbb{F} \ (Aircraft \times Aircraft) \mid collset = CalcCollisionSet(posns_2, motions_2) \bullet \\ \\ (\# \ collset = 0 \land \ collisions_2 = 0) \lor (\# \ collset > 0 \land collisions_2 \ge (\# \ collset) \ div \ 2)
```

```
Init_
         OSD1StateCDx'
        posns_2' = \emptyset \land motions_2' = \emptyset

currentFrame_2' = \mathbf{new} \ RawFrame \land state_2' = \mathbf{new} \ StateTable
         collisions_2' = 0
       RecordFrame
         \Delta OSD1StateCDx
        frame?: Frame
        posns_2' = \mathit{frame}?
        motions'_2 = \{a : \text{dom } posns'_2 \bullet a \mapsto \text{if } a \in \text{dom } posns_2 \text{ then } (posns'_2 a) -_V (posns_2 a) \text{ else } ZeroV\}
       posns_{2} = \begin{pmatrix} \lambda \ a : Aircraft \mid currentFrame_{2} . find(a) \neq -1 \bullet \\ \begin{cases} \text{let } i == currentFrame_{2} . find(a) \bullet \\ \\ MkVector \begin{pmatrix} currentFrame_{2} . positions . getA(3*i), \\ currentFrame_{2} . positions . getA(3*i+1), \\ currentFrame_{2} . positions . getA(3*i+2) \end{pmatrix} \end{pmatrix}
     posns_{2}' = \begin{pmatrix} \lambda \ a : Aircraft \mid currentFrame_{2}' . \ find(a) \neq -1 \bullet \\ \\ \left( \begin{array}{c} \text{let } i == \ currentFrame_{2}' . \ find(a) \bullet \\ \\ MkVector \left( \begin{array}{c} currentFrame_{2}' . \ find(a) \bullet \\ \\ currentFrame_{2}' . \ positions . \ getA(3*i), \\ \\ currentFrame_{2}' . \ positions . \ getA(3*i+1), \\ \\ currentFrame_{2}' . \ positions . \ getA(3*i+2) \end{pmatrix} \right) \\ motions_{2}' = \begin{pmatrix} \lambda \ a : Aircraft \mid currentFrame_{2}' . \ find(a) \neq -1 \bullet \\ \\ \left( \begin{array}{c} \text{let } prev == \ state_{2}' . \ position\_map . \ get(MkCallSign(a)) \bullet \\ \\ \text{if } prev \neq \textbf{null} \\ \\ \text{then } posns_{2}'(a) -_{V} \ MkVector(prev . \ x, \ prev . \ y, \ prev . \ z) \\ \\ \text{else } ZeroV \end{pmatrix} \right) \\ \hline \\ \end{array}
        \exists collset : \mathbb{F}(Aircraft \times Aircraft) \mid collset = CalcCollisionSet(posns'_2, motions'_2) \bullet
                  (\# collset = 0 \land collisions_2' = 0) \lor (\# collset > 0 \land collisions_2' \ge (\# collset) \text{ div } 2)
       Calc Collisions
        \Xi OSD1StateCDx
         colls! : \mathbb{N}
         colls! = collisions_2
BReq1 \stackrel{\frown}{=} next\_frame? frame \longrightarrow
            \begin{pmatrix} RecordFrame; \\ \mathbf{var}\ colls: \mathbb{N} \bullet CalcCollisions; \ output\_collisions! \ colls \longrightarrow BReq1 \end{pmatrix} 
• Init; BReq1
```

As a result of this first refinement stage of SD, *RecordFrame* in the refined process not only records the radar frame and updates the previous aircraft positions but also calculates the collisions. Contrary, the *CalcCollisions* operation now simply returns the value of the shared variable *collisions*<sub>2</sub> rather than performing any calculation as it was the case before.

#### Refinement 2

The second refinement of SD introduces the shared variable work of type Partition. It is a shared object between the handlers and used to distribute the computational work determined by voxel\_map. We hence concurrently introduce the local variable voxel\_map, too. The latter records aircraft positions and motions in a HashMap object that maps Vector2d to List[Motion] objects. In the program, this corresponds to the voxel hashing performed by ReducerHandler of which voxel\_map is a local variable. The type Vector2d is used to index the voxel space. The Motion class records the call sign, current position, and previous position of an aircraft, and List models a standard (Java) list. The class specifications for Partition, Motion, Vector2d, HashMap, and List can all be found in the Appendix A.

#### Refining State

The state of the refining process is given by the following state schema.

Again, all state components of the previous model are retained. Subscripts are used for disambiguation. We note that  $voxel\_map$  has not been added as a state component.

#### Retrieve Relation

The retrieve relation of the second refinement relates work and voxel\_map.

```
OSD2RetrCDx \\ OSD2StateCDx \\ OSD2StateCDx \\ \\ posns_3 = posns_2 \land motions_3 = motions_2 \\ currentFrame_3 = currentFrame_2 \land state_3 = state_2 \\ work_3 \neq \mathbf{null} \\ \exists \ voxel\_map : HashMap[\ Vector2d, List[\ Motion]] \ | \ voxel\_map \neq \mathbf{null} \bullet \\ \begin{pmatrix} \forall \ a_1, \ a_2 : Aircraft \ | \ \{a_1, \ a_2\} \subseteq \operatorname{dom} \ posns_3 \bullet \\ (a_1, a_2) \in CalcCollisionSet(posns_3, motions_3) \Rightarrow \\ \begin{pmatrix} \exists \ l : List[\ Motion] \ | \ l \in voxel\_map \ . \ values() \ . \ elems() \bullet \\ MkMotion(a_1, posns_3 \ a_1 - v \ motions_3 \ a_1, posns_3 \ a_1) \in l . \ elems() \land \\ MkMotion(a_2, posns_3 \ a_2 - v \ motions_3 \ a_2, posns_3 \ a_2) \in l . \ elems() \end{pmatrix} \\ voxel\_map \ . \ values() \ . \ elems() = \bigcup \ \{i : 1 \ . \ 4 \bullet \ work_3 \ . \ getDetectorWork(i) \ . \ elems() \} \\ collisions_3 = collisions_2 \end{aligned}
```

As before,  $voxel\_map$  and work must not be **null** to avoid undefinedness issues. We observe  $voxel\_map$  has been introduced as a local variable rather than a state component of the process. Above we furthermore make use of an auxiliary function MkMotion, loosely specified below.

```
MkMotion: Aircraft \times Vector \times Vector \rightarrow Motion
```

It yields a *Motion* object for an *Aircraft* and its previous and current position. It corresponds to the constructor of the Motion class in the SCJ program of the parallel  $\mathsf{CD}_x$ . We recall that the logical method elems()

returns the elements of a List object as a set. The results of the method call  $work_3$ . getDetectorWork(i) determines the voxels to be checked by detector i; it is of type List[List[Motion]]. The method call  $voxel\_map.values()$  returns the list of values in the hash table (it is also of type List[List[Motion]]).

#### Refining Process

The process for the second refinement of the SD phase is presented below.

```
process OSD2BReqsCDx \stackrel{\frown}{=} \mathbf{begin}
```

```
state OSD2StateCDx
 posns_3: Frame
 motions_3: Frame
 currentFrame_3: RawFrame
 state_3: State Table
 work_3: Partition
 collisions_3:int
 dom posns_3 = dom motions_3
 currentFrame_3 \neq \mathbf{null} \land state_3 \neq \mathbf{null} \land work_3 \neq \mathbf{null}
 \exists voxel\_map : HashMap[Vector2d, List[Motion]] \mid voxel\_map \neq \mathbf{null} \bullet
                              \lambda : Aircraft \mid currentFrame_3 . find(a) \neq -1 \bullet
    posns_{3} = \begin{pmatrix} \text{let } i == currentFrame_{3} . find(a) \bullet \\ MkVector \begin{pmatrix} currentFrame_{3} . positions . getA(3*i), \\ currentFrame_{3} . positions . getA(3*i+1), \\ currentFrame_{3} . positions . getA(3*i+2) \end{pmatrix}
                                   \begin{pmatrix} \lambda \ a : Aircraft \mid currentFrame_3 \cdot find(a) \neq -1 \bullet \\ \left( \begin{array}{c} \mathbf{let} \ prev == \ state_3 \cdot position\_map \cdot get(MkCallSign(a)) \bullet \\ \mathbf{if} \ prev \neq \mathbf{null} \\ \mathbf{then} \ posns_3(a) -_V \ MkVector(prev \cdot x, prev \cdot y, prev \cdot z) \\ \mathbf{else} \ Zero \ V \end{pmatrix} 
           \forall a_1, a_2 : Aircraft \mid \{a_1, a_2\} \subseteq \text{dom } posns_3 \bullet
                 (a_{1}, a_{2}) \in CalcCollisionSet(posns_{3}, motions_{3}) \Rightarrow
\begin{pmatrix} \exists l : List[Motion] \mid l \in voxel\_map . values() . elems() \bullet \\ MkMotion(a_{1}, posns_{3} a_{1} - _{V} motions_{3} a_{1}, posns_{3} a_{1}) \in l . elems() \land \\ MkMotion(a_{2}, posns_{3} a_{2} - _{V} motions_{3} a_{2}, posns_{3} a_{2}) \in l . elems() \end{pmatrix}
      voxel\_map.values().elems() = \bigcup \{i: 1...4 \bullet work_3.getDetectorWork(i).elems()\}
      \exists \ collset : \mathbb{F}(Aircraft \times Aircraft) \mid collset = CalcCollisionSet(posns_3, motions_3) \bullet
             (\# collset = 0 \land collisions_3 = 0) \lor (\# collset > 0 \land collisions_3 \ge (\# collset) \text{ div } 2)
Init
```

```
RecordFrame
                 \Delta OSD2StateCDx
                frame?: Frame
                posns_3' = frame?
                 motions_3' = \{a : \text{dom } posns_3' \bullet a \mapsto \text{if } a \in \text{dom } posns_3 \text{ then } (posns_3' a) -_V (posns_3 a) \text{ else } ZeroV\}
                \exists voxel\_map : HashMap[Vector2d, List[Motion]] \mid voxel\_map \neq \mathbf{null} \bullet
                                                                                \lambda a : Aircraft \mid currentFrame_3 \cdot find(a) \neq -1 \bullet
                                    osns_{3} = \begin{pmatrix} \text{let } i == currentFrame_{3} \cdot find(a) \bullet \\ MkVector \begin{pmatrix} currentFrame_{3} \cdot positions \cdot getA(3*i), \\ currentFrame_{3} \cdot positions \cdot getA(3*i+1), \\ currentFrame_{3} \cdot positions \cdot getA(3*i+2) \end{pmatrix}
                                                                                 \lambda a : Aircraft \mid currentFrame'_3 . find(a) \neq -1 \bullet
                                                                                    \begin{pmatrix} \mathbf{let} \ i == \ currentFrame_3' \ . \ find(a) \bullet \\ MkVector \begin{pmatrix} currentFrame_3' \ . \ positions \ . \ getA(3*i), \\ currentFrame_3' \ . \ positions \ . \ getA(3*i+1), \\ currentFrame_3' \ . \ positions \ . \ getA(3*i+2) \end{pmatrix} \end{pmatrix}
                                    (carrent raine_3 \cdot positions \cdot getr(o + t + 2))))
notions_3' = \begin{pmatrix} \lambda \ a : Aircraft \mid current Frame_3' \cdot find(a) \neq -1 \bullet \\ \text{ } \\ \text{ }
                            voxel\_map.values().elems() = \bigcup \{i: 1...4 \bullet work'_3.getDetectorWork(i).elems()\}
                           \exists collset : \mathbb{F}(Aircraft \times Aircraft) \mid collset = CalcCollisionSet(posns'_3, motions'_3) \bullet
                                             (\# \mathit{collset} = 0 \land \mathit{collisions}_3' = 0) \lor (\# \mathit{collset} > 0 \land \mathit{collisions}_3' \ge (\# \mathit{collset}) \ \mathrm{div} \ 2)
             Calc Collisions
                \Xi OSD2StateCDx
                colls! : \mathbb{N}
                colls! = collisions_3
BReq1 \stackrel{\frown}{=} next\_frame? frame \longrightarrow
                       (RecordFrame;
                          \mathbf{var}\ colls: \mathbb{N} \bullet CalcCollisions; output\_collisions! colls \longrightarrow BReq1
\bullet Init; BReq1
end
```

With the above refinement we have introduced all shared data. In addition to recording the frame, updating the motions and calculation collisions, *RecordFrame* here also constructs the voxel map and carries out the calculation for dividing the computational work between the detector handlers by setting *work*.

This concludes the models for SD and in order to finalise the O anchor, the subsequent and last phase eliminates the auxiliary model variables  $posns_3$  and  $motions_3$ .

## 4.4 Phase EL

 $F: RawFrame \rightarrow Frame$ 

In the EL phase we remove the model variables  $posns_3$  and  $motions_3$  which so far have been kept as auxiliary variables to facilitate the formulation of retrieve relations and operation refinements. This is achieved by turning them into local constants within the state schema and data operations.

Not to clutter up the models, we introduce the following global functions F and G.

They calculate the abstract state components *posns* and *motions* from the concrete state components *currentFrame* and *state*. The functions are introduced solely to simplify the presentation of the models.

#### Refining Process

The process for the result of the EL stage is presented below.

```
process OBReqsCDx \stackrel{\frown}{=} \mathbf{begin}
```

```
Init.
           OStateCDx'
           currentFrame' = \mathbf{new} \, RawFrame
           state' = \mathbf{new} \, State \, Table
           work' = \mathbf{new} \, Partition(4)
           collisions' = 0
         RecordFrame
           \Delta OStateCDx
           frame?: Frame
           \exists posns, motions : Frame; posns', motions' : Frame
                       dom\ posns = dom\ motions \land dom\ posns' = dom\ motions' \bullet
           \exists voxel\_map : HashMap[Vector2d, List[Motion]] \mid voxel\_map \neq \mathbf{null} \bullet
                   posns = F(currentFrame) \land motions = G(currentFrame, state) \land
                   posns' = F(currentFrame') \land motions' = G(currentFrame', state') \land
                   posns' = frame? \land
                   motions' =
                               \{a: \operatorname{dom} posns' \bullet a \mapsto \operatorname{if} a \in \operatorname{dom} posns \operatorname{then} (posns' a) -_V (posns a) \operatorname{else} ZeroV\} \land A
                           \forall a_1, a_2 : Aircraft \mid \{a_1, a_2\} \subseteq \text{dom } posns' \bullet
                                      (a_1, a_2) \in CalcCollisionSet(posns', motions') \Rightarrow
                                                       \begin{cases} \exists \ l : List[Motion] \mid l \in voxel\_map \ . \ values() \ . \ elems() \bullet \\ MkMotion(a_1, posns' \ a_1 -_V \ motions' \ a_1, posns' \ a_1) \in l \ . \ elems() \land \\ MkMotion(a_2, posns' \ a_2 -_V \ motions' \ a_2, posns' \ a_2) \in l \ . \ elems() \end{cases} 
                   voxel\_map.values().elems() = \bigcup \{i: 1...4 \bullet work'.getDetectorWork(i).elems()\} \land voxel\_map.values().elems() = \bigcup \{i: 1...4 \bullet work'.getDetectorWork(i).elems()\} \land voxel\_map.values() = \bigcup \{i: 1...4 \bullet work'.getDetectorWork(i).elems() = \bigcup
                   \exists \ collset : \mathbb{F}(Aircraft \times Aircraft) \mid collset = CalcCollisionSet(posns', motions') \bullet
                               (\# collset = 0 \land collisions' = 0) \lor (\# collset > 0 \land collisions' \ge (\# collset) \operatorname{div} 2)
         Calc Collisions
           \Xi OStateCDx
           colls! : \mathbb{N}
           colls! = collisions
BReg1 = next\_frame? frame \longrightarrow
                  RecordFrame;
                 \mathbf{var}\ colls: \mathbb{N} \bullet CalcCollisions; \ output\_collisions! colls \longrightarrow BReq1
• Init; BReq1
end
```

With this process, we can now specify the top-level O anchor process.

```
system OCDx \triangleq OBReqsCDx \llbracket \{ | next\_frame, output\_collisions \} \rrbracket ATReqsCDx
```

Because the data refinements only affect the process for the behavioural requirements, the process for the timing requirements is the same as in the a abstract model. The simplification, decomposition and algorithmic refinement of the data operations *RecordFrame* is still due. This is an issue for the subsequent E anchor, which we discussed in detail in the next section.

## 5 Anchor E

In this section, we present the models for the E anchor. This anchor consists of the following five phases.

- 1. CP (collapse parallelism)
- 2. MS (mission architecture)
- 3. HS (handler architecture)
- 4. SH (encapsulate shared data)
- 5. AR (algorithmic refinement)

In comparison to the serial line example in [6], we subdivided the MH phase (missions and handlers) into two separate phase, one for the missions (MS) and one for the handlers (HS). Moreover the order of the SH phase and former MH phase has been reversed. The reason for this is that the latter determines the design that gives rise to atomic data operations emerging during SH. In the  $CD_x$ , this is, in particular, access to the shared *collisions* variable and also the refinement of a barrier mechanism that emerges during Stage 5 of MH. Clearly, we cannot refine those atomic operations before the design is in place, and for this reason SH has to be postponed until the missions and handlers design has fully emerged.

The MS, HS and SH phases are additionally subdivided into a number of logical stages which are explained as we go along. This revealed a set of refinement patterns which, along with the respective laws, are presented in detail too; a cumulative list of all elementary laws and high-level laws is included in Appendix B. The E anchor is overall the most challenging and interesting of the models and at the heart of the refinement approach; it also posses a few significant challenges for automation.

## 5.1 Phase CP

The process after performing the collapsing of parallelism is given below.

```
system ECPCDx \stackrel{\frown}{=} begin
```

```
 \begin{array}{c} \textbf{state} \ ECPStateCDx \\ currentFrame : RawFrame \\ state : StateTable \\ work : Partition \\ collisions : int \\ \hline \\ currentFrame \neq \textbf{null} \land state \neq \textbf{null} \land work \neq \textbf{null} \\ \exists \ voxel\_map : HashMap[Vector2d, List[Motion]] \ | \ voxel\_map \neq \textbf{null} \bullet \\ \exists \ posns : Frame; \ motions : Frame \ | \ dom \ posns = \ dom \ motions \bullet \\ \hline \\ \left( \begin{array}{c} posns = F(currentFrame) \land motions = G(currentFrame, state) \land \\ \hline \left( \begin{array}{c} \forall \ a_1, a_2 : Aircraft \ | \ \{a_1, a_2\} \subseteq \text{dom} \ posns \bullet \\ \hline \left( a_1, a_2 \right) \in CalcCollisionSet(posns, motions) \Rightarrow \\ \hline \left( \begin{array}{c} \exists \ l : List[Motion] \ | \ l \in voxel\_map . values() . \ elems() \land \\ \hline MkMotion(a_1, posns \ a_1 - _V \ motions \ a_2, posns \ a_2) \in l . \ elems() \\ \hline voxel\_map . values() . \ elems() = \bigcup \left\{ i : 1 ... \ 4 \bullet \ work . \ getDetectorWork(i) . \ elems() \right\} \land \\ \hline \exists \ collset : \mathbb{F} \left( Aircraft \times Aircraft) \ | \ collset = CalcCollisionSet(posns, motions) \bullet \\ \hline (\# \ collset = 0 \land \ collisions = 0) \lor (\# \ collset > 0 \land \ collisions \geq (\# \ collset) \ div \ 2) \\ \hline \end{array}
```

```
Init.
           ECPStateCDx'
           currentFrame' = \mathbf{new} \, RawFrame
          state' = \mathbf{new} \, State \, Table
          work' = \mathbf{new} \, Partition(4)
           collisions' = 0
         RecordFrame
           \Delta ECPStateCDx
          frame?: Frame
          \exists posns, motions : Frame; posns', motions' : Frame
                     dom\ posns = dom\ motions \land dom\ posns' = dom\ motions' \bullet
          \exists voxel\_map : HashMap[Vector2d, List[Motion]] \mid voxel\_map \neq \mathbf{null} \bullet
                  posns = F(currentFrame) \land motions = G(currentFrame, state) \land
                  posns' = F(currentFrame') \land motions' = G(currentFrame', state') \land
                  posns' = frame? \land
                  motions' =
                             \{a : \text{dom } posns' \bullet a \mapsto \text{if } a \in \text{dom } posns \text{ then } (posns' a) - V \ (posns a) \text{ else } ZeroV\} \land A
                         \forall a_1, a_2 : Aircraft \mid \{a_1, a_2\} \subseteq \text{dom } posns' \bullet
                                   (a_1, a_2) \in CalcCollisionSet(posns', motions') \Rightarrow
                                                  \begin{pmatrix} \exists \ l : List[Motion] \mid l \in voxel\_map . values() . elems() \bullet \\ MkMotion(a_1, posns' \ a_1 -_V motions' \ a_1, posns' \ a_1) \in l . elems() \land \\ MkMotion(a_2, posns' \ a_2 -_V motions' \ a_2, posns' \ a_2) \in l . elems() \end{pmatrix} 
                  voxel\_map.values().elems() = \bigcup \{i: 1...4 \bullet work'.getDetectorWork(i).elems()\} \land voxel\_map.values().elems() = \bigcup \{i: 1...4 \bullet work'.getDetectorWork(i).elems()\} \land voxel\_map.values() = \bigcup \{i: 1...4 \bullet work'.getDetectorWork(i).elems() = \bigcup
                  \exists \ collset : \mathbb{F}(Aircraft \times Aircraft) \mid collset = CalcCollisionSet(posns', motions') \bullet
                             (\# collset = 0 \land collisions' = 0) \lor (\# collset > 0 \land collisions' \ge (\# collset) \operatorname{div} 2)
         Calc Collisions
          \Xi ECPStateCDx
          colls! : \mathbb{N}
          colls! = collisions
StartCycle \ \widehat{=} \ (next\_frame ? frame \ 0 \ t_1 \longrightarrow (RecordFrame ; CalcStep(t_1))) \blacktriangleleft INP\_DL
 CalcStep \triangleq \mathbf{val} \ t : TIME \bullet \mathbf{wait} \ w : 0 \dots (FRAME\_PERIOD - OUT\_DL - t) \bullet
           \mathbf{var}\ colls: int \bullet CalcCollisions; OutputStep(t, w, colls)
 OutputStep \stackrel{\frown}{=} \mathbf{val} \ t_1 : TIME; \ \mathbf{val} \ w : TIME; \ \mathbf{val} \ colls : int \bullet
                \begin{pmatrix} output\_collisions ! colls @ t_2 \longrightarrow \\ \mathbf{wait} \ FRAME\_PERIOD - (t_1 + w + t_2) \end{pmatrix} \blacktriangleleft OUT\_DL \ ; \ StartCycle
• Init; StartCycle
end
```

The definitions of the process state as well as the Init, RecordFrame and CalcCollisions data operations are exactly as in OBReqsCDx. The parallelisms with ATReqsCDx, however, has been collapsed, giving rise to the new actions StartCycle, CalcStep and OutputStep. The necessary refinement steps and laws are not further discussed here, however the overall refinement procedure ought to be automatable.

## 5.2 Phase MH

In this section, we give a full and meticulous account of the refinement steps carried out during the MH phase. The detailed work on the parallel  $\mathsf{CD}_x$  models revealed a finer subdivision of refinements during this phase, namely into six stages. The journal paper [6] explains the purpose of each stage in more detail.

#### Stages of the MH phase

- 1. Definition of cycle timings.
- 2. Decomposition of data operations that are implemented across different missions and handlers.
- 3. Distribution of time budgets.
- 4. Transformation of sequential data operations into parallel handler actions.
- 5. Transformation of parallel data operations into parallel handler actions.
- 6. Extraction of the missions and handlers.

In the remainder of the section, we discuss the elementary refinement steps for each stage.

#### 5.2.1 Stage 1

In Stage 1 we introduce a cyclic design that embeds the overall timing requirements. Part of this is to introduce an interleaving with a **wait** statement and distribute deadlines in order to localise them to their corresponding prefixes as much as this is possible. In our case study, we specifically introduce an interleaving with **wait** FRAME\_PERIOD and distribute deadlines on the communications on next\_frame and output\_collisions. This stage involves a sequence of low-level refinements using distribution laws for deadlines and extraction laws for interleaving with a wait; we identify them as we go along. For readability, we occasionally highlight mathematical text in colour to emphasise which part of an action or process has been affected by a transformation.

#### Flatten Local Actions

We first flatten the three local actions StartCycle, CalcStep, OutputStep into a single local actions which corresponds to the behaviour of the system and hence will be called System. The flattening facilitates the application of subsequent refinement laws; it is justified by the copy rule.

```
 \begin{array}{l} \mathbf{system} \ \ CDxE\_MH1 \ \ \widehat{=} \ \mathbf{begin} \\ \dots \\ System \ \ \widehat{=} \\ \left( \begin{array}{l} next\_frame \ ? \ frame \ @ \ t_1 \longrightarrow \\ \\ \left( \begin{array}{l} RecordFrame; \\ \mathbf{wait} \ w : 0 \dots (FRAME\_PERIOD - OUT\_DL - t_1) \ \bullet \\ \mathbf{var} \ colls : \ int \ \bullet \ CalcCollisions; \\ \left( \begin{array}{l} output\_collisions \ ! \ colls \ @ \ t_2 \longrightarrow \\ \\ \mathbf{wait} \ FRAME\_PERIOD - (t_1 + w + t_2) \ \end{array} \right) \ \blacktriangleleft \ OUT\_DL; \\ System \\ \bullet \ Init \ ; \ System \\ \mathbf{end} \end{array}
```

We note that the state and data operations of the process are omitted as they are similar to the ones of ECPCDx, the result of the CP phase. In what follows, we merely focus on the refinement of the System action and ignore the rest of the process. Process refinement is established by monotonicity laws, as usual.

#### Narrow Time Budgets

An objective of the refinement in this stage is to remove reference to the locally bound variables  $t_1$  and w in order to remove the **wait** block and time prefix, and subsequently distribute the deadline on  $next\_frame$ . The respective subsequent refinement is facilitated by narrowing the time budget for the computation in each cycle. Here, in particular, we narrow the time budget determined by the

```
wait w:0...(FRAME\_PERIOD - OUT\_DL - t_1) \bullet A(t_1, w)
```

statement. The following two laws enable the respective action refinement.

Circus Time Law 1 (narrow-time-budget-1)

```
wait t_1 	ldots t_2 \sqsubseteq wait t_1' 	ldots t_2' provided t_1 \le t_1' and t_2' \le t_2
```

Circus Time Law 2 (narrow-time-budget-2)

```
wait w: t_1 \dots t_2 \bullet A \sqsubseteq \text{wait } w: t_1' \dots t_2' \bullet A \text{ provided } t_1 \leq t_1' \text{ and } t_2' \leq t_2
```

The first law applies to simple nondeterministic waits whereas the second applies to wait blocks.

We thus perform the following refinement.

System

 $\sqsubseteq$  "application of the law narrow-time-budget-2 using  $t_1 \leq INP\_DL$ "

To apply the law, we require a local (contextual) assumption  $t_1 \leq INP\_DL$ . Suitable opening and closing rules for the window inference mechanism that realises action refinement introduce assumptions like the above. To give an example, constructs of the form  $(c \circ t \longrightarrow A(t)) \blacktriangleleft d$  are expected to have special opening rules that introduce the contextual assumption  $t \leq d$  when shifting the focus to A(t). We see that the introduction of contextual assumptions during refinement is mostly a technical issue; we will not further discuss it here but point to the literature on window inference [] and mechanised *Circus* refinement [11].

## Introduce Interleaving for Cycle Time

In this step, the aim is to replace the inner wait  $FRAME\_PERIOD - (t_1 + w + t_2)$  with an outer interleaving with wait  $FRAME\_PERIOD$ . This interleaving was already present in the abstract ATReqsCDx process, however, has been removed during the CP phase. To achieve this we require the following law.

Circus Time Law 3 (time-prefix-elim)

```
(c \circ t \longrightarrow \mathbf{wait} \ t_1 - t) \blacktriangleleft d \equiv ((c \longrightarrow \mathbf{skip}) \blacktriangleleft d) \parallel \mathbf{wait} \ t_1 \ \mathbf{provided} \ d \leq t_1
```

It yields the refinement given next.

...  $\equiv$  "application of the law time-prefix-elim using  $OUT\_DL \leq FRAME\_PERIOD - t_1 - w$ "

```
 \begin{pmatrix} next\_frame ? frame @ t_1 \longrightarrow \\ RecordFrame; \\ \mathbf{wait} \ w : 0 \dots (FRAME\_PERIOD - INP\_DL - OUT\_DL) \bullet \\ \mathbf{var} \ colls : int \bullet CalcCollisions; \\ (output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL \\ \| \| \mathbf{wait} \ FRAME\_PERIOD - (t_1 + w) \\ Sustem \end{pmatrix};
```

The proviso  $OUT\_DL \leq FRAME\_PERIOD - t_1 - w$  is discharged by the contextual assumptions

- (1)  $t_1 \leq INP\_DL$  and
- $(2) \ w \leq FRAME\_PERIOD INP\_DL OUT\_DL$

using elementary laws of linear arithmetics. This relies on another opening rule for window inference that applies to actions of the form **wait**  $w: t_0 \dots t_1 \bullet A(w)$  and introduces the local assumption  $t_0 \le w \le t_1$ .

#### Extract Interleaving for Cycle Time

We next extract the interleaving with wait  $FRAME\_PERIOD - (t_1 + w)$  to the outer level. This requires a number of extraction laws for interleaving with a basic wait t statement. Below we present them.

```
Circus Time Law 4 (extract-inter-wait-seq)
```

$$Op$$
;  $(A \parallel \mathbf{wait}\ t) \equiv (Op;\ A) \parallel \mathbf{wait}\ t$   
**provided**  $Op$  is a data operation and  $wrtV(Op) \cap FV(t) = \emptyset$ 

Circus Time Law 5 (extract-inter-wait-var)

```
\operatorname{var} x : T \bullet (A \parallel \operatorname{wait} t) \equiv (\operatorname{var} x : T \bullet A) \parallel \operatorname{wait} t provided x \notin FV(t)
```

Circus Time Law 6 (extract-inter-wait-waitblock)

```
wait w: t_1 \dots t_2 \bullet (A(w) \parallel \mathbf{wait} \ t - w) \equiv (\mathbf{wait} \ w: t_1 \dots t_2 \bullet A(w)) \parallel \mathbf{wait} provided t_2 \leq t
```

Circus Time Law 7 (extract-inter-wait-prefix)

$$(c \ @ \ t \longrightarrow (A(t) \parallel \mathbf{wait} \ (t_1 - t))) \blacktriangleleft d \equiv ((c \ @ \ t \longrightarrow A(t)) \blacktriangleleft d) \parallel \mathbf{wait} \ t_1$$
 provided  $d \le t_1$ 

The laws allow us to proceed with the refinement as follows.

... ≡ "application of extraction laws for interleaving"

```
\begin{pmatrix} next\_frame ? frame @ t_1 \longrightarrow \\ RecordFrame; \\ \mathbf{wait} \ w : 0 \dots (FRAME\_PERIOD - INP\_DL - OUT\_DL) \bullet \\ \mathbf{var} \ colls : int \bullet CalcCollisions; \\ (output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL; \\ System \\ \parallel \mathbf{wait} \ FRAME\_PERIOD \end{pmatrix} \blacktriangleleft INP\_DL
```

We omitted the detailed refinement steps for this transformation; they are straight-forward.

## Remove Unused Time Variables

We observe that the local constants  $t_1$  and w, introduced by the time prefix and wait block, are not referenced anymore. The two laws below justify their removal.

Circus Time Law 8 (remove-unused-time-prefix)

```
c \circ t \longrightarrow A \equiv c \longrightarrow A provided t \notin FV(A)
```

Circus Time Law 9 (remove-unused-wait-block)

```
wait w: T \bullet A \equiv \text{wait } T; A \text{ provided } w \notin FV(A)
```

Application of the above laws yields the following simplified action.

... 

"application of the laws remove-unused-time-prefix and remove-unused-wait-block"

```
\begin{pmatrix} \left( \begin{array}{c} next\_frame? frame \longrightarrow \\ RecordFrame; \\ \mathbf{wait} \ 0 \dots (FRAME\_PERIOD - INP\_DL - OUT\_DL) \bullet \\ \mathbf{var} \ colls: int \bullet \ CalcCollisions; \\ (output\_collisions! \ colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL; \\ System \\ \parallel \mathbf{wait} \ FRAME\_PERIOD \end{pmatrix} \blacktriangleleft INP\_DL
```

The removal of the locally bound  $t_1$  and w was essential in order to distribute the outer synchronisation deadline on  $next\_frame$ . This is done in the last sub-step of Stage 1.

#### Distribute Deadlines

The primary law we use in this sub-step is

Circus Time Law 10 (distr-sync-deadline-seq)

$$(c \longrightarrow (A_1; A_2)) \triangleleft d \equiv ((c \longrightarrow A_1) \triangleleft d); A_2$$

We also require a basic law that distributes a prefix over a sequence.

Circus Law 1 (distr-prefix-seq)

$$c \longrightarrow (A_1; A_2) \equiv (c \longrightarrow A_1); A_2$$

The application of the two laws in sequence yields

 $\dots \equiv$  "application of the law distr-prefix-seq"

```
 \begin{pmatrix} (next\_frame ? frame \longrightarrow \textit{RecordFrame}); \\ \textbf{wait} \ 0 \dots (FRAME\_PERIOD - INP\_DL - OUT\_DL); \\ \textbf{var} \ colls : int \bullet CalcCollisions; \\ (output\_collisions ! colls \longrightarrow \textbf{skip}) \blacktriangleleft OUT\_DL; \\ System \\ ||| \textbf{wait} \ FRAME\_PERIOD
```

 $\dots \equiv$  "application of the law distr-sync-deadline-seq"

```
\begin{pmatrix} (next\_frame ? frame \longrightarrow RecordFrame) \blacktriangleleft INP\_DL; \\ \mathbf{wait} \ 0 \dots (FRAME\_PERIOD - INP\_DL - OUT\_DL); \\ \mathbf{var} \ colls : int \bullet CalcCollisions; \\ (output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL; \\ System \\ \parallel \mathbf{wait} \ FRAME\_PERIOD \end{pmatrix}
```

In a final sub-step, we extract the sequence with *System* from the inner block. This uses associativity of sequential composition as well as a basic law to extract the sequence with *System* from the variable declaration. These laws shall not be presented here but can be found in Appendix B.1.

...  $\Box$  "application of associativity and distribution laws to extract sequence with System"

```
 \begin{pmatrix} \left( \begin{array}{c} (next\_frame? frame \longrightarrow RecordFrame) \blacktriangleleft INP\_DL; \\ \mathbf{wait} \ 0 \ldots (FRAME\_PERIOD - INP\_DL - OUT\_DL); \\ \mathbf{var} \ colls : int \bullet CalcCollisions; \\ (output\_collisions! \ colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL \\ \end{pmatrix} \right); \\ \| \mathbf{wait} \ FRAME\_PERIOD \\ System  \end{pmatrix}
```

The above action concludes Stage 1 of the MH phase. All deadlines have been localised to the corresponding synchronisations. Besides, we have narrowed the time budget and introduced the cycle time. We notice, however, that the time budget is still captures by a single **wait**; it is decomposed later on in Stage 3.

#### 5.2.2 Stage 2

In this stage we decompose the data operations to match them to the design of missions and handlers. Here, this is, in particular, the *RecordFrame* data operation. The decomposition is performed in two separate refinements which we present in the sequel.

#### Refinement 1

The first refinement decomposes the *RecordFrame* into three sequential data operations. Specifically, they are *StoreFrame*, *ReduceAndPartitionWork* and *DetectCollisions*.

```
system CDxE\_MH2A \stackrel{\frown}{=} \mathbf{begin}
state CDxMH2AState == ECPStateCDx
   Init
    CDxMH2AState
    currentFrame' = \mathbf{new} RawFrame
    state' = \mathbf{new} \, State \, Table
    voxel\_map' = \mathbf{new} \ HashMap[Vector2d, List[Motion]]
    work' = \mathbf{new} \, Partition(4)
    collisions' = 0
   StoreFrame
    \Delta CDxMH2AState
    frame?: Frame
    \exists posns, posns' : Frame; motions, motions' : Frame
        dom\ posns = dom\ motions \land dom\ posns' = dom\ motions' \bullet
       posns' = frame? \land
      motions' =
           \{a: \operatorname{dom} posns' \bullet a \mapsto \operatorname{if} a \in \operatorname{dom} posns \operatorname{then} (posns' a) -_V (posns a) \operatorname{else} Zero V \}
      posns = F(currentFrame) \land motions = G(currentFrame, state) \land
       posns' = F(currentFrame') \land motions' = G(currentFrame', state')
   Reduce And Partition Work
    \Delta \textit{CDxMH2AState}
    currentFrame' = currentFrame \land state' = state
    \exists posns : Frame; motions : Frame \mid dom posns = dom motions \bullet
       posns = F(currentFrame) \land motions = G(currentFrame, state) \land
         \forall a_1, a_2 : Aircraft \mid \{a_1, a_2\} \subseteq \text{dom } posns \bullet
             (a_1, a_2) \in CalcCollisionSet(posns, motions) \Rightarrow
                    (\exists l : List[Motion] \mid l \in voxel\_map' . values() . elems() \bullet
                         MkMotion(a_1, posns \ a_1 - V \ motions \ a_1, posns \ a_1) \in l \ . \ elems()
                         MkMotion(a_2, posns \ a_2 - V \ motions \ a_2, posns \ a_2) \in l \ . \ elems()
   DetectCollisions
    \Delta CDxMH2AState
    currentFrame' = currentFrame \land state' = state \land voxel\_map' = voxel\_map \land work' = work
    \exists posns : Frame; motions : Frame \mid dom posns = dom motions \bullet
      posns = F(currentFrame) \land motions = G(currentFrame, state) \land
       \exists collset : \mathbb{F}(Aircraft \times Aircraft) \mid collset = CalcCollisionSet(posns, motions) \bullet
           (\# collset = 0 \land collisions' = 0) \lor (\# collset > 0 \land collisions' \ge (\# collset) \text{ div } 2)
```

Data Operation	Handler	Instances	Execution
StoreFrame	InputFrameHandler	1	sequential
Reduce And Partition Work	ReducerHandler	1	sequential
Detect Collisions	DetectorHandler	4	parallel

Table 2: Mapping of data operations in  $CDxE\_MH1A$  to handlers in the program.

We observe that the *RecordFrame* data operation has been removed from the model: its behaviour is now realised by the sequence of *StoreFrame*, *ReduceAndPartitionWork* and *DetectCollisions* in *StartCycle*. The decomposition of *RecordFrame* is necessary for the subsequent design that splits its behaviour between the handlers of the mission. Table 2 summarises which handler(s) of the application caters for which data operation in the model. We note that *DetectCollisions* is implemented by four handler instances.

In addition to refining data operations, we also carry out a minor action refinement of the *System* action in the above process. After decomposition, the following fragment emerges in the *System* action.

```
\begin{pmatrix} next\_frame ? frame \longrightarrow \\ StoreFrame; \\ ReduceAndPartitionWork; \\ DetectCollisions \end{pmatrix} \blacktriangleleft INP\_DL
```

The action refinement binds the prefix to the *StoreFrame* data operation and distributes the deadline through the sequence. This is similar to the refinement in the last sub-step of Stage 1, using exactly the same laws.

 $\ldots \equiv$  "application of the laws distr-prefix-seq and distr-sync-deadline-seq"

```
\begin{pmatrix} (next\_frame?frame \rightarrow StoreFrame) \blacktriangleleft INP\_DL; \\ ReduceAndPartitionWork; \\ DetectCollisions \end{pmatrix}
```

In general, the decomposition of *RecordFrame* essentially extracts conjuncts of the schema predicate that modify the value of specific variables. In principle, this can be automated, subject to guidance by the user.

#### Refinement 2

The second refinement further decomposes the *DetectCollisions* data operation into a conjunction. This is desirable because the behaviour of this operation will later be implemented by parallel handlers.

```
system CDxE\_MH2B \stackrel{\frown}{=} \mathbf{begin}
state \ CDxMH2BState == ECPStateCDx
       Init.
         CDxMH2BState'
         currentFrame' = \mathbf{new} RawFrame
         state' = \mathbf{new} \, State Table
         voxel\_map' = \mathbf{new} \; HashMap[Vector2d, List[Motion]]
         work' = \mathbf{new} \, Partition(4)
         collisions' = 0
       StoreFrame
         \Delta CDxMH2BState
        frame?: Frame
         \exists posns, posns' : Frame; motions, motions' : Frame
                   dom\ posns = dom\ motions \land dom\ posns' = dom\ motions' \bullet
               posns' = frame? \land
               motions' =
                         \{a: \operatorname{dom} posns' \bullet a \mapsto \operatorname{if} a \in \operatorname{dom} posns \operatorname{then} (posns' a) -_V (posns a) \operatorname{else} Zero V\} \wedge
               posns = F(currentFrame) \land motions = G(currentFrame, state) \land
               posns' = F(currentFrame') \land motions' = G(currentFrame', state')
       Reduce And Partition Work
         \Delta CDxMH2BState
         currentFrame' = currentFrame \land state' = state
         \exists posns : Frame; motions : Frame \mid dom posns = dom motions \bullet
               posns = F(currentFrame) \land motions = G(currentFrame, state) \land formula = for
                      \forall a_1, a_2 : Aircraft \mid \{a_1, a_2\} \subseteq \text{dom } posns \bullet
                               (a_1, a_2) \in CalcCollisionSet(posns, motions) \Rightarrow \\ \begin{pmatrix} \exists \ l : List[Motion] \mid l \in voxel\_map' \ . \ values() \ . \ elems() \bullet \\ MkMotion(a_1, posns \ a_1 - _V \ motions \ a_1, posns \ a_1) \in l \ . \ elems() \land \\ \end{pmatrix}
                                                         MkMotion(a_2, posns \ a_2 - V \ motions \ a_2, posns \ a_2) \in l \ . \ elems()
       CalcPartCollisions
         \Xi CDxMH2BState
         pcolls!:int
        i? : 1 . . 4
        pcolls! =
                                a_1 : Aircraft; \ a_2 : Aircraft \mid
                                               (\exists \ l: List[Motion] \ | \ l \in work \ . \ getDetectorWork(i?). \ elems() \bullet )
                                               \exists v_1, v_2 : Vector; w_1, w_2 : Vector \bullet
                                                         MkMotion(a_1, v_1, w_1) \in l . elems() \land
                                                          MkMotion(a_2, v_2, w_2) \in l . elems() \land
                                                           collide((v_1, w_1 - V v_1), (v_2, w_2 - V v_2))
```

```
Set Collisions From Parts
     \Delta CDxMH2BState
     collsbag?: bag int
     currentFrame' = currentFrame \land state' = state
     voxel\_map' = voxel\_map \land work' = work
     \exists s : \text{seq } int \mid s = items \ collsbag? \bullet \ collisions' = \Sigma \ s
DetectCollisions =
         \mathbf{var}\ colls1, colls2, colls3, colls4: int ullet
                  \begin{pmatrix} (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1) \land \\ (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2) \land \\ (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3) \land \\ (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4) \end{pmatrix} 
                SetCollisionsFromParts([colls1, colls2, colls3, colls4])
    Calc Collisions
     \Xi CDxMH2BState
     colls! : \mathbb{N}
     \exists posns : Frame; motions : Frame \mid dom posns = dom motions \bullet
         posns = F(currentFrame) \land motions = G(currentFrame, state) \land
         \exists \ collset : \mathbb{F}(Aircraft \times Aircraft) \mid collset = CalcCollisionSet(posns, motions) \bullet
               (\# collset = 0 \land colls! = 0) \lor (\# collset > 0 \land colls! > (\# collset) \text{ div } 2)
System =
                 (next\_frame ? frame \longrightarrow StoreFrame) \blacktriangleleft INP\_DL;
                 Reduce And Partition Work;
                DetectCollisions;
wait 0...(FRAME\_PERIOD - INP\_DL - OUT\_DL);
var colls: int \bullet CalcCollisions;
(output\_collisions! colls \longrightarrow skip) \blacktriangleleft OUT\_DL
| wait FRAME\_PERIOD
• Init; System
end
```

Two data operations CalcPartCollisions and SetCollisionsFromParts have been introduced in this refinement. The first one calculates the collisions result for a particular partition of the subdivided work. The second one merges all partial results (this is just adding them together). The merge operation is specified in terms of a bag to emphasise that the order in which the results are computed and merged is immaterial. This will later on be exploited when parallelising DetectCollisions at the level of actions.

We note, however, that the behaviour of the detector handlers is not fully parallelised yet in this model. In particular, the effect of *SetCollisionsFromParts* has to be distributed into the handlers. This cannot be done at the level of data operations though due to the absence of sharing.

This model concludes Stage 2 of the MH phase. All refinement at the level of data operations is completed here and subsequent stages focus on the refinement of actions. In general, decomposition of data operations is a non-trivial design task; automation through tools may be envisaged for particular patterns.

Time Budget	Respective Data Operation
$SF_{TB}$	StoreFrame
$RPW_{TB}$	Reduce And Partition Work
$DC_{TB}$	Detect Collisions
$CC_{TB}$	Calc Collisions

Table 3: Time budgets introduced for the data operations.

#### 5.2.3 Stage 3

In this stage we distribute time budgets between data operations. This involves two sub-steps.

- 1. Decompose nondeterministic wait statements for time budgets where appropriate.
- 2. Move decomposed time budgets to the respective data operation.

The decomposition in sub-step (1) is effectively achieved by the following two laws.

Circus Time Law 11 (split-time-budget-1)

```
wait 0 ... t \equiv \text{wait } 0 ... t_1; wait 0 ... t_2 provided t = t_1 + t_2
```

Circus Time Law 12 (split-time-budget-2)

```
wait 0 \dots t \subseteq \text{wait } 0 \dots t_1; wait 0 \dots t_2 provided t_1 + t_2 \leq t
```

In our example, we use multiple applications of the second law.

 $\ldots \sqsubseteq$  "multiple applications of law split-time-budget-2"

```
 \left( \left( \begin{array}{c} (next\_frame? frame \longrightarrow StoreFrame) \blacktriangleleft INP\_DL; \\ ReduceAndPartitionWork; \\ DetectCollisions; \\ \mathbf{wait} \ 0 \dots SF_{TB}; \\ \mathbf{wait} \ 0 \dots RPW_{TB}; \\ \mathbf{wait} \ 0 \dots DC_{TB}; \\ \mathbf{wait} \ 0 \dots CC_{TB}; \\ \mathbf{var} \ colls: int \bullet CalcCollisions; \\ (output\_collisions! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL \right) \\ \|\| \mathbf{wait} \ FRAME\_PERIOD \\ System \end{array} \right)
```

Above, the axiomatic constants  $SF_{TB}$ ,  $RPW_{TB}$ ,  $DC_{TB}$  and  $CC_{TB}$  have been introduced to determine the time budgets for individual data operations. Table 3 summarises the relationship between these constants and the corresponding data operation of the *Circus* process.

At this point, it is in fact not necessary to precisely specify the values of the time budgets. However, to discharge the proviso of the above refinement, we require at least the following property.

$$SF_{TB} + RPW_{TB} + DC_{TB} + CC_{TB} \leq FRAME\_PERIOD - INP\_DL - OUT\_DL$$

We therefore introduce the time budgets axiomatically as follows. This assumes  $TIME : \mathbb{P}(\mathbb{A})$ .

```
\begin{array}{c} SF_{TB}: TIME \\ RPW_{TB}: TIME \\ DC_{TB}: TIME \\ CC_{TB}: TIME \\ \hline \hline SF_{TB} + RPW_{TB} + DC_{TB} + CC_{TB} \leq FRAME\_PERIOD - INP\_DL - OUT\_DL \end{array}
```

In sub-step (2), we move the operation-specific time budgets to the data operations they apply to. This uses

associativity of sequential composition  $(A_1; A_2); A_3 \equiv A_1; (A_2; A_3)$ , elementary distribution theorems for sequencing, and the following commutativity law for a time budget and data operation.

## Circus Time Law 13 (time-budget-op-comm)

```
P(Op; \mathbf{wait} t_1 \dots t_2) \equiv P(\mathbf{wait} t_1 \dots t_2; Op) provided Op is a data operation
```

This law is in fact non-compositional: it is a law about processes rather than actions. Hence, it only holds if the underlying action Op; wait  $t_1 cdots t_2$  is embedded in a process P. The justification for the law comes from the structure and semantics of processes that prevents one from observing the precise time at which an (internal) state change takes place. It is proved by induction over the structure of processes.

Using multiple and symmetric applications of the previous law, we proceed to obtain

 $\dots \sqsubseteq$  "multiple applications of law time-budget-op-comm and elementary laws"

```
 \left( \begin{pmatrix} (next\_frame?frame \longrightarrow \mathbf{wait} \ 0 \ldots SF_{TB} \ ; \ StoreFrame) \blacktriangleleft INP\_DL; \\ \mathbf{wait} \ 0 \ldots RPW_{TB} \ ; \ ReduceAndPartitionWork; \\ \mathbf{wait} \ 0 \ldots DC_{TB} \ ; \ DetectCollisions; \\ \mathbf{var} \ colls : int \bullet \mathbf{wait} \ 0 \ldots CC_{TB} \ ; \ CalcCollisions; \\ (output\_collisions! \ colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL \\ \parallel \mathbf{wait} \ FRAME\_PERIOD \\ System \right)
```

Once again, we omit the details of the elementary refinement steps (the laws used are in Appendix B.1). This concludes Stage 3 since all data operations are now equipped with an operation-specific time budget.

#### 5.2.4 Stage 4

This stage addresses the parallelisation of sequential data operations into parallel handler actions. Generally, this takes advantage of the following two laws.

Circus Law 2 (seq-to-par-1)

Circus Law 3 (seq-to-par-2)

```
A_1 \; ; \; A_2 \equiv ((A_1 \; ; \; c \, ! \; x \longrightarrow \mathbf{skip}) \; \llbracket \; wrtV(A_1) \; | \; \{ \mid c \mid \} \; | \; wrtV(A_2) \; \rrbracket \; (c \, ? \; x \longrightarrow A_2)) \setminus \{ \mid c \mid \}  provided wrtV(A_1) \cap wrtV(A_2) = \varnothing and wrtV(A_1) \cap usedV(A_2) = \{x\} and c \notin usedC(A_1) \cup usedC(A_2)
```

The first law is applicable when no shared data is calculated and passed between the sequential actions, or in other words, the first action  $A_1$  does not write data that the second action  $A_2$  reads. If there is such data, the second law has to be applied. We note that in seq-to-par-1, the new channel c is typeless and we can think of it purely in terms of establishing control of execution. In seq-to-par-2, the new channel c is typed according to the shared data that is passed between the sequential actions. It thus fulfils the dual purpose of exercising control and providing a means for communicating shared data through the parallelism.

In our example, we apply seq-to-par-2 three times to fully parallelise the sequence of data operations. The law seq-to-par-1 is not used, although we do require it later on in the refinement for the SH phase. The three applications of seq-to-par-2 are interleaved with auxiliary refinement steps that distribute input prefixes and extract the hiding of the new channels. Below, we highlight the focus of the action refinement in this stage.

```
System \triangleq \left( \begin{pmatrix} (next\_frame?frame \longrightarrow (\mathbf{wait} \ 0 \ldots SF_{TB} \ ; \ StoreFrame)) \blacktriangleleft INP\_DL; \\ \mathbf{wait} \ 0 \ldots RPW_{TB} \ ; \ ReduceAndPartitionWork; \\ \mathbf{wait} \ 0 \ldots DC_{TB} \ ; \ DetectCollisions; \\ \mathbf{var} \ colls : int \bullet \mathbf{wait} \ 0 \ldots CC_{TB} \ ; \ CalcCollisions; \\ (output\_collisions! \ colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL \\ \parallel \mathbf{wait} \ FRAME\_PERIOD \\ System \right),
```

We proceed by refining the highlighted action as follows.

```
 \begin{pmatrix} (next\_frame?frame \longrightarrow (\mathbf{wait} \ 0 \ldots SF_{TB} \ ; \ StoreFrame)) \blacktriangleleft INP\_DL; \\ \mathbf{wait} \ 0 \ldots RPW_{TB} \ ; \ ReduceAndPartitionWork; \\ \mathbf{wait} \ 0 \ldots DC_{TB} \ ; \ DetectCollisions; \\ \mathbf{var} \ colls : int \bullet \mathbf{wait} \ 0 \ldots CC_{TB} \ ; \ CalcCollisions; \\ (output\_collisions! \ colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL
```

 $\equiv$  "application of the law seq-to-par-2 introducing a channel reduce of type RawFrame  $\times$  StateTable"

```
 \left( \begin{array}{l} \left( (next\_frame ? frame \longrightarrow (\mathbf{wait} \ 0 \ldots SF_{TB} \ ; \ StoreFrame)) \blacktriangleleft INP\_DL; \\ reduce ! \ currentFrame ! \ state \longrightarrow \mathbf{skip} \\ \llbracket \{ currentFrame, state \} \mid \{ \ reduce \ \} \mid \{ voxel\_map, work, collisions \} \rrbracket \\ \left( \begin{array}{l} reduce ? \ currentFrame ? \ state \longrightarrow \\ \left( \begin{array}{l} \mathbf{vait} \ 0 \ldots RPW_{TB} \ ; \ ReduceAndPartitionWork; \\ \mathbf{wait} \ 0 \ldots DC_{TB} \ ; \ DetectCollisions; \\ \mathbf{var} \ colls : int \bullet \mathbf{wait} \ 0 \ldots CC_{TB} \ ; \ CalcCollisions; \\ (output\_collisions ! \ colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL \end{array} \right) \right)
```

 $\equiv$  "distribution of input prefix reduce? currentFrame? state  $\longrightarrow \dots$  using elementary laws"

```
 \left( \begin{array}{l} \left( (next\_frame ? frame \longrightarrow (\mathbf{wait} \ 0 \ldots SF_{TB} \ ; \ StoreFrame)) \blacktriangleleft INP\_DL; \\ reduce ! \ currentFrame ! \ state \longrightarrow \mathbf{skip} \\ \llbracket \{ currentFrame, state \} \mid \{ \ reduce \ \} \mid \{ voxel\_map, work, collisions \} \rrbracket \\ \left( \begin{array}{l} \left( \begin{array}{l} reduce ? \ currentFrame ? \ state \longrightarrow \\ \mathbf{wait} \ 0 \ldots RPW_{TB} \ ; \ ReduceAndPartitionWork \end{array} \right); \\ \left( \begin{array}{l} \mathbf{wait} \ 0 \ldots DC_{TB} \ ; \ DetectCollisions; \\ \mathbf{var} \ colls : int \bullet \mathbf{wait} \ 0 \ldots CC_{TB} \ ; \ CalcCollisions; \\ (output\_collisions ! \ colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL \end{array} \right) \right)
```

≡ "application of the law seq-to-par-2 introducing a channel detect of type Partition"

```
 \begin{pmatrix} (next\_frame ? frame \longrightarrow (\mathbf{wait} \ 0 \dots SF_{TB} \ ; \ StoreFrame)) \blacktriangleleft INP\_DL; \\ reduce ! \ currentFrame ! \ state \longrightarrow \mathbf{skip} \\ \llbracket \{ currentFrame, state \} \mid \{ \ reduce \} \mid \{ voxel\_map, work, collisions \} \rrbracket \\ \begin{pmatrix} (reduce ? \ currentFrame ? \ state \longrightarrow \\ \mathbf{wait} \ 0 \dots RPW_{TB} \ ; \ ReduceAndPartitionWork; \\ \mathbf{detect} ! \ work \longrightarrow \mathbf{skip} \\ \llbracket \{ voxel\_map, work \} \mid \{ \ \mathbf{detect} \} \mid \{ collisions \} \rrbracket \\ \begin{pmatrix} \mathbf{detect} ? \ work \longrightarrow \\ \mathbf{wait} \ 0 \dots DC_{TB} \ ; \ DetectCollisions; \\ \mathbf{var} \ colls : \ int \bullet \mathbf{wait} \ 0 \dots CC_{TB} \ ; \ CalcCollisions; \\ (output\_collisions ! \ colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL \end{pmatrix}
```

 $\equiv$  "distribution of input prefix detect? work  $\longrightarrow \dots$  using elementary laws"

```
 \begin{pmatrix} (next\_frame ? frame \longrightarrow (\mathbf{wait} \ 0 \ldots SF_{TB} \ ; \ StoreFrame)) \blacktriangleleft INP\_DL; \\ reduce ! \ currentFrame ! \ state \longrightarrow \mathbf{skip} \\ \llbracket \{ currentFrame, state \} \mid \{ \ reduce \, \} \mid \{ voxel\_map, work, collisions \} \rrbracket \\ \begin{pmatrix} (reduce ? \ currentFrame ? \ state \longrightarrow \\ \mathbf{wait} \ 0 \ldots RPW_{TB} \ ; \ ReduceAndPartitionWork; \\ detect ! \ work \longrightarrow \mathbf{skip} \\ \llbracket \{ voxel\_map, work \} \mid \{ \ detect \, \} \mid \{ collisions \} \rrbracket \\ \begin{pmatrix} (detect ? \ work \longrightarrow \\ \mathbf{wait} \ 0 \ldots DC_{TB} \ ; \ DetectCollisions) \\ \end{pmatrix} \setminus \{ \ detect \, \} \\ \begin{pmatrix} \mathbf{var} \ colls : int \bullet \mathbf{wait} \ 0 \ldots CC_{TB} \ ; \ CalcCollisions; \\ (output\_collisions ! \ colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL \end{pmatrix}
```

 $\equiv$  "extraction of hiding of the channel detect using elementary laws"

 $\equiv$  "application of the law seq-to-par-2 introducing a channel output of type int"

```
(next\_frame? frame \longrightarrow (\mathbf{wait} \ 0 \dots SF_{TB}; StoreFrame)) \blacktriangleleft INP\_DL;
           reduce ! currentFrame ! state \longrightarrow \mathbf{skip}
             [[\{currentFrame, state\} \mid \{|reduce\}| \mid \{voxel\_map, work, collisions\}]]
           reduce ? currentFrame ? state \longrightarrow
           wait 0 ... RPW_{TB}; ReduceAndPartitionWork;
           detect ! work \longrightarrow \mathbf{skip}
             [\{voxel\_map, work\} \mid \{|detect|\} \mid \{collisions\}]]
                                                                                                                           \setminus \{ | reduce, detect | \}
              detect?work \longrightarrow
              wait 0 ... DC_{TB}; DetectCollisions;
              output ! collisions \longrightarrow \mathbf{skip}
                                                                                                \setminus \{ | output | \}
                [\{collisions\} \mid \{ output \} \mid \varnothing ]
              foutput ? collisions \longrightarrow
              \mathbf{var}\ colls: int \bullet \mathbf{wait}\ 0 \dots CC_{TB}\ ; \ Calc Collisions;
              (output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL
= "extraction of hiding of the channel output using elementary laws"
          (next\_frame? frame \longrightarrow (\mathbf{wait} \ 0 \dots SF_{TB} \ ; \ StoreFrame)) \blacktriangleleft INP\_DL;
           reduce ! currentFrame ! state \longrightarrow \mathbf{skip}
             [[\{currentFrame, state\} \mid \{| reduce |\} \mid \{voxel\_map, work, collisions\}]]
           reduce ? currentFrame ? state \longrightarrow
           wait 0 ... RPW_{TB}; ReduceAndPartitionWork;
           detect ! work \longrightarrow \mathbf{skip}
             [\{voxel\_map, work\} \mid \{|detect|\} \mid \{collisions\}]]
           detect?work \longrightarrow
           wait 0 ... DC_{TB}; DetectCollisions;
          output ! collisions \longrightarrow \mathbf{skip}
             [[\{collisions\} \mid \{|output|\} \mid \varnothing]]
           coutput ? collisions \longrightarrow
           \mathbf{var}\ colls: int \bullet \mathbf{wait}\ 0 \dots CC_{TB}\ ; \ Calc Collisions;
           (output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL
          \{| reduce, detect, output |\}
```

After inserting the refined action into the overall *System* we obtain the following result.

```
System =
                (next\_frame ? frame \longrightarrow (wait 0 ... SF_{TB}; StoreFrame)) \blacktriangleleft INP\_DL;
                reduce ! currentFrame ! state \longrightarrow \mathbf{skip}
                  [[\{currentFrame, state\} \mid \{| reduce |\} \mid \{voxel\_map, work, collisions\}]]
                reduce ? currentFrame ? state \longrightarrow
                wait 0 ... RPW_{TB}; ReduceAndPartitionWork;
                detect ! work \longrightarrow \mathbf{skip}
                  [\{voxel\_map, work\} \mid \{|detect|\} \mid \{collisions\}]]
                detect?work \longrightarrow
                wait 0 ... DC_{TB}; DetectCollisions;
                output ! collisions \longrightarrow \mathbf{skip}
                  [[\{collisions\} \mid \{|output|\} \mid \varnothing]]
                output ? collisions \longrightarrow
                var colls: int \bullet wait 0 ... CC_{TB}; Calc Collisions;
                (output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL
               \{| reduce, detect, output |\}
          \parallel \parallel wait FRAME\_PERIOD
       System
```

We further extract the hiding of the new channels to the outer level.

 $\dots \equiv$  "extraction of hiding of { reduce, detect, output } using elementary laws"

 $\{\mid reduce, \, detect, \, output \, \}$ 

This concludes Stage 4 of the refinement for the MH phase. We observe that all sequential data operations have been transformed into parallel actions. Execution control and the passing of data is achieved by the new channels *reduce*, *detect* and *output*. Parallelisation is, however, not completed yet. In particular, the parallelism of detection handlers has not emerged in this stage. The application of laws follows a uniform pattern and hence automation guided by the developer should be possible in this stage.

## 5.2.5 Stage 5

Stage 5 deals with the transformation of parallel data operations (schema conjunctions) into parallel handler actions. In our example, this is the refinement of *DetectCollisions*, including its time budget. Below we use the copy rule to expand the definition of *DetectCollisions* in the *System* action.

The focus of the subsequent refinement steps is highlighted.

 $\dots \equiv$  "copy rule expanding DetectCollisions"

```
System =
                    (next\_frame ? frame \longrightarrow (wait 0 ... SF_{TB} ; StoreFrame)) \blacktriangleleft INP\_DL;
                   reduce ! currentFrame ! state \longrightarrow \mathbf{skip}
                      [{currentFrame, state} | {reduce} | {voxel\_map, work, collisions}]
                    reduce ? currentFrame ? state \longrightarrow
                    wait 0 ... RPW_{TB}; ReduceAndPartitionWork;
                    detect \,!\: work \longrightarrow \mathbf{skip}
                       [\{voxel\_map, work\} \mid \{|detect|\} \mid \{collisions\}]]
                    detect ? work \longrightarrow \mathbf{wait} \ 0 \dots DC_{TB};
                        \mathbf{var}\ colls1,\ colls2,\ colls3,\ colls4:\ int
                                 (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1) \land (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1)
                                 \begin{array}{c} (\exists \, i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \, \wedge \, i? = 2) \, \wedge \\ (\exists \, i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \, \wedge \, i? = 3) \, \wedge \end{array} 
                                 (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4)
                              SetCollisionsFromParts([colls1, colls2, colls3, colls4])
                    output ! collisions \longrightarrow \mathbf{skip}
                      [\{collisions\} \mid \{|output|\} \mid \varnothing]
                    output ? collisions \longrightarrow
                    \mathbf{var}\ colls: int \bullet \mathbf{wait}\ 0..\ CC_{TB}\ ;\ Calc Collisions;
                    (output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL
             \parallel \parallel wait FRAME\_PERIOD
         System
           \{| reduce, detect, output |\}
```

We start by decomposing the time budget for *DetectCollisions* as already illustrated in Stage 3.

```
 \begin{aligned} & \textbf{wait } 0 \dots DC_{TB}; \\ & \textbf{(var } colls1, colls2, colls3, colls4: int \bullet \\ & \textbf{(}\exists i?: \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1) \land \\ & \textbf{(}\exists i?: \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2) \land \\ & \textbf{(}\exists i?: \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3) \land \\ & \textbf{(}\exists i?: \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4) \\ & SetCollisionsFromParts([[colls1, colls2, colls3, colls4]]) \\ \end{aligned} ; \end{aligned}
```

⊑ "application of the law split-time-budget-2"

```
 \begin{array}{l} \mathbf{wait} \ 0 \ .. \ CPC_{TB} \ ; \ \mathbf{wait} \ 0 \ .. \ SC_{TB} \ ; \\ \mathbf{(a..., colls1, colls2, colls3, colls4: int \bullet)} \\ & \left( (\exists \ i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land \ i? = 1) \land \\ & \left( (\exists \ i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land \ i? = 2) \land \\ & \left( (\exists \ i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land \ i? = 3) \land \\ & \left( (\exists \ i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land \ i? = 4) \right) \\ & SetCollisionsFromParts([[colls1, colls2, colls3, colls4]]) \end{array} \right)
```

This assume the presence of two further constants  $CPC_{TB}$  and  $SC_{TB}$  with  $CPC_{TB} + SC_{TB} \leq DC_{TB}$ . We next distribute the time budgets in order to attach them to the respective data operations. This, again,

is similar in principle to what we has already been illustrated in Stage 3. The following supplementary law facilitates distribution of time budgets into local variable declarations.

# Circus Time Law 14 (distr-wait-seq-var)

```
wait t_1 ... t_2; var x : T \bullet A \equiv \text{var } x : T \bullet (\text{wait } t_1 ... t_2 ; A)
provided x \notin FV(t_1) and x \notin FV(t_2)
```

The resulting transformation is given below.

≡ "application of the laws distr-wait-seq-var and time-budget-op-comm"

```
 \begin{pmatrix} \mathbf{var}\ colls1, colls2, colls3, colls4: int \bullet \mathbf{wait}\ 0 \dots CPC_{TB}; \\ (\exists\ i?: \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1) \land \\ (\exists\ i?: \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2) \land \\ (\exists\ i?: \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3) \land \\ (\exists\ i?: \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4) \end{pmatrix} , \\ \mathbf{wait}\ 0 \dots SC_{TB}; \\ SetCollisionsFromParts(\llbracket colls1, colls2, colls3, colls4 \rrbracket)
```

The next law is used to turn the schema conjunction into a parallelism of actions.

# Circus Law 4 (conj-to-par)

```
\mathit{Op}_1 \, \wedge \, \mathit{Op}_2 \, \equiv \, \mathit{Op}_1 \, \llbracket \, \mathit{wrt} \, V(\mathit{Op}_1) \, | \, \varnothing \, | \, \mathit{wrt} \, V(\mathit{Op}_2) \, \rrbracket \, \mathit{Op}_2 \, \, \, \mathbf{provided} \, \, \, \mathit{wrt} \, V(\mathit{Op}_1) \cap \mathit{wrt} \, V(\mathit{Op}_2) = \varnothing
```

It is applicable since all schemas in the above conjunction write to a different variable collsi.

 $\ldots \equiv$  "multiple applications of law conj-to-par"

```
 \begin{pmatrix} \mathbf{var}\ colls1, colls2, colls3, colls4: int \bullet \mathbf{wait}\ 0... CPC_{TB}; \\ \left(\exists\ i?: \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1\right) \\ \left[ \{colls1\} \mid \varnothing \mid \{colls2, colls3, colls4\} \right] \\ \left(\exists\ i?: \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2\right) \\ \left[ \{colls2\} \mid \varnothing \mid \{colls3, colls4\} \right] \\ \left(\exists\ i?: \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3\right) \\ \left[ \{colls3\} \mid \varnothing \mid \{colls4\} \right] \\ \left(\exists\ i?: \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4\right) \\ \mathbf{wait}\ 0... SC_{TB}; \\ SetCollisionsFromParts([[colls1, colls2, colls3, colls4]]) \\ \end{pmatrix}
```

Another distribution law enables us to move wait  $0 \dots CPC_{TB}$  into the parallelism.

## Circus Time Law 15 (distr-wait-seq-par)

```
wait t_1 
ldots t_2; (Op_1 \llbracket \dots \rrbracket Op_2) \equiv (wait t_1 
ldots t_2; Op_1) \llbracket \dots \rrbracket (wait t_1 
ldots t_2; Op_2) provided Op_1 and Op_2 are data operations
```

Intuitively, since the two data operations execute in parallel, each of them has a time budget wait  $t_1 \dots t_2$ .

 $\ldots \equiv$  "multiple applications of law distr-wait-seq-par"

```
 \begin{pmatrix} \mathbf{var}\ colls1, colls2, colls3, colls4: int \bullet \\ \begin{pmatrix} (\mathbf{wait}\ 0 \dots CPC_{TB}\ ; \ (\exists\ i?: \mathbb{Z}\ \bullet\ CalcPartCollisions[colls1/pcolls!] \land i? = 1)) \\ \| \{colls1\} \mid \varnothing \mid \{colls2, colls3, colls4\} \| \\ (\mathbf{wait}\ 0 \dots CPC_{TB}\ ; \ (\exists\ i?: \mathbb{Z}\ \bullet\ CalcPartCollisions[colls2/pcolls!] \land i? = 2)) \\ \| \{colls2\} \mid \varnothing \mid \{colls3, colls4\} \| \\ (\mathbf{wait}\ 0 \dots CPC_{TB}\ ; \ (\exists\ i?: \mathbb{Z}\ \bullet\ CalcPartCollisions[colls3/pcolls!] \land i? = 3)) \\ \| \{colls3\} \mid \varnothing \mid \{colls4\} \| \\ (\mathbf{wait}\ 0 \dots CPC_{TB}\ ; \ (\exists\ i?: \mathbb{Z}\ \bullet\ CalcPartCollisions[colls4/pcolls!] \land i? = 4)) \end{pmatrix}   \mathbf{wait}\ 0 \dots SC_{TB}\ ; \ SetCollisionsFromParts(\| colls1, colls2, colls3, colls4 \| )
```

Above we still have the sequence with the SetCollisionsFromParts data operation; it also has to be parallelised. To achieve this, we use seq-to-par-2 from Stage 4 again, this time introducing a channel setColls of type  $int \times int \times int \times int$  to communicate all partial results computed by the four detector handlers.

 $\dots \equiv$  "application of the law seq-to-par-2"

```
 \begin{pmatrix} \mathbf{var}\ colls1, colls2, colls3, colls4 : int \bullet \\ \begin{pmatrix} (\mathbf{wait}\ 0 \dots CPC_{TB}\ ; \ (\exists\ i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1)) \\ \| \{colls1\} \mid \varnothing \mid \{colls2, colls3, colls4\} \| \\ (\mathbf{wait}\ 0 \dots CPC_{TB}\ ; \ (\exists\ i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2)) \\ \| \{colls2\} \mid \varnothing \mid \{colls3, colls4\} \| \\ (\mathbf{wait}\ 0 \dots CPC_{TB}\ ; \ (\exists\ i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3)) \\ \| \{colls3\} \mid \varnothing \mid \{colls4\} \| \\ (\mathbf{wait}\ 0 \dots CPC_{TB}\ ; \ (\exists\ i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4)) \end{pmatrix} \\ setColls! \ colls1! \ colls2! \ colls3! \ colls4 \longrightarrow \mathbf{skip} \\ \| \{colls1, colls2, colls3, colls4\} \mid \{setColls\} \mid \{collsions\} \| \\ (setColls? \ colls1? \ colls2? \ colls3? \ colls4 \longrightarrow \mathbf{skip} \\ \mathbf{wait}\ 0 \dots SC_{TB}\ ; \ SetCollisionsFromParts([[\ colls1, \ colls2, \ colls3, \ colls4]])) \end{pmatrix}
```

To eliminate the prefix that was introduced in the left branch of the parallelism, we require a specialised channel decomposition law that replaces the channel setColls of type  $int \times int \times int \times int$  by a channel recColls of type int. The intention of the refinement is to decompose a single communication on setColls into an interleaving of four communications on recColls. We note that setColls is assumed to be concealed in the context where this law is applicable. A detailed investigation of the law is future work for now.

 $\dots \equiv$  "application of a specialised high-level channel decomposition law"

```
\mathbf{var}\ colls1, colls2, colls3, colls4: int ullet
                (wait 0 \dots CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1))'
                      [[\{colls1\} \mid \varnothing \mid \{colls2, colls3, colls4\}]]
                 (wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2))
                      [\{colls2\} \mid \varnothing \mid \{colls3, colls4\}]]
                (wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3))
                      [\![\{colls3\} \mid \varnothing \mid \{colls4\}]\!]
                (wait 0 .. CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4))
                (recColls ! colls1 \longrightarrow \mathbf{skip}) \parallel 
                (recColls! colls2 \longrightarrow \mathbf{skip}) \parallel
                (recColls ! colls 3 \longrightarrow \mathbf{skip}) \parallel \parallel
                (recColls ! colls 4 \longrightarrow \mathbf{skip})
               [{colls1, colls2, colls3, colls4} | {recColls} | {collisions}]
             \mathbf{var}\ colls1.\ colls2.\ colls3.\ colls4:int
                (recColls?x \longrightarrow colls1 := x) \parallel
                \begin{array}{c} (recColls? x \longrightarrow colls2 := x) \parallel \\ (recColls? x \longrightarrow colls3 := x) \parallel \\ (recColls? x \longrightarrow colls4 := x) \end{array} 
             wait 0..SC_{TB}; SetCollisionsFromParts([colls1, colls2, colls3, colls4])
           \{| recColls |\}
```

Likewise, another specialised law is subsequently used to distribute the interleaving in the left hand of the outer parallelism into the inner parallelism that computes that partial collision results.

... 

"application of a specialised high-level law for distribution of an interleaving of prefixes"

```
\mathbf{var}\ colls1,\ colls2,\ colls3,\ colls4:\ int
               \text{wait } 0 \dots CPC_{TB} \; ; \; (\exists \; i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1);
                recColls ! colls 1 \longrightarrow \mathbf{skip}
                  [[\{colls1\} \mid \varnothing \mid \{colls2, colls3, colls4\}]]
               wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2);
               recColls ! colls 2 \longrightarrow \mathbf{skip}
                  [\![\{colls2\} \mid \varnothing \mid \{colls3, colls4\}]\!]
               wait 0 \dots CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3);
               recColls ! colls 3 \longrightarrow \mathbf{skip}
                  [\{colls3\} \mid \varnothing \mid \{colls4\}]]
               wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4):
               recColls ! colls 4 \longrightarrow \mathbf{skip}
               [\{colls1, colls2, colls3, colls4\} \mid \{\{recColls\}\} \mid \{collisions\}]
             \mathbf{var}\ colls1,\ colls2,\ colls3,\ colls4:\ int
                (recColls?x \longrightarrow colls1 := x) \parallel
                (recColls? x \longrightarrow colls2 := x) \parallel | (recColls? x \longrightarrow colls3 := x) \parallel | (recColls? x \longrightarrow colls4 := x)
             wait 0 ... SC_{TB}; SetCollisionsFromParts([colls1, colls2, colls3, colls4]]
           \{ | recColls | \}
```

A final high-level law sequentialises the interleaving in the right-hand branch of the outer parallelism.

... 

"application of a specialised high-level law for sequentialising prefix interleaving"

```
var colls1, colls2, colls3, colls4: int \bullet
              wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1);
              recColls ! colls 1 \longrightarrow \mathbf{skip}
                 [[\{colls1\} \mid \varnothing \mid \{colls2, colls3, colls4\}]]
               wait 0 . . CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2);
              recColls! colls2 \longrightarrow \mathbf{skip}
                 [\{colls2\} \mid \varnothing \mid \{colls3, colls4\}]]
              wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3);
              recColls ! colls 3 \longrightarrow \mathbf{skip}
                 [\![\{colls3\} \mid \varnothing \mid \{colls4\}]\!]
              wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4):
              recColls ! colls 4 \longrightarrow \mathbf{skip}
             [[\{colls1, colls2, colls3, colls4\} \mid \{[recColls]\} \mid \{collisions\}]]
           \mathbf{var}\ colls1,\ colls2,\ colls3,\ colls4:\ int
               (recColls?x \longrightarrow colls1 := x);
               (recColls?x \longrightarrow colls2 := x);
               (recColls?x \longrightarrow colls3 := x);
(recColls?x \longrightarrow colls4 := x)
            wait 0..SC_{TB}; SetCollisionsFromParts([colls1, colls2, colls3, colls4])
          \{ | recColls | \}
```

This refinement is valid because the SetCollisionsFromParts operation is parametrised in terms of a bag and therefore is agnostic to the order in which results are communicated through the recColls channel. The sequentialising of the interleaving is important in order to decompose and distribute the time budget  $SC_{TB}$  between the elements of the sequence. Hence, in the next step we decompose and distribute the time budget  $SC_{TB}$  into two time budgets,  $RC_{TB}$  and  $SCFP_{TB}$  where  $4*RC_{TB} + SCFP_{TB} \leq SC_{TB}$ .

 $\dots \equiv$  "decomposition and distribution of wait  $0 \dots SC_{TB}$  using laws from Stage 3"

```
var colls1, colls2, colls3, colls4: int \bullet
              wait 0.. CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1);
              recColls! colls1 \longrightarrow \mathbf{skip}
                 [\![\{colls1\} \mid \varnothing \mid \{colls2, colls3, colls4\}]\!]
              wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2);
              recColls ! colls2 \longrightarrow \mathbf{skip}
                 [\{colls2\} \mid \varnothing \mid \{colls3, colls4\}]
              wait 0.. CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3);
              recColls ! colls 3 \longrightarrow \mathbf{skip}
                [[\{colls3\} \mid \varnothing \mid \{colls4\}]]
              wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4);
              recColls ! colls 4 \longrightarrow \mathbf{skip}
             [\{colls1, colls2, colls3, colls4\} \mid \{\{recColls\}\} \mid \{collisions\}]
           var colls1, colls2, colls3, colls4: int \bullet
               (recColls?x \longrightarrow (\mathbf{wait} 0 ... RC_{TB}; colls1 := x));
              (recColls?x \longrightarrow (\mathbf{wait} 0 \dots RC_{TB}; colls2 := x));
              (recColls?x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB}; \ colls3 := x));
              (recColls? x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB}; \ colls4 := x))
            \mathbf{wait} \ 0 \dots SCFP_{TB} \ ; \ SetCollisionsFromParts([colls1, colls2, colls3, colls4])
          \{ | recColls | \}
```

The above refinement is justified by the assumption  $4*RC_{TB} + SCFP_{TB} \leq SC_{TB}$  about the new constants, and also the fact that the communication on recColls is concealed. A detailed formulation of the law including its proof are future work. For one, it seems that it is not compositional. Comparing, for instance,

```
\begin{pmatrix} (recColls?x \longrightarrow colls1 := x);\\ (recColls?x \longrightarrow colls2 := x);\\ (recColls?x \longrightarrow colls3 := x);\\ (recColls?x \longrightarrow colls4 := x) \end{pmatrix} \text{ and }
\begin{pmatrix} (recColls?x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB}; \ colls1 := x));\\ (recColls?x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB}; \ colls2 := x));\\ (recColls?x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB}; \ colls3 := x));\\ (recColls?x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB}; \ colls4 := x)) \end{pmatrix}
```

we observe that the second action refuses more than the first action in terms of its time-wise behaviour. Namely, the first action permits multiple recColls events to occur in the same instant whereas in the second action there can be a delay of up to  $RC_{TB}$  time units between them, so this is not a failures refinement. Therefore, we have to refine the above fragment in context rather than in isolation; this is future work.

We next distribute the local variable declarations into the respective parallel branches that write to the variable. A few basic laws, namely distr-var-hide, distr-var-par, remove-var and compact-write-sets-par are useful here; they are given in Appendix B.1. We shall not discuss the refinement in detail.

... \equiv "application of the laws distr-var-hide, distr-var-par, remove-var and compact-write-sets-par"

```
 \left( \begin{array}{c} \left( \begin{array}{c} \mathbf{var} \ colls1: int \bullet \\ \mathbf{wait} \ 0 \ .. \ CPC_{TB} \ ; \ (\exists i?: \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1); \\ recColls! \ colls1 \longrightarrow \mathbf{skip} \\ \|\varnothing \ | \varnothing \ | \varnothing \| \\ \mathbf{var} \ colls2: int \bullet \\ \mathbf{wait} \ 0 \ .. \ CPC_{TB} \ ; \ (\exists i?: \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2); \\ recColls! \ colls2 \longrightarrow \mathbf{skip} \\ \|\varnothing \ | \varnothing \ | \varnothing \| \\ \mathbf{var} \ colls3: int \bullet \\ \mathbf{wait} \ 0 \ .. \ CPC_{TB} \ ; \ (\exists i?: \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3); \\ recColls! \ colls3 \longrightarrow \mathbf{skip} \\ \|\varnothing \ | \varnothing \ | \varnothing \| \\ \mathbf{var} \ colls4: int \bullet \\ \mathbf{wait} \ 0 \ .. \ CPC_{TB} \ ; \ (\exists i?: \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4); \\ recColls! \ colls4 \longrightarrow \mathbf{skip} \\ \|\varnothing \ | \ \{ recColls \} \ | \ \{ collsions \} \| \\ \mathbf{var} \ colls1; \ colls2, \ colls3, \ colls4: int \bullet \\ \mathbf{var} \ colls1; \ colls2, \ colls3, \ colls4: int \bullet \\ \mathbf{var} \ colls1; \ colls2, \ colls3, \ colls4: int \bullet \\ \mathbf{var} \ colls1; \ colls2, \ colls3, \ colls4: int \bullet \\ \mathbf{var} \ colls1; \ colls2, \ colls3, \ colls4: \ colls2: \ colls3: \ colls2: \ colls3: \ colls4: \ colls3: \ colls4: \ colls2: \ colls3: \ colls4: \ colls4:
```

With the last refinement we have localised the declaration of the *collsi* into the parallel detector handlers. This transformation concludes the refinement of the action fragment of *System* that corresponds to the four detector handlers, whose parallelism has fully emerged now. The right-hand action of the top-level parallelism is a residual control fragment that later on in the SH phase is going to be refined into shared data access to the *collisions* variable by suitable atomic operations.

We now inject the refinement of the parallel detector handlers back into the System action.

```
System =
                  (next\_frame ? frame \longrightarrow (wait 0 .. SF_{TB} ; StoreFrame)) \blacktriangleleft INP\_DL;
                  reduce ! currentFrame ! state \longrightarrow \mathbf{skip}
                     [{currentFrame, state} | {reduce} | {voxel\_map, work, collisions}]]
                   reduce ? currentFrame ? state \longrightarrow
                   wait 0 ... RPW_{TB}; ReduceAndPartitionWork;
                   detect \,!\: work \longrightarrow \mathbf{skip}
                     [[\{voxel\_map, work\} \mid \{|detect|\} \mid \{collisions\}]]
                   detect ? work \longrightarrow
                           \mathbf{var}\ colls1: int \bullet \mathbf{wait}\ 0 \dots CPC_{TB};
                          (\exists \, i? : \mathbb{Z} \bullet \mathit{CalcPartCollisions}[\mathit{colls}1/\mathit{pcolls}!] \wedge i? = 1);
                          recColls ! colls 1 \longrightarrow \mathbf{skip}
                            \llbracket \varnothing \mid \varnothing \mid \varnothing \rrbracket
                           \mathbf{var}\ colls2: int \bullet \mathbf{wait}\ 0 \dots CPC_{TB};
                          (\exists \, i? : \mathbb{Z} \bullet \mathit{CalcPartCollisions}[\mathit{colls2/pcolls!}] \land i? = 2);
                          recColls ! colls 2 \longrightarrow \mathbf{skip}
                            \llbracket \varnothing \mid \varnothing \mid \varnothing \rrbracket
                           \mathbf{var}\ colls3: int \bullet \mathbf{wait}\ 0 \dots CPC_{TB};
                          (\exists \ i? : \mathbb{Z} \bullet \mathit{CalcPartCollisions}[\mathit{colls3/pcolls!}] \land \ i? = 3);
                          recColls ! colls 3 \longrightarrow \mathbf{skip}
                                                                                                                                    \setminus \{ | recColls | \};
                            \mathbf{var}\ colls4: int \bullet \mathbf{wait}\ 0 \dots CPC_{TB};
                           (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4);
                          recColls ! colls 4 \longrightarrow \mathbf{skip}
                            \llbracket \varnothing \mid \{ | recColls \} \mid \{ collisions \} \rrbracket
                           \mathbf{var}\ colls1, colls2, colls3, colls4: int ullet
                              (recColls?x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB}; \ colls 1 := x));
                             (recColls?x \longrightarrow (\mathbf{wait} \ 0 .. RC_{TB}; \ colls2 := x));
(recColls?x \longrightarrow (\mathbf{wait} \ 0 .. RC_{TB}; \ colls3 := x));
                             (recColls?x \longrightarrow (\mathbf{wait} 0 .. RC_{TB}; colls4 := x))
                           wait 0 \dots SCFP_{TB};
                           SetCollisionsFromParts([colls1, colls2, colls3, colls4])
                   output ! collisions \longrightarrow \mathbf{skip}
                     [\{collisions\} \mid \{\{autput\}\} \mid \emptyset]
                   output ? collisions \longrightarrow
                   var colls: int \bullet wait 0 ... CC_{TB}; CalcCollisions;
                   (output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL
           \parallel \parallel wait FRAME\_PERIOD
        System
           \{| reduce, detect, output |\}
```

We observe that the resulting action does not have the desired shape yet: this is a parallelism of handlers plus possible parallel control actions. The refinement that concludes Stage 5 thus has to carry out elementary transformations to put the action (back) into this form.

## Consolidation of Mission Actions

The consolidation steps here, in particular, involve extracting the hiding of recColls and distributing the prefixes  $detect?work \longrightarrow ...$  and  $output\_collisions!collisions \longrightarrow \mathbf{skip}$  into the parallel actions which they surround. The required laws for distributing the prefixes are presented below.

```
Circus Law 9 (distr-prefix-par-1)
c ? x \longrightarrow (A_1 \llbracket ns_1 \mid cs \mid ns_2 \rrbracket A_2) \equiv (c ? x \longrightarrow A_1 \llbracket ns_1 \mid cs \cup \{c\} \mid ns_2 \rrbracket c ? x \longrightarrow A_2)
provided c \notin usedC(A_1) and c \notin usedC(A_2)

Circus Law 10 (distr-prefix-par-2)
(A_1 \llbracket ns_1 \mid cs \mid ns_2 \rrbracket A_2) \; ; \; c! x \longrightarrow \mathbf{skip} \equiv
(A_1 \; ; \; c? y \longrightarrow \mathbf{skip}) \; \llbracket ns_1 \mid cs \cup \{\!\! | c \>\!\! \} \mid ns_2 \>\!\! \rrbracket (A_2 \; ; \; c! x \longrightarrow \mathbf{skip})
```

Their application yields the following action.

**provided**  $c \notin usedC(A_1)$  and  $c \notin usedC(A_2)$  and  $x \notin ns_1$ 

... 

"application of the laws distr-prefix-par-1 and distr-prefix-par-2 and extraction of hiding"

```
(next\_frame ? frame \longrightarrow (wait 0 ... SF_{TB}; StoreFrame)) \blacktriangleleft INP\_DL;
        reduce ! currentFrame ! state \longrightarrow \mathbf{skip}
           [\![\{\mathit{currentFrame}, \mathit{state}\} \mid \{\![\ \mathit{reduce}\ ]\!] \mid \{\mathit{voxel\_map}, \mathit{work}, \mathit{collisions}\}]\!]
         reduce ? currentFrame ? state \longrightarrow
         wait 0 ... RPW_{TB}; ReduceAndPartitionWork;
        detect ! work \longrightarrow \mathbf{skip}
           [[\{voxel\_map, work\} \mid \{|detect|\} \mid \{collisions\}]]
           \forall detect ? work \longrightarrow var colls1 : int \bullet wait 0 .. CPC_{TB};
            (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1);
           \llbracket \varnothing \mid \{ \mid detect, output \mid \} \mid \varnothing \rrbracket
            detect? work → var colls2 : int • wait 0 .. CPC_{TB};
            (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2);
           \llbracket \varnothing \mid \{ \mid detect, output \mid \} \mid \varnothing \rrbracket
            detect? work → var colls3: int • wait 0.. CPC_{TB};
           (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3);
           \llbracket \varnothing \mid \{ \mid detect, output \mid \} \mid \varnothing \rrbracket
            detect? work → var colls4 : int • wait 0 .. CPC_{TB};
            (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4);
           recColls! colls4 \longrightarrow \mathbf{skip}; output?y \longrightarrow \mathbf{skip}
              \llbracket \varnothing \mid \{ \mid detect, output, recColls \mid \} \mid \{ collisions \} \rrbracket
           \forall detect ? work \longrightarrow var colls1, colls2, colls3, colls4 : int \bullet
               (recColls?x \longrightarrow (\mathbf{wait} 0 .. RC_{TB}; colls1 := x));
               (recColls?x \longrightarrow (\mathbf{wait} 0 .. RC_{TB}; colls2 := x));
               (recColls?x \longrightarrow (\mathbf{wait} 0 .. RC_{TB}; colls3 := x));
             \setminus (recColls?x \longrightarrow (\mathbf{wait} 0 ... RC_{TB}; colls4 := x))
            wait 0 \dots SCFP_{TB};
            SetCollisionsFromParts([[colls1, colls2, colls3, colls4]]);
            output ! collisions \longrightarrow \mathbf{skip}
           [[\{collisions\} \mid \{[output]\} \mid \varnothing]]
         output ? collisions \longrightarrow
         \mathbf{var}\ colls: int \bullet \mathbf{wait}\ 0...\ CC_{TB}\ ;\ Calc Collisions;
         (output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL
  \parallel wait FRAME\_PERIOD
System
```

{| reduce, detect, output, recColls |}

We lastly reorder the parallelism and adjust write sets in order to isolated the handler actions and the control fragment into separate parallel branches.

 $\dots \equiv$  "reordering parallel actions and adjusting write sets"

```
(next\_frame ? frame \longrightarrow (wait 0 ... SF_{TB}; StoreFrame)) \blacktriangleleft INP\_DL;
           reduce ! currentFrame ! state \longrightarrow \mathbf{skip}
              [\{currentFrame, state\} \mid \{|reduce|\} \mid \{voxel\_map, work\}]
           \mathbf{wait} \ 0 \dots RPW_{TB} \ ; \ \textit{ReduceAndPartitionWork};
           detect ! work \longrightarrow \mathbf{skip}
              [[\{voxel\_map, work\} \mid \{|detect|\} \mid \varnothing]]
            detect ? work \longrightarrow \mathbf{var} \ colls1 : int \bullet \mathbf{wait} \ 0 \dots CPC_{TB};
            (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1);
           recColls! colls1 \longrightarrow \mathbf{skip}; output? y \longrightarrow \mathbf{skip}
              \llbracket \varnothing \mid \{ \mid detect, output \mid \} \mid \varnothing \rrbracket
            detect? work \longrightarrow \mathbf{var} \ colls2 : int \bullet \mathbf{wait} \ 0 \dots CPC_{TB};
           (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2);
           recColls! colls2 \longrightarrow \mathbf{skip}; output? y \longrightarrow \mathbf{skip}
              \llbracket \varnothing \mid \{ \mid detect, output \mid \} \mid \varnothing \rrbracket
            detect? work \longrightarrow var\ colls3: int \bullet wait 0...\ CPC_{TB};
           (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3);
           recColls! colls3 \longrightarrow \mathbf{skip}; output? y \longrightarrow \mathbf{skip}
              \llbracket \varnothing \mid \{ \mid detect, output \mid \} \mid \varnothing \rceil \}
            detect ? work \longrightarrow \mathbf{var} \ colls4 : int \bullet \mathbf{wait} \ 0 \dots CPC_{TB};
           (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4);
           recColls ! colls 4 \longrightarrow \mathbf{skip} ; output ? y \longrightarrow \mathbf{skip}
              \llbracket \varnothing \mid \{\mid output \mid \} \mid \varnothing \rrbracket
            output? collisions \longrightarrow
           \mathbf{var}\ colls: int \bullet \mathbf{wait}\ 0 \dots CC_{TB}\ ; \ Calc Collisions;
           (output\_collisions! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL
           [\{currentFrame, state, voxel\_map, work\}]
                \{|detect, output, recColls|\} | \{collisions\} \}
        detect ? work \longrightarrow \mathbf{var} \ colls1, \ colls2, \ colls3, \ colls4: int \bullet
            (recColls? x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB}; \ colls 1 := x));
            (\mathit{recColls}\,?\,x \longrightarrow (\mathbf{wait}\,0 \mathrel{.\,.} RC_{TB}\;;\; \mathit{colls}2 := x))\;;
           (recColls?x \longrightarrow (\mathbf{wait} 0 .. RC_{TB}; colls3 := x));
           (recColls?x \longrightarrow (\mathbf{wait} 0 .. RC_{TB}; colls4 := x))
        wait 0 \dots SCFP_{TB};
        SetCollisionsFromParts([[colls1, colls2, colls3, colls4]]);
        output ! collisions \longrightarrow \mathbf{skip}
 \parallel \parallel wait FRAME\_PERIOD
\{| reduce, detect, output, recColls | \}
```

The above refinement of *System* concludes Stage 5 of the MH phase. The parallelism of handler actions has fully emerged now. Whereas Stage 4 seems to provide good opportunities for automation, Stage 5, in comparison, appears to be more challenging in that respect. Automation may be envisage through the application of high-level refinement patterns that encapsulate particular structures, such as the shape of *DetectCollisions*. Subsequent elementary refinements could possibly be tackled by refinement tactics [10].

## 5.2.6 Stage 6

In Stage 6 we extract the mission and handler actions. The only remaining transformation required for this is to distribute the top-level recursion in the *System* action into the parallel branches that correspond to handler actions and control fragments. Lock-step progress per cycle is ensured by a new channel *sync*.

### Distribute Recursion into Parallel Actions

The result of distributing the top-level recursion in *System* is given below.

 $\dots \equiv$  "distribution law for recursion into parallel actions"

```
(\mu X \bullet (next\_frame ? frame \longrightarrow (wait 0 ... SF_{TB}; StoreFrame)) \blacktriangleleft INP\_DL;
         reduce! currentFrame! state \longrightarrow skip; sync \longrightarrow X
            [[\{currentFrame, state\} \mid \{| reduce, sync|\} \mid \{voxel\_map, work\}]]
          \mu X \bullet reduce ? currentFrame ? state \longrightarrow
          wait 0 ... RPW_{TB}; ReduceAndPartitionWork;
         detect ! work \longrightarrow \mathbf{skip} ; sync \longrightarrow X
            [\{voxel\_map, work\} \mid \{|detect, sync|\} \mid \varnothing]
          \mu X \bullet detect? work \longrightarrow \mathbf{var} \ colls1: int \bullet \mathbf{wait} \ 0 \dots CPC_{TB};
          (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1);
         recColls! colls1 \longrightarrow \mathbf{skip}; output?y \longrightarrow \mathbf{skip}; sync \longrightarrow X
            \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
          \mu X \bullet detect? work \longrightarrow \mathbf{var} \ colls2: int \bullet \mathbf{wait} \ 0 \dots CPC_{TB};
         (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2);
         recColls ! colls 2 \longrightarrow \mathbf{skip} ; output ? y \longrightarrow \mathbf{skip} ; sync \longrightarrow X
            \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
          \mu X \bullet detect? work \longrightarrow \mathbf{var} \ colls3: int \bullet \mathbf{wait} \ 0... \ CPC_{TB};
          (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3);
          recColls ! colls 3 \longrightarrow \mathbf{skip} ; output ? y \longrightarrow \mathbf{skip} ; sync \longrightarrow X
            \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
          \mu X \bullet detect? work \longrightarrow \mathbf{var} \ colls4: int \bullet \mathbf{wait} \ 0 \dots CPC_{TB};
          (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4);
          recColls! colls4 \longrightarrow \mathbf{skip}; output? y \longrightarrow \mathbf{skip}; sync \longrightarrow X
            \llbracket \varnothing \mid \{ \mid output, \underline{sync} \} \mid \varnothing \rrbracket
          \mu X \bullet output ? collisions \longrightarrow
          \mathbf{var}\ colls: int \bullet \mathbf{wait}\ 0..\ CC_{TB}\ ;\ Calc Collisions;
          (output\_collisions! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL; sync \longrightarrow
        [\{currentFrame, state, voxel\_map, work\}]
               \{|detect, output, recColls, sync|\} \mid \{collisions\}\}
      (\mu X \bullet detect ? work \longrightarrow \mathbf{var} \ colls1, \ colls2, \ colls3, \ colls4 : int \bullet)
          (recColls?x \longrightarrow (\mathbf{wait} 0..RC_{TB}; colls1 := x));
          (recColls?x \longrightarrow (\mathbf{wait} 0 .. RC_{TB}; colls2 := x));
          (recColls?x \longrightarrow (\mathbf{wait} 0 .. RC_{TB}; colls3 := x));
          (recColls? x \longrightarrow (\mathbf{wait} \ 0 .. RC_{TB}; \ colls4 := x))
      wait 0 \dots SCFP_{TB};
      SetCollisionsFromParts([colls1, colls2, colls3, colls4]);
      output ! collisions \longrightarrow \mathbf{skip} ; \ \mathit{sync} \longrightarrow X
     [\{currentFrame, state, voxel\_map, work, collisions\} \mid \{|sync|\} \mid \varnothing]
(\mu X \bullet \text{wait } FRAME\_PERIOD ; sync \longrightarrow X)
 \{| reduce, detect, output, recColls, sync \}
```

A binary version of the distribution law required above is given in the sequel; it was already presented as rec-sync in [3]. We note that we use a generalised version of this law that deals with n parallel branches.

## Circus Law 11 (lockstep-intro)

```
(\mu X \bullet (A_1 \llbracket ns_1 \mid cs \mid ns_2 \rrbracket A_2) \; ; \; X) \equiv \begin{pmatrix} (\mu X \bullet A_1 \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs \cup \{ \lceil Sync \rceil \} \mid ns_2 \rrbracket \end{pmatrix} \setminus \{ \lceil sync \rceil \} \\ (\mu X \bullet A_2 \; ; \; sync \longrightarrow X) \end{pmatrix} \setminus \{ \lceil sync \rceil \}
\mathbf{provided} \; \; sync \not\in usedC(A_1) \cup usedC(A_2) \; \; \mathbf{and} \; \; wrtV(A_1) \cap usedV(A_2) = \varnothing \; \; \mathbf{and} \; wrtV(A_2) \cap usedV(A_1) = \varnothing
```

We omit the presentation of the generalised law, but it is straight forward.

### **Introduce Handler Actions**

Local actions are now introduced for the seven handlers that have emerged.

```
InputFrameHandler \stackrel{\frown}{=}
               \begin{pmatrix} \mu X \bullet (next\_frame? frame \longrightarrow (\mathbf{wait} \ 0 \dots SF_{TB}; StoreFrame)) \blacktriangleleft INP\_DL; \\ reduce! currentFrame! state \longrightarrow \mathbf{skip}; sync \longrightarrow X \end{pmatrix}
ReducerHandler \stackrel{\frown}{=}
                   (\mu X \bullet reduce ? currentFrame ? state \longrightarrow 

wait 0 ... RPW_{TB} ; ReduceAndPartitionWork;

(detect ! work \longrightarrow \mathbf{skip} ; sync \longrightarrow X)
DetectorHandler1 =
                   (\mu X \bullet detect ? work \longrightarrow \mathbf{var} \ colls1 : int \bullet \mathbf{wait} \ 0 ... \ CPC_{TB};)

(\exists \ i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land \ i? = 1);

(\neg cColls! \ colls1 \longrightarrow \mathbf{skip}; \ output ? \ y \longrightarrow \mathbf{skip}; \ sync \longrightarrow X)
DetectorHandler2 \cong
                 \begin{pmatrix} \mu X \bullet detect? work \longrightarrow \mathbf{var} \ colls2: int \bullet \mathbf{wait} \ 0 \dots CPC_{TB}; \\ (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2); \\ recColls! \ colls2 \longrightarrow \mathbf{skip}; \ output?y \longrightarrow \mathbf{skip}; \ sync \longrightarrow X \end{pmatrix}
DetectorHandler3 =
                  \begin{pmatrix} \mu X \bullet detect? work \longrightarrow \mathbf{var} \ colls3: int \bullet \mathbf{wait} \ 0 \dots CPC_{TB}; \\ (\exists i?: \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3); \\ recColls! \ colls3 \longrightarrow \mathbf{skip}; \ output?y \longrightarrow \mathbf{skip}; \ sync \longrightarrow X \end{pmatrix} 
DetectorHandler4 =
                 \begin{pmatrix} \mu X \bullet detect? work \longrightarrow \mathbf{var} \ colls4: int \bullet \mathbf{wait} \ 0 \dots CPC_{TB}; \\ (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4); \\ recColls! \ colls4 \longrightarrow \mathbf{skip}; \ output?y \longrightarrow \mathbf{skip}; \ sync \longrightarrow X \end{pmatrix}
 OutputCollisionsHandler \stackrel{\frown}{=}
                   (\mu X \bullet output ? collisions \longrightarrow 

\mathbf{var}\ colls : int \bullet \mathbf{wait}\ 0 ...\ CC_{TB}\ ; \ Calc\ Collisions;

(output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL\ ; \ sync \longrightarrow
```

We also introduce a local action for the parallel fragment that controls handler execution.

 $InteractionHandlers \cong \begin{cases} \mu X \bullet detect? work \longrightarrow \mathbf{var} \ colls1, \ colls2, \ colls3, \ colls4: \ int \bullet \\ \left( (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB}; \ colls1:=x)); \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB}; \ colls2:=x)); \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB}; \ colls3:=x)); \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB}; \ colls4:=x)) \end{cases}, \\ \mathbf{wait} \ 0 \ldots SCFP_{TB}; \\ SetCollisionsFromParts([[\ colls1, \ colls2, \ colls3, \ colls4]]); \\ output! \ collisions \longrightarrow \mathbf{skip}; \ sync \longrightarrow X \end{cases}$ 

Lastly, the cycle time is captured by a further local action.

```
Cycle = (\mu X \bullet \mathbf{wait} \ FRAME\_PERIOD \ ; \ sync \longrightarrow X)
```

With this, the System action is written in the following manner to bring out the mission structure.

```
System =
                'Input Frame Handler
                      \llbracket \{\mathit{currentFrame}, \mathit{state}\} \mid \{ | \mathit{reduce}, \mathit{sync} \mid \} \mid \{ \mathit{voxel\_map}, \mathit{work} \} \rrbracket
                       [\{voxel\_map, work\} \mid \{|detect, sync|\} \mid \varnothing]
                 Detector Handler 1\\
                       \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
                 Detector Handler 2
                       \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
                 DetectorHandler3
                       \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
                Detector Handler 4
                       \llbracket \varnothing \mid \{ \mid output, sync \} \mid \varnothing \rrbracket
                 Output Collisions Handler \\
                   [[currentFrame, state, voxel\_map, work]]
                         \{|detect, output, recColls, sync|\} | \{collisions\}|\}
             Interaction Handlers
               [\{currentFrame, state, voxel\_map, work, collisions\} \mid \{\{sync\}\} \mid \emptyset]
          Cycle
            \{| reduce, detect, output, recColls, sync |\}
```

It exhibits the desired shape of a parallelism of handler actions, including two auxiliary actions: one that controls handler execution and another one for the cycle time. Auxiliary control actions are expected at this point, and their elimination is an issue for SH rather than MH. The *Cycle* action is logically attributed to *InputFrameHandler* and will eventually be collapsed with it; for the time being, however, we keep it as a separate parallel branch in order to facilitate the subsequent refinement in the SH phase.

To conclude the account on the MH models, we present the complete process that results from this phase. For this, we have to declare several channels that have been introduced during the course of refinement.

```
\begin{tabular}{ll} {\bf channel} \ reduce: RawFrame \times StateTable \\ {\bf channel} \ detect: Partition \\ {\bf channel} \ output, recColls: int \\ {\bf channel} \ sync \\ \end{tabular}
```

Whereas reduce, detect, output and sync are specification channels, recColls later becomes a method channel.

### 5.2.7 Process

The complete process for the MH phase is presented below. Its state and data operations are in fact the same as those of  $CDxE\_MH2B$ , the result of Stage 2.

```
system CDxE\_MH \stackrel{\frown}{=} \mathbf{begin}
\mathbf{state}\ \mathit{CDxMHState}\ ==\ \mathit{ECPStateCDx}
       Init.
         CDxMHState'
        currentFrame' = \mathbf{new} RawFrame
        state' = \mathbf{new} \, State Table
        voxel\_map' = \mathbf{new} \ HashMap[Vector2d, List[Motion]]
        work' = \mathbf{new} \, Partition(4)
         collisions' = 0
       StoreFrame
        \Delta CDxMHState
        frame?: Frame
        \exists posns, posns' : Frame; motions, motions' : Frame
                  dom\ posns = dom\ motions \land dom\ posns' = dom\ motions' \bullet
              posns' = frame? \land
              motions' =
                        \{a: \operatorname{dom} posns' \bullet a \mapsto \operatorname{if} a \in \operatorname{dom} posns \operatorname{then} (posns' a) -_V (posns a) \operatorname{else} ZeroV\} \land
              posns = F(currentFrame) \land motions = G(currentFrame, state) \land
              posns' = F(currentFrame') \land motions' = G(currentFrame', state')
       Reduce And Partition Work
        \Delta CDxMHState
        currentFrame' = currentFrame \land state' = state
        \exists posns : Frame; motions : Frame \mid dom posns = dom motions \bullet
              posns = F(currentFrame) \land motions = G(currentFrame, state) \land formula = for
                    \forall a_1, a_2 : Aircraft \mid \{a_1, a_2\} \subseteq \text{dom } posns \bullet
                             (a_1, a_2) \in CalcCollisionSet(posns, motions) \Rightarrow
                                         \exists l : List[Motion] \mid l \in voxel\_map' . values() . elems() \bullet
                                                     MkMotion(a_1, posns\ a_1 - V \ motions\ a_1, posns\ a_1) \in l.\ elems() \land
                                                      MkMotion(a_2, posns \ a_2 - V \ motions \ a_2, posns \ a_2) \in l \ . \ elems()
       CalcPartCollisions
        \Xi CDxMHState
        pcolls!:int
        i? : 1 . . 4
        pcolls! =
                              a_1 : Aircraft; \ a_2 : Aircraft \mid
                                            \exists \ l : List[Motion] \mid l \in work \ . \ getDetectorWork(i?). \ elems() ullet
                                            \exists v_1, v_2 : Vector; w_1, w_2 : Vector \bullet
                                                      MkMotion(a_1, v_1, w_1) \in l . elems() \land
                                                      MkMotion(a_2, v_2, w_2) \in l \cdot elems() \land
                                                       collide((v_1, w_1 - V v_1), (v_2, w_2 - V v_2))
```

```
Set Collisions From Parts
    \Delta CDxMHState
    collsbag?: bag int
    currentFrame' = currentFrame \land state' = state
    voxel\_map' = voxel\_map \land work' = work
    Calc Collisions
    \Xi CDxMHState
    colls! : \mathbb{N}
    \exists posns : Frame; motions : Frame \mid dom posns = dom motions \bullet
       posns = F(currentFrame) \land motions = G(currentFrame, state) \land
       \exists collset : \mathbb{F}(Aircraft \times Aircraft) \mid collset = CalcCollisionSet(posns, motions) \bullet
             (\# collset = 0 \land colls! = 0) \lor (\# collset > 0 \land colls! \ge (\# collset) \operatorname{div} 2)
InputFrameHandler \stackrel{\frown}{=}
      (\mu X \bullet (next\_frame? frame \longrightarrow (wait 0 ... SF_{TB}; StoreFrame)) \blacktriangleleft INP\_DL;)
      \setminus reduce ! currentFrame ! state \longrightarrow \mathbf{skip} ; sync \longrightarrow X
ReducerHandler \stackrel{\frown}{=}
       '\mu X \bullet reduce ? currentFrame ? state \longrightarrow
       wait 0 ... RPW_{TB}; ReduceAndPartitionWork; detect! work \longrightarrow skip; sync \longrightarrow X
DetectorHandler1 =
       \mu X \bullet detect? work \longrightarrow \mathbf{var} \ colls1: int \bullet \mathbf{wait} \ 0 \dots CPC_{TB};
        (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1);
       (recColls! colls1 \longrightarrow \mathbf{skip}; output?y \longrightarrow \mathbf{skip}; sync \longrightarrow X)
DetectorHandler2 =
       '\mu X \bullet detect ? work \longrightarrow \mathbf{var} \ colls2 : int \bullet \mathbf{wait} \ 0 \dots CPC_{TB};
       (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2);

(\forall i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2);

(\forall i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2);
DetectorHandler3 =
       \mu X \bullet detect ? work \longrightarrow \mathbf{var} \ colls3 : int \bullet \mathbf{wait} \ 0 \dots CPC_{TB};
        (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3);
      DetectorHandler4 =
       \mu X \bullet detect ? work \longrightarrow \mathbf{var} \ colls4 : int \bullet \mathbf{wait} \ 0 \dots CPC_{TB};
        (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4);
       (recColls! colls4 \longrightarrow \mathbf{skip}; output? y \longrightarrow \mathbf{skip}; sync \longrightarrow X)
OutputCollisionsHandler \stackrel{\frown}{=}
        \mu X \bullet output ? collisions \longrightarrow
        \mathbf{var}\ colls: int \bullet \mathbf{wait}\ 0...\ CC_{TB}\ ;\ Calc Collisions;
       (output\_collisions! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL; sync \longrightarrow X
```

```
Interaction Handlers \cong
         \mu X \bullet detect ? work \longrightarrow \mathbf{var} \ colls1, colls2, colls3, colls4 : int \bullet
             (recColls?x \longrightarrow (\mathbf{wait} 0 .. RC_{TB}; colls1 := x));
             (recColls?x \longrightarrow (\mathbf{wait} \ 0 ... RC_{TB}; \ colls2 := x));

(recColls?x \longrightarrow (\mathbf{wait} \ 0 ... RC_{TB}; \ colls3 := x));

(recColls?x \longrightarrow (\mathbf{wait} \ 0 ... RC_{TB}; \ colls4 := x));
          SetCollisionsFromParts([colls1, colls2, colls3, colls4]);
          output ! collisions \longrightarrow \mathbf{skip} ; sync \longrightarrow X
Cycle = (\mu X \bullet \mathbf{wait} \ FRAME\_PERIOD ; \ sync \longrightarrow X)
System =
                 Input Frame Handler
                       \llbracket \{\mathit{currentFrame}, \mathit{state}\} \mid \{ | \mathit{reduce}, \mathit{sync} \mid \} \mid \{ \mathit{voxel\_map}, \mathit{work} \} ]
                  (ReducerHandler
                       [\{voxel\_map, work\} \mid \{|detect, sync|\} \mid \varnothing]
                 (DetectorHandler1
                       \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
                 (Detector Handler 2
                       \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
                 (DetectorHandler3
                       [\varnothing \mid \{| detect, output, sync |\} \mid \varnothing ]
                 (Detector Handler 4)
                       \llbracket \varnothing \mid \{\mid output, sync \mid\} \mid \varnothing \rrbracket
                 OutputCollisionsHandler)))))
                   [[currentFrame, state, voxel\_map, work]]
                         \{|detect, output, recColls, sync|\} | \{collisions\}|\}
             Interaction Handlers \\
               [\{currentFrame, state, voxel\_map, work, collisions\} \mid \{\{sync\}\} \mid \emptyset]
            \{| reduce, detect, output, recColls, sync |\}
• Init; System
end
```

Parts of the process that could not be parsed due to limitations of the Circus parser in CZT are highlighted.

## 5.3 Phase SH

The SH phase is subdivided into four stages.

- 1. Encapsulate shared data of sequential handlers.
- 2. Encapsulate shared data of concurrent handlers.
- 3. Introduce data to realise control mechanisms.
- 4. Collect specification of the memory area data.

This subdivision refines the account in [5]. In this section, we discuss the refinement steps in each stage separately. For this, we require specialised high-level laws which are presented in Section 5.3.1, where we call them 'patterns'. We thus have Pattern 1 and Pattern 2 being used in Stage 1, Pattern 3 being used in Stage 2, and Pattern 4 being used in Stage 3. The refinement patterns are expected to be useful in other case studies too, and apply to action shapes emerging in Stage 4 and Stage 5 of the MH phase.

### 5.3.1 Patterns

In this section, we present several high-level patterns that are used in the refinements carried out during the SH phase. We also examine the proof of some of those patterns, or at least sketch out a proof strategy.

#### Pattern 1

The first pattern targets data passing between sequentially executed handlers. It is used in Stage 1 of the SH phase. The action structure we refine results from the application of the law seq-to-par-2 which is typically applied during Stage 4 of the MH phase. We recaptured the shape of this action below.

$$\begin{pmatrix}
(\mu X \bullet A_1 \; ; \; c \, ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\
\llbracket ns_1 \mid cs \mid ns_2 \rrbracket \\
(\mu X \bullet c \, ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X)
\end{pmatrix} \setminus \{ \mid c \mid \}$$

Our target for its refinement is the following action.

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c_1 \! ! \, x \longrightarrow \mathbf{skip} \; ; \; c_3 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid (cs \setminus \{\!\! \} c \, \}\!\! ) \cup \{\!\! \} c_3 \mid \!\! \} \mid ns_2 \rrbracket \\ (\mu X \bullet c_3 \longrightarrow \mathbf{skip} \; ; \; c_2 \! ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \cup ns_2 \mid \{\!\! \} c_1, c_2 \mid \!\! \} \mid \varnothing \rrbracket \\ \begin{pmatrix} \mathbf{var} \; v : \; T \bullet \\ \mu X \bullet \begin{pmatrix} (c_1 \, ? \, x \longrightarrow v := x) \; \Box \\ (c_2 \! ! \, v \longrightarrow \mathbf{skip}) \end{pmatrix} \; ; \; X \end{pmatrix}$$

where  $c_1$ ,  $c_2$  and  $c_3$  are fresh channels

Initially, the channel c fulfils a dual purpose of controlling execution and passing data between the parallel actions. These concerns are disentangled by the refinement: data sharing is realised by the typed channels  $c_1$  and  $c_2$ , and the typeless channel  $c_3$  establishes the (sequential) flow of execution. It is possible to deal with the control issue separately; for this, we merely aim for the following intermediate refinement.

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c_1 \! ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs \setminus \{ \! \mid c \! \mid \mid ns_2 \rrbracket \\ (\mu X \bullet c_2 \, ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \end{pmatrix} \\ \llbracket ns_1 \cup ns_2 \mid \{ \! \mid c_1, c_2, sync \! \mid \mid \varnothing \rrbracket \} \\ \begin{pmatrix} \mathbf{var} \; v : \; T \bullet \\ \mu X \bullet \begin{pmatrix} (c_1 \, ? \; x \longrightarrow v := x) \; \Box \\ (c_2 \, ! \; v \longrightarrow \mathbf{skip}) \end{pmatrix} \; ; \; X \end{pmatrix} \\ \llbracket \varnothing \mid \{ \! \mid c_1, c_2 \; \mid \mid \varnothing \rrbracket \} \\ (\mu X \bullet c_1 \, ? \; y \longrightarrow c_2 \, ? \; y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X \end{pmatrix}$$

**provided**  $c_1$  and  $c_2$  are fresh channels

We then, however, require further refinement steps that introduce the control mechanism via a new channel. Thus here, the pattern breaks down into two steps. The advantage of this approach is that we can account for different strategies for designing control. The disadvantage is that it is not clear (yet) to what extent the refinement of control can be automated, and how much guidance by the user is required for automation.

In Pattern 1, there seems moreover not a notable design space for realising the control aspect; hence, we opt for the solution that uses a single law that already embeds the control mechanism via the synchronisation

channel  $c_3$ . Intuitively, we can think of this channel in terms of (abstractly) modelling a software event.

# High-level Law 1 (seq-share-1)

$$\begin{pmatrix}
(\mu X \bullet A_{1} ; c! x \longrightarrow \mathbf{skip} ; sync \longrightarrow X) \\
[ns_{1} | cs | ns_{2}] \\
(\mu X \bullet c? x \longrightarrow A_{2} ; sync \longrightarrow X)
\end{pmatrix} \setminus \{ c \}$$

$$\equiv
\begin{pmatrix}
(\mu X \bullet A_{1} ; c_{1}! x \longrightarrow \mathbf{skip} ; c_{3} \longrightarrow \mathbf{skip} ; sync \longrightarrow X) \\
[ns_{1} | (cs \setminus \{ c \}) \cup \{ c_{3} \} | ns_{2}] \\
(\mu X \bullet c_{3} \longrightarrow \mathbf{skip} ; c_{2}? x \longrightarrow A_{2} ; sync \longrightarrow X)
\end{pmatrix} \setminus \{ c_{3} \}$$

$$\begin{bmatrix}
[ns_{1} \cup ns_{2} | \{ c_{1}, c_{2} \} | \varnothing] \\
(\mathbf{var} v : T \bullet \\
\mu X \bullet \begin{pmatrix}
(c_{1}? x \longrightarrow v := x) \square \\
(c_{2}! v \longrightarrow \mathbf{skip})
\end{pmatrix}; X
\end{pmatrix}$$

**provided**  $\{c, sync\} \subseteq cs \land c \notin usedC(A_1) \cup usedC(A_2) \text{ and } c_1, c_2 \text{ and } c_3 \text{ are fresh channels}$ 

The right-hand action of the resulting parallelism contributes directly to the MArea action. It is worth to examine the proof of this law in more detail as it reveals some common and recurring themes.

### **Proof of Pattern 1**

The proof is done by transforming the left-hand action of the law into the right-hand action using (mostly) elementary refinement laws. We start with the left-hand side of the law.

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c \, ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs \mid ns_2 \rrbracket \\ (\mu X \bullet c \, ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \end{pmatrix} \setminus \{ \mid c \mid \}$$

$$\mathbf{where} \; \{ \mid c, sync \mid \} \subseteq cs \land c \not\in usedC(A_1) \cup usedC(A_2) \}$$

The first step replaces the channel c by two channels  $c_1$  and  $c_2$ . We require a specialised law for this.

### Circus Law 12 (replace-sync-chan-seq)

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c \! ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs \mid ns_2 \rrbracket \\ (\mu X \bullet c \; ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \end{pmatrix} \setminus \{\!\! \mid c \!\! \}$$

$$\equiv$$

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c_1 \! ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs \setminus \{\!\! \mid c \!\! \mid \mid ns_2 \!\! \rfloor \\ (\mu X \bullet c_2 \; ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \end{pmatrix} \setminus \{\!\! \mid c_1, c_2 \!\! \mid \}$$

$$\llbracket ns_1 \cup ns_2 \mid \{\!\! \mid c_1, c_2, sync \!\! \mid \mid \varnothing \rfloor \} \\ (\mu X \bullet c_1 \; ? \, x \longrightarrow c_2 \! ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix}$$

$$\mathsf{provided} \; \{\!\! \mid c, sync \!\! \mid \} \subseteq cs \land c \not\in usedC(A_1) \cup usedC(A_2) \; \mathbf{and} \; c_1 \; \mathbf{and} \; c_2 \; \mathbf{are} \; \mathbf{fresh} \; \mathbf{channels}$$

Strictly, synchronisation on sync is not necessary in the right-hand parallel action. However, including it turns out to simplify subsequent refinement steps. In particular, when the right-hand control fragment is later on decomposed and collapsed with other actions the presence of sync is useful.

After applying the law we, obtain the following result.

 $\ldots \equiv$  "application of the law replace-sync-chan-seq"

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c_1 \! ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs' \mid ns_2 \rrbracket \\ (\mu X \bullet c_2 ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \cup ns_2 \mid \{ \mid c_1, c_2, sync \mid \} \mid \varnothing \rrbracket \\ (\mu X \bullet c_1 ? \, x \longrightarrow c_2 ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix} \setminus \{ \mid c_1, c_2 \mid \}$$

where  $cs' = cs \setminus \{ c \}$  and  $c_1$  and  $c_2$  are fresh channels

We observe that the left-hand action of the outer parallelism already has the correct shape for the *intermediate* target. We therefore focus on the right-hand action. Basically, we want to bring it into a form that resembles the desired shape for the MArea action. The strategy for this is to introduce a parallelism in which one action becomes the significant part of MArea and the other action a residual part that exercises control.

Before performing this parallelisation, we encapsulate the shared data in a local variable by virtue of four basic laws var-intro, extract-var-prefix, extract-var-seq and extract-var-rec listed in Appendix B.1. This permits the following refinement steps, applied to the left-hand action of the parallelism.

```
\mu X \bullet c_1 ? x \longrightarrow c_2 ! x \longrightarrow \mathbf{skip} ; sync \longrightarrow X
```

 $\equiv$  "application of the law var-intro"

$$\mu X \bullet c_1 ? x \longrightarrow (\mathbf{var} \ v : T \bullet v := x \ ; \ c_2 ! v \longrightarrow \mathbf{skip}) \ ; \ sync \longrightarrow X$$

 $\equiv$  "application of the law extract-var-prefix"

$$\mu X \bullet (\mathbf{var} \ v : T \bullet c_1 ? x \longrightarrow v := x ; c_2 ! v \longrightarrow \mathbf{skip}) ; sync \longrightarrow X)$$

 $\equiv$  "application of the law extract-var-seq"

$$\mu X \bullet (\mathbf{var} \ v : T \bullet c_1 ? x \longrightarrow v := x \ ; \ c_2 ! v \longrightarrow \mathbf{skip} \ ; \ \mathit{sync} \longrightarrow X)$$

 $\equiv$  "application of the law extract-var-rec"

$$\operatorname{var} v : T \bullet \mu X \bullet (c_1 ? x \longrightarrow v := x ; c_2 ! v \longrightarrow \operatorname{skip} ; sync \longrightarrow X)$$

We next refine the body of the recursion introducing the aforementioned parallelism. The slightly more specific laws required for this are listed below; others are included in Appendix B.1.

The following three laws are variations of distributing a prefix into a parallelism (Law A.24 in [4]).

Circus Law 17 (distr-prefix-par-3)

$$c \longrightarrow \mathbf{skip}$$
;  $(\mathbf{skip} \llbracket ns_1 \mid cs \mid ns_2 \rrbracket A) \equiv \mathbf{skip} \llbracket ns_1 \mid cs \mid ns_2 \rrbracket (c \longrightarrow A)$  provided  $c \notin cs$ 

Circus Law 18 (distr-prefix-par-4)

$$c! x \longrightarrow \mathbf{skip}$$
;  $(A_1 \llbracket ns_1 \mid cs \mid ns_2 \rrbracket A_2) \equiv (c! x \longrightarrow \mathbf{skip}; A_1) \llbracket ns_1 \mid cs \mid ns_2 \rrbracket (c? y \longrightarrow A_2)$   
**provided**  $c \in cs$  and  $y$  is not free in  $A_2$ 

Circus Law 19 (distr-prefix-par-5)

$$c ? x \longrightarrow v := x ; (A_1 \llbracket ns_1 \mid cs \mid ns_2 \rrbracket A_2) \equiv$$

$$(c ? x \longrightarrow v := x ; A_1) \llbracket ns_1 \cup \{v\} \mid cs \mid ns_2 \rrbracket (c ? y \longrightarrow A_2)$$
**provided**  $c \in cs$  **and**  $v$  and  $y$  are not free in  $A_2$ 

The following law is important for the steps that establish the recursive shape of MArea.

Circus Law 20 (extchoice-par-intro)

$$\begin{array}{l} ((c \longrightarrow A_1) \; ; \; A_2) \; \llbracket \; ns_1 \; | \; cs \; | \; ns_2 \; \rrbracket \; (c \longrightarrow A_3) \; \equiv \\ (((c \longrightarrow A_1) \; \Box \; (c_1 \longrightarrow B_1) \; \Box \; \ldots \; \Box \; (c_n \longrightarrow B_n)) \; ; \; A_2) \; \llbracket \; ns_1 \; | \; cs \; | \; ns_2 \; \rrbracket \; (c \longrightarrow A_3) \end{array}$$

**provided**  $c \in cs$  and c is distinct from all  $c_i$  (the  $B_i$  can be chosen arbitrarily)

With the above laws we can proceed with the proof as follows.

$$\operatorname{var} v: T \bullet \mu X \bullet (c_1? x \longrightarrow v := x; c_2! v \longrightarrow \operatorname{skip}; sync \longrightarrow X)$$

≡ "application of the laws seq-skip-left-intro and distr-prefix-seq"

$$\operatorname{var} v : T \bullet \mu X \bullet (c_1 ? x \longrightarrow v := x ; c_2 ! v \longrightarrow \operatorname{skip} ; sync \longrightarrow \operatorname{skip}) ; X$$

≡ "application of the laws seq-skip-left-intro and par-skip-intro"

$$\mathbf{var}\ v:\ T\bullet\mu X\bullet\left(\begin{matrix}c_1?\ x\longrightarrow v:=x\ ;\ c_2!\ v\longrightarrow \mathbf{skip}\ ;\ sync\longrightarrow \mathbf{skip};\\ \left(\mathbf{skip}\ \llbracket\ \varnothing\ |\ \varnothing\ \|\ \mathbf{skip}\right)\end{matrix}\right);\ X$$

≡ "application of the law extend-sync-par"

$$\mathbf{var}\,v:\,T\bullet\mu X\bullet\left(\begin{matrix}c_1\,?\,x\longrightarrow v:=x\;;\;\;c_2\,!\,v\longrightarrow\mathbf{skip}\;;\;\;sync\longrightarrow\mathbf{skip};\\\left(\mathbf{skip}\,\llbracket\,\varnothing\mid\left\{\,\begin{matrix}c_1,c_2\,\end{smallmatrix}\right\}\mid\varnothing\,\rrbracket\,\mathbf{skip}\right)\end{matrix}\right)\;;\;\;X$$

≡ "application of the law distr-prefix-par-3"

$$\mathbf{var}\,v:\,T\bullet\mu X\bullet\begin{pmatrix}c_1\,?\,x\longrightarrow v:=x\;;\;\;c_2\,!\,v\longrightarrow\mathbf{skip};\\\mathbf{skip}\\\mathbb{[}\varnothing\mid\{\!\mid c_1,c_2\,\}\!\mid\varnothing]\!\end{pmatrix};\;\;X$$
$$\underbrace{sync\longrightarrow\mathbf{skip}}$$

≡ "application of the law distr-prefix-par-4 and eliminating sequence with skip"

$$\mathbf{var}\,v:T\bullet\mu X\bullet\begin{pmatrix}c_1?x\longrightarrow v:=x;\\ \left(\begin{matrix}c_2!v\longrightarrow\mathbf{skip}\end{matrix}\right)\\ \llbracket\varnothing\mid\{\!\!\{c_1,c_2\}\!\!\}\mid\varnothing\rrbracket\\ \left(\begin{matrix}c_2?y\longrightarrow sync\longrightarrow\mathbf{skip}\end{matrix}\right)\end{pmatrix};\;X$$

≡ "application of the laws extchoice-par-intro and extchoice-comm"

$$\mathbf{var} \ v : T \bullet \mu X \bullet \begin{pmatrix} c_1 ? x \longrightarrow v := x; \\ \left( \begin{matrix} (c_1 ? x \longrightarrow v := x \square c_2 ! v \longrightarrow \mathbf{skip}) \\ \llbracket \varnothing \mid \{ c_1, c_2 \} \mid \varnothing \rrbracket \\ \left( c_2 ? y \longrightarrow \mathit{sync} \longrightarrow \mathbf{skip} \right) \end{pmatrix} \right); \ X$$

= "application of the law distr-prefix-par-5"

$$\mathbf{var}\,v:\,T\bullet\mu X\bullet\left(\begin{pmatrix}\left(c_1\,?\,x\longrightarrow v:=x\right);\\\left(c_1\,?\,x\longrightarrow v:=x\;\square\;c_2\,!\,v\longrightarrow\mathbf{skip}\right)\right)\\ \llbracket\{v\}\mid \{\!\!\mid c_1,c_2\,\}\!\!\mid \varnothing\rrbracket\\ \left(c_1\,?\,y\longrightarrow c_2\,?\,y\longrightarrow sync\longrightarrow\mathbf{skip}\right)\end{pmatrix}\right);\,\,X$$

≡ "application of the laws extchoice-par-intro and extchoice-comm"

$$\mathbf{var}\,v:T\bullet\mu X\bullet\begin{pmatrix} \left((c_1\,?\,x\longrightarrow v:=x\,\square\,c_2\,!\,v\longrightarrow\mathbf{skip});\\ (c_1\,?\,x\longrightarrow v:=x\,\square\,c_2\,!\,v\longrightarrow\mathbf{skip}) \\ \llbracket\{v\}\mid\{\!\mid c_1,c_2\,\}\!\mid\varnothing\rrbracket\\ (c_1\,?\,y\longrightarrow c_2\,?\,y\longrightarrow\mathit{sync}\longrightarrow\mathbf{skip}) \right);X$$

We now use a specialised law to distribute the recursion into the parallelism. This law is somewhat similar to rec-sync in [2], however lock-step progress is achieve by a synchronisation at the start of the recursion rather than the end. We first recapture rec-sync in [2], used later on too, and name it distr-rec-par-1.

# Circus Law 25 (distr-rec-par-1)

$$\mu X \bullet (A_1 \llbracket ns_1 \mid cs \mid ns_2 \rrbracket A_2) \; ; \; c \longrightarrow X \equiv$$

$$(\mu X \bullet A_1 \; ; \; c \longrightarrow X) \llbracket ns_1 \mid cs \mid ns_2 \rrbracket (\mu X \bullet A_2 \; ; \; c \longrightarrow X)$$
**provided**  $c \in cs$  **and**  $c \notin usedC(A_1) \cup usedC(A_2)$  **and**

$$wrtV(A_1) \cap usedV(A_2) = \varnothing \text{ and } wrtV(A_2) \cap usedV(A_1)$$

We next give the alternative version that we require for the transformation in the sequel.

# Circus Law 26 (distr-rec-par-2)

$$\mu X \bullet (((c_1 \longrightarrow A_1 \square c_2 \longrightarrow A_2) \; ; \; A_3) \; \llbracket \; ns_1 \mid cs \mid ns_2 \; \rrbracket \; (c_1 \longrightarrow A_4)) \; ; \; X \equiv \\ (\mu X \bullet (c_1 \longrightarrow A_1 \square c_2 \longrightarrow A_2) \; ; \; A_3 \; ; \; X) \; \llbracket \; ns_1 \mid cs \mid ns_2 \; \rrbracket \; (\mu X \bullet c_1 \longrightarrow A_4 \longrightarrow X) \\ \textbf{provided} \; \{c_1, c_2\} \subseteq cs \; \textbf{ and } \; c_1 \not \in usedC(A_i) \; \text{for all } i \in \{1, 2, 3, 4\} \; \textbf{ and} \\ (wrtV(A_1) \cup wrtV(A_2) \cup wrtV(A_3)) \cap usedV(A_4) = \varnothing \; \textbf{ and} \\ wrtV(A_4) \cap (usedV(A_1) \cup usedV(A_2) \cup usedV(A_3)) = \varnothing$$

The external choice in the left-hand branch of the parallel action is another elaboration we require to use the law. It is not a problem as it is a Hobson's choice in the context of the right-hand parallel action.

We thus obtain the following refinement.

 $\equiv$  "application of the law distr-rec-par-2" Problem: What about the provisos  $c_1 \not\in usedC(A_3)$ ?

$$\mathbf{var} \ v : \ T \bullet \begin{pmatrix} \left( \begin{matrix} \mu X \\ \mu X \end{matrix} \bullet \begin{pmatrix} (c_1 ? x \longrightarrow v := x \square c_2 ! v \longrightarrow \mathbf{skip}); \\ (c_1 ? x \longrightarrow v := x \square c_2 ! v \longrightarrow \mathbf{skip}) \end{matrix} \right); \\ \llbracket \{v\} \mid \{\!\! \mid c_1, c_2 \, \}\!\! \mid \varnothing \rrbracket \\ \left( \begin{matrix} \mu X \end{matrix} \bullet c_1 ? y \longrightarrow c_2 ? y \longrightarrow sync \longrightarrow X \end{matrix} \right) \end{pmatrix}$$

The application of extchoice-par-intro now reveals its purpose of putting the left-hand action of the parallelism into a form  $\mu X \bullet A$ ; A; X. The following law simplifies it eliminating repeated actions A.

## Circus Law 27 (elim-repeated-seq-rec)

$$\mu X \bullet A$$
;  $A$ ; ...;  $A$ ;  $X \equiv \mu X \bullet A$ ;  $X$ 

We thus obtain the action below in the next refinement step.

= "application of the law elim-repeated-seg-rec"

$$\mathbf{var}\,v:\,T\bullet\begin{pmatrix} \left(\mu X\bullet\begin{pmatrix} (c_1\,?\,x\longrightarrow v:=x)\;\;\square\\ (c_2\,!\,v\longrightarrow \mathbf{skip}) \end{pmatrix};\;\;X\right)\\ \llbracket\{v\}\mid \{\!\mid c_1,c_2\,\}\!\mid\varnothing\rrbracket\\ \left(\mu X\bullet\;c_1\,?\,y\longrightarrow c_2\,?\,y\longrightarrow sync\longrightarrow X\right) \end{pmatrix}$$

The only remaining task is to distribute the local variable block into the left-hand action of the parallelism. This is achieved by the basic laws distr-var-par and var-elim given in Appendix B.1. This produces the two parallel fragments of the *intermediate* result presented at the beginning of the section.

= "application of the laws distr-var-par and var-elim and adjusting write sets of the parallelism"

$$\begin{pmatrix} \mathbf{var} \ v : T \bullet \\ \mu X \bullet \begin{pmatrix} (c_1 ? x \longrightarrow v := x) \ \Box \\ (c_2 ! v \longrightarrow \mathbf{skip}) \end{pmatrix}; X \end{pmatrix}$$

$$\llbracket \varnothing \mid \{ c_1, c_2 \} \mid \varnothing \rrbracket \\ (\mu X \bullet c_1 ? y \longrightarrow c_2 ? y \longrightarrow sync \longrightarrow X) \end{pmatrix}$$

The left-hand action of the parallelism now has the desired shape for *MArea*. The right-hand action encapsulates control of execution, however, without any concerns for shared data. This part needs to be further refined by introducing a basic channel to establish the necessary control between the two handlers. Therefore, we ignore the left-hand action for now and continue refining the right-hand action in combination with the parallelism of handlers. The proof tactic as is follows.

- 1. Introduce a fresh hidden channel  $c_3$  in the control branch and extend its scope.
- 2. Decompose the control branch into a parallelism of smaller fragments.
- 3. Match and collapse these parallel fragments suitably with the handlers.

These steps are fairly straight-forward and do not require specialised laws, apart from step laws to introduce and collapse parallelism. More importantly, they reveal a general strategy for eliminating control fragments. Below we recapture the current (intermediate) result of the refinement steps so far.

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c_1 \, ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs' \mid ns_2 \rrbracket \\ (\mu X \bullet c_2 \, ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \cup ns_2 \mid \{ \; c_1, \, c_2, \, sync \; \} \mid \varnothing \rrbracket \\ \begin{pmatrix} \mathbf{var} \; v : \; T \bullet \\ \mu X \bullet \begin{pmatrix} (c_1 \, ? \, x \longrightarrow v := x) \; \Box \\ (c_2 \, ! \; v \longrightarrow \mathbf{skip}) \end{pmatrix} ; \; X \end{pmatrix} \\ \llbracket \varnothing \mid \{ \; c_1, \, c_2 \; \} \mid \varnothing \rrbracket \\ (\mu X \bullet c_1 \, ? \, y \longrightarrow c_2 \, ? \, y \longrightarrow sync \longrightarrow X) \end{pmatrix}$$
 where  $\{ \; c, \, sync \; \} \subseteq cs \land c \not\in usedC(A_1) \cup usedC(A_2) \; \text{ and } \end{cases}$ 

The focus for the remaining part of the proof is the fragment below which we extract from this action.

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c_1 \, ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \; | \; cs' \; | \; ns_2 \rrbracket \\ (\mu X \bullet c_2 \, ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \cup ns_2 \; | \; \{ c_1, c_2, sync \; \} \; | \; \varnothing \rrbracket \\ (\mu X \bullet c_1 \, ? \, y \longrightarrow c_2 \, ? \, y \longrightarrow sync \longrightarrow X) \end{pmatrix}$$

 $cs' = cs \setminus \{ c \}$  and  $c_1$  and  $c_2$  are fresh channels

Thus, we ignore the middle action which already converged into the desirable shape for MArea.

We first introduce the typeless control channel  $c_3$ .

 $\dots \equiv$  "introduction of a hidden communication on a new channel  $c_3$  and extracting its hiding"

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c_1 \! ! \; x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs' \mid ns_2 \rrbracket \\ (\mu X \bullet c_2 ? \; x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \cup ns_2 \mid \{ c_1, c_2, sync \mid \varnothing \rrbracket \\ (\mu X \bullet c_1 ? \; y \longrightarrow c_3 \longrightarrow c_2 ? \; y \longrightarrow sync \longrightarrow X) \end{pmatrix} \setminus \{ c_3 \}$$

The derivation might use the following law to introduce the prefix at the right place.

# Circus Law 29 (hidden-sync-intro)

$$A \equiv (c \longrightarrow A) \setminus \{ c \} \text{ provided } c \notin usedC(A)$$

The remaining steps merely extract the concealment of  $c_3$  from the prefixes, recursion and parallelism using the elementary laws extract-hide-prefix, extract-hide-rec and extract-hide-par-right in Appendix B.1.

We once again use (custom) step laws to introduce a parallelism inside the right-hand recursion with the objective of splitting the recursion into a parallelism of two recursions of which each one is collapsed with one of the handlers. Below we just give the result after introducing the parallelism.

□ "introduction of parallelism using (custom) parallel step laws"

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c_1 \! ! \; x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs' \mid ns_2 \rrbracket \\ (\mu X \bullet c_2 ? \; x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \cup ns_2 \mid \{ c_1, c_2, sync \} \mid \varnothing \rrbracket \\ \begin{pmatrix} (c_1 ? \; y \longrightarrow \mathbf{skip} \; ; \; c_3 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket \varnothing \mid \{ c_3 \} \mid \varnothing \rrbracket \\ (c_3 \longrightarrow \mathbf{skip} \; ; \; c_2 ? \; y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix} \end{pmatrix}$$

⊑ "application of the law distr-rec-par-1 to distribution the recursion into the parallelism"

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c_1 \! ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs' \mid ns_2 \rrbracket \\ (\mu X \bullet c_2 ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \cup ns_2 \mid \{ c_1, c_2, sync \} \mid \varnothing \rrbracket \\ (\mu X \bullet c_1 ? \, y \longrightarrow \mathbf{skip} \; ; \; c_3 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket \varnothing \mid \{ c_3 \} \mid \varnothing \rrbracket \\ (\mu X \bullet c_3 \longrightarrow \mathbf{skip} \; ; \; c_2 ? \, y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix}$$

 $\sqsubseteq$  "reordering parallel actions matching them with a control fragment"

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c_1 \! ! x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid \{ \} c_1, sync \; \} \mid \varnothing \rrbracket \\ (\mu X \bullet c_1 ? y \longrightarrow \mathbf{skip} \; ; \; c_3 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix} \\ \llbracket ns_1 \mid cs' \cup \{ \} c_3 \; \} \mid ns_2 \rrbracket \\ \begin{pmatrix} (\mu X \bullet c_2 ? x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \\ \llbracket ns_2 \mid \{ \} c_2, sync \; \} \mid \varnothing \rrbracket \\ (\mu X \bullet c_3 \longrightarrow \mathbf{skip} \; ; \; c_2 ? y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix}$$

⊑ "extracting recursions from both parallelisms (symmetric law of distr-rec-par-1)"

$$\left( \begin{pmatrix} \mu X \bullet \begin{pmatrix} (A_1 \; ; \; c_1 \! ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid \{ c_1, sync \} \mid \varnothing \rrbracket \\ (c_1 ? \, y \longrightarrow \mathbf{skip} \; ; \; c_3 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix} \right)$$

$$\left[ ns_1 \mid cs' \cup \{ c_3 \} \mid ns_2 \rrbracket \\ \left( c_2 ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \\ \llbracket ns_2 \mid \{ c_2, sync \} \mid \varnothing \rrbracket \\ (c_3 \longrightarrow \mathbf{skip} \; ; \; c_2 ? \, y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix} \right)$$

 $\sqsubseteq$  "collapsing of parallel actions using step laws; this exploits that  $c_1$ ,  $c_2$  and  $c_3$  are fresh"

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c_1 \, ! \, x \longrightarrow \mathbf{skip} \; ; \; c_3 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs' \cup \{ \mid c_3 \mid \} \mid ns_2 \rrbracket \\ (\mu X \bullet c_3 \longrightarrow \mathbf{skip} \; ; \; c_2 \, ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \end{pmatrix} \setminus \{ \mid c_3 \mid \}$$

Injecting the result back into the context of the refined action.

$$\begin{pmatrix} \left(\mu X \bullet A_1 \; ; \; c_1 \, ! \, x \longrightarrow \mathbf{skip} \; ; \; c_3 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X\right) \\ \llbracket ns_1 \mid cs' \cup \llbracket \; c_3 \; \rrbracket \; \mid ns_2 \rrbracket \\ \left(\mu X \bullet c_3 \longrightarrow \mathbf{skip} \; ; \; c_2 \, ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X\right) \\ \llbracket ns_1 \cup ns_2 \mid \llbracket \; c_1, c_2 \; \rrbracket \; \mid \varnothing \rrbracket \\ \begin{pmatrix} \mathbf{var} \; v : \; T \bullet \\ \mu X \bullet \begin{pmatrix} (c_1 \, ? \, x \longrightarrow v := x) \; \Box \\ (c_2 \, ! \, v \longrightarrow \mathbf{skip}) \end{pmatrix} \; ; \; X \end{pmatrix}$$

where  $cs' = cs \setminus \{ c \}$  and  $c_1$  and  $c_2$  are fresh channels

This is exactly the right-hand side of the law and thus concludes the proof. We next look at a generalisation of this law that furthermore turns out to be required in our case study.

### Pattern 2

In our case study, we may anticipate to apply Pattern 1 three times, mirroring three applications of seq-to-par-2 in Stage 4 of the MH phase. It turns out though that seq-share-1 is too specific to be applied in all three cases where data is passed between sequential handlers. This is due to inhomogeneities introduced in Stage 5, more specifically during the merge sub-step concluding that stage. We consider the fragment

from the *System* action. The data here is transmitted through the *detect* channel, however, with *four* synchronising actions at the receiving end. Also the synchronisations on *output* within the detection handlers are an issue. The sender in this case is the control action *InteractionHandlers*, omitted above.

This highlights the need for further laws to introduce sharing in sequential handler actions. A generalised version of seq-share-2 accounts for a possible parallelism of handlers concurrently reading the data.

# High-level Law 2 (seq-share-2)

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c \! ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs_1 \mid ns_2 \cup ns_3 \cup \ldots \cup ns_n \rrbracket \\ (\mu X \bullet c \; ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \\ \llbracket ns_2 \mid cs_2 \mid ns_3 \cup ns_4 \cup \ldots \cup ns_n \rrbracket \\ (\mu X \bullet c \; ? \, x \longrightarrow A_3 \; ; \; sync \longrightarrow X) \\ \ldots \\ \llbracket ns_{n-1} \mid cs_{n-1} \mid ns_n \rrbracket \\ (\mu X \bullet c \; ? \, x \longrightarrow A_n \; ; \; sync \longrightarrow X) \end{pmatrix} \setminus \{ \mid c \mid \}$$

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c_1 \! ! \, x \longrightarrow \mathbf{skip} \; ; \; c_3 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid (cs_1 \setminus \{\!\!\mid c \,\!\mid\!\}) \cup \{\!\!\mid c_3 \,\!\mid\!\} \mid ns_2 \cup ns_3 \cup \ldots \cup ns_n \rfloor \\ (\mu X \bullet c_3 \longrightarrow \mathbf{skip} \; ; \; c_2 \, ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \\ \llbracket ns_2 \mid (cs_2 \setminus \{\!\!\mid c \,\!\mid\!\}) \cup \{\!\!\mid c_3 \,\!\mid\!\} \mid ns_3 \cup ns_4 \cup \ldots \cup ns_n \rfloor \\ (\mu X \bullet c_3 \longrightarrow \mathbf{skip} \; ; \; c_2 \, ? \, x \longrightarrow A_3 \; ; \; sync \longrightarrow X) \\ \cdots \\ \llbracket ns_{n-1} \mid (cs_{n-1} \setminus \{\!\!\mid c \,\!\mid\!\}) \cup \{\!\!\mid c_3 \,\!\mid\!\} \mid ns_n \rfloor \\ (\mu X \bullet c_3 \longrightarrow \mathbf{skip} \; ; \; c_2 \, ? \, x \longrightarrow A_n \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \cup ns_2 \cup \ldots \cup ns_n \mid \{\!\!\mid c_1, c_2 \,\!\mid\!\} \mid \varnothing \rfloor \\ \begin{pmatrix} \mathbf{var} \; v : \; T \bullet \\ \mu X \bullet \begin{pmatrix} (c_1 \, ? \; x \longrightarrow v := x) \ \Box \\ (c_2 \, ! \; v \longrightarrow \mathbf{skip}) \end{pmatrix} \; ; \; X \end{pmatrix}$$

**provided**  $\{c, sync\} \subseteq cs_i \land c \notin usedC(A_i) \text{ for } i \in 1...n \text{ and } c_1, c_2 \text{ and } c_3 \text{ are fresh channels}$ 

A detailed proof of this law is omitted but is expected to be very similar to the one for seq-share-1, up to the point where we match and collapse the control fragment with the handlers actions.

Below, we recapture the control fragment that is collapsed in the proof of the law seq-share-1.

$$\begin{pmatrix} (\mu X \bullet c_1 ? y \longrightarrow \mathbf{skip} ; c_3 \longrightarrow \mathbf{skip} ; sync \longrightarrow X) \\ \llbracket \varnothing \mid \{\!\! \mid c_3 \, \}\!\! \mid \varnothing \rrbracket \\ (\mu X \bullet c_3 \longrightarrow \mathbf{skip} ; c_2 ? y \longrightarrow \mathbf{skip} ; sync \longrightarrow X) \end{pmatrix}$$

In the proof of seq-share-2, we require an additional step that duplicates the right-hand action using idempotency of parallel composition. The respective law is.

# Circus Law 34 (idem-par)

```
A \equiv (A \parallel \varnothing \mid usedC(A) \mid \varnothing \parallel A) provided wrtV(A) = \varnothing and A is deterministic
```

Using the law, we refine the control fragment before matching and collapsing it with the handlers.

 $\dots \equiv$  "multiple applications of the law idem-par"

Hereafter we proceed in essence in the same way as in the proof of seq-share-1, reordering the parallelisms to match each control action with a handler action and use step laws to collapse those parallel actions.

### Pattern 3

The third pattern targets the refinement of the control action *InteractionHandlers*. This will become an issue for Stage 2 of the SH phase where we encapsulate shared data that is concurrently accessed.

 $InteractionHandlers \triangleq \begin{cases} \mu X \bullet detect? work \longrightarrow \mathbf{var} \ colls1, \ colls2, \ colls3, \ colls4: \ int \bullet \\ \left( (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB}; \ colls1:= x)); \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB}; \ colls2:= x)); \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB}; \ colls3:= x)); \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB}; \ colls4:= x)) \end{cases}$   $\mathbf{wait} \ 0 \ldots SCFP_{TB}; \\ SetCollisionsFromParts([[ \ colls1, \ colls2, \ colls3, \ colls4]]); \\ output! \ collisions \longrightarrow \mathbf{skip}; \ sync \longrightarrow X \end{cases}$ 

This action emerges as a residual control fragment during the refinement in Stage 5 of the MH phase. Rather than defining a law that applies to actions of the above shape, we formulate a more general law that is more likely to be reusable. It besides abstracts from the actual number of parallel handlers.

## High-level Law 3 (par-share)

```
 \begin{pmatrix} \mathbf{var} \ v : T \bullet \\ \mu X \bullet \ start \longrightarrow \mathbf{wait} \ 0 \dots Init_{TB} \ ; \ InitOp; \\ \mathbf{var} \ x_1, x_2, \dots, x_n : T \bullet \\ (record? x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB} \ ; \ x_1 := x)); \\ (record? x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB} \ ; \ x_2 := x)); \\ \dots \\ (record? x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB} \ ; \ x_n := x)); \\ \mathbf{wait} \ 0 \dots Merge_{TB} \ ; \ MergeOp([[x_1, x_2, \dots, x_n]]); \\ output! \ v \longrightarrow \mathbf{skip} \ ; \ sync \longrightarrow X   \sqsubseteq   \begin{pmatrix} \mathbf{var} \ v : T \bullet \\ \mu X \bullet (\mathbf{var} \ v : T \bullet (\mathbf{vait} \ 0 \dots RC_{TB} \ ; \ MergeOp([[x]])) \ \Box \\ record? \ x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB} \ ; \ MergeOp([[x]])) \ \Box \\ record? \ x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB} \ ; \ MergeOp([[x]])) \ \Box \\ (record? \ y \longrightarrow \mathbf{skip}) \ \| \\ (record? \ y
```

**provided** InitOp and MergeOp are data operations and  $wrtV(InitOp) = \{v\} = wrtV(MergeOp)$  and  $MergeOp(b_1 \uplus b_2) = MergeOp(b_1)$ ;  $MergeOp(b_2)$ 

We observe that the time budget  $\mathbf{wait} \ 0 \dots Merge_{TB}$  is removed by the law, assuming that the time budget  $\mathbf{wait} \ 0 \dots RC_{TB}$  already subsumes the time require for the merge operation. This models a program design in which the merge is done incrementally with each call of a method that records a partial result.

The law assumes that the merge operation (SetCollisionsFromParts in the  $\mathsf{CD}_x$ ) is can be expressed in terms of a sequence InitOp; MergeOp. For the  $\mathsf{CD}_x$  refinement, the respective decomposition is as follows.

The necessary proof is to show that SetCollisionsFromParts(cb) = InitOp; MergeOp(cb). This is not difficult by eliminating the schema sequence using the one-point rule. The reason we require manual decomposition prior to applying the law is that it seems not possible to derive the initialisation and step-wise merge operation automatically, for instance, from SetCollisionsFromParts. Thus, this transformation has to be done by the user but in practical terms this should in most cases not be difficult.

The left-hand action of the result of the law contributes to *MArea*. Channels may be renamed and further decomposed during the AR phase into *Call* and *Ret* pairs to correspond to methods in the program. The right-hand action is a control fragment that needs to be decomposed and distributed into the parallelism of handlers. Again, there is an issue of control versus sharing. The law in this case does not attempt to commit to a particular control mechanism but merely designs access to the shared data. The strategy for eliminating the control action is exactly as illustrated in the proof of the law seq-share-1.

The proof of par-share shall not be discussed in detail here. This is future work for the time being but one may expect similar themes to emerge as in the proofs of the previous laws for refinement patterns.

## Discussion

The difference between seq-share-1 / seq-share-2 and par-share is that the control action is implicitly eliminated in the former laws whereas in the latter law it persists. We could potentially define a more high-level version of par-share that takes the context of the refined action into account and aggregates the elimination of the residual parallelism. But on the other hand, this might restrict applicability of the law in the general case.

The important conclusion we draw is that for modularity, we require a general tactic, preferably automated, to eliminate control actions. There seem three obvious approaches for this.

- 1. Define sufficiently high-level laws that do not give rise to such actions in the first place.
- 2. Have specialised high-level laws that eliminate them in the context of parallel handler actions.
- 3. Have a generic strategy that eliminates them, possibly modulo guidance by the user.

In terms of automation, approach (1) and (2) seem most promising. We also see that (2) improves modularity in comparison to (1). In terms of genericity, (3) seems more powerful than the other approaches. I cannot see a clear strategy for approach (3) yet though and there are various open issues, for instance, with regards to the information that the user has to provide during refinement. This is a challenge for future work.

Below we present a corresponding law for approach (2) to eliminate the control action that emerges from application of the law par-share. It applies in the context of n+1 parallel handlers.

# High-level Law 4 (par-share-control)

```
\begin{pmatrix} \left( \mu X \bullet A \; ; \; start \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X \right) \\ \llbracket ns \; | \; \{ \; start, \; sync \; \} \; | \; \varnothing \rrbracket \\ \left( \mu X \bullet start \longrightarrow \mathbf{var} \; v : \; T \bullet A_1 \; ; \; record \; ! \; v \longrightarrow \mathbf{skip} \; ; \; output \; ? \; y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X \right) \\ \llbracket \varnothing \; | \; \{ \; start, \; output, \; sync \; \} \; | \; \varnothing \rrbracket \\ \left( \mu X \bullet start \longrightarrow \mathbf{var} \; v : \; T \bullet A_2 \; ; \; record \; ! \; v \longrightarrow \mathbf{skip} \; ; \; output \; ? \; y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X \right) \\ \dots \\ \llbracket \varnothing \; | \; \{ \; start, \; output, \; sync \; \} \; | \; \varnothing \rrbracket \\ \left( \mu X \bullet start \longrightarrow \mathbf{var} \; v : \; T \bullet A_n \; ; \; record \; ! \; v \longrightarrow \mathbf{skip} \; ; \; output \; ? \; y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X \right) \\ \llbracket ns \; | \; \{ \; start, \; record, \; output, \; sync \; \} \; | \; \varnothing \rrbracket \\ \left( \mu X \bullet init \longrightarrow start \longrightarrow \begin{pmatrix} (record \; ? \; y \longrightarrow \mathbf{skip}) \; \Vert \\ (record \; ? \; y \longrightarrow \mathbf{skip}) \; \Vert \\ \dots \\ (record \; ? \; y \longrightarrow \mathbf{skip}) \; \Vert \\ \dots \\ (record \; ? \; y \longrightarrow \mathbf{skip}) \; \Vert \\ \end{pmatrix} \; ; \; output \; ? \; y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X \end{pmatrix} \; ,
  \begin{bmatrix} (\mu X \bullet A; & init \longrightarrow \mathbf{skip}; & start \longrightarrow \mathbf{skip}; & sync \longrightarrow X) \\ & [ns \mid \{ | start, sync | \} \mid \varnothing ] ] \\ (\mu X \bullet start \longrightarrow \mathbf{var} \ v : T \bullet A_1; & record ! \ v \longrightarrow \mathbf{skip}; & output? \ y \longrightarrow \mathbf{skip}; & sync \longrightarrow X) \\ & [ns \mid \{ | start, output, sync | \} \mid \varnothing ] ] \\ (\mu X \bullet start \longrightarrow \mathbf{var} \ v : T \bullet A_2; & record ! \ v \longrightarrow \mathbf{skip}; & output? \ y \longrightarrow \mathbf{skip}; & sync \longrightarrow X) \\ & \cdots \\ & [ns \mid \{ | start, output, sync | \} \mid \varnothing ] ] \\ (\mu X \bullet start \longrightarrow \mathbf{var} \ v : T \bullet A_n; & record ! \ v \longrightarrow \mathbf{skip}; & output? \ y \longrightarrow \mathbf{skip}; & sync \longrightarrow X) \end{bmatrix} 
                                            \{start, record, output, sync\} \cap usedC(A_i) = \emptyset \text{ for all } i:1...n
```

We observe that the parallel fragment is almost entirely absorbed, apart from a prefix  $init \longrightarrow \mathbf{skip}$  that we highlighted above. The proof of the law decomposes the control action as illustrated below.

```
 \begin{pmatrix} (\mu X \bullet init \longrightarrow start \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket \varnothing \; | \; \{ \; start, sync \; \} \; | \; \varnothing \rrbracket \\ (\mu X \bullet start \longrightarrow record \; ? \; y \longrightarrow output \; ? \; y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket \varnothing \; | \; \{ \; start, output, sync \; \} \; | \; \varnothing \rrbracket \\ (\mu X \bullet start \longrightarrow record \; ? \; y \longrightarrow output \; ? \; y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \dots \\ \llbracket \varnothing \; | \; \{ \; start, output, sync \; \} \; | \; \varnothing \rrbracket \\ (\mu X \bullet start \longrightarrow record \; ? \; y \longrightarrow output \; ? \; y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix}
```

The smaller control fragments that emerge are collapsed, as before, with the handler actions. All of this is justified using step laws. The prefixes  $sync \longrightarrow skip$  reveal their use in facilitating distribution of recursions.

### Pattern 4

This pattern is needed for the refinement in Stage 3 for our case study. Unlike the previous three patterns, which are geared towards the encapsulation of shared data that is already somewhat explicit in the model, Pattern 4 addresses the refinement of a control mechanism. The pattern effectively refines a synchronisation barrier by a mechanism that makes use of shared data. It is capture by two laws: the first law sync-barrier-elim eliminates the synchronisation barrier by virtue of a control action, and the second law sync-barrier-control replaces the control action by an action that introduces new shared data and becomes part of the MArea action. This factorisation fosters modularisation of the refinement strategy; whereas sync-barrier-elim is universally applied, we may envisage different designs that eliminate the control fragment.

Below we present the first law to remove the barrier by virtue of a control action.

## High-level Law 5 (sync-barrier-elim)

$$\begin{pmatrix} (\mu X \bullet start \longrightarrow A_1 \; ; \; done \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs_1 \mid ns_2 \cup \ldots \cup ns_n \rrbracket \\ (\mu X \bullet start \longrightarrow A_2 \; ; \; done \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_2 \mid cs_2 \mid ns_3 \cup \ldots \cup ns_n \rrbracket \\ \ldots \\ \llbracket ns_{n-1} \mid cs_{n-1} \mid ns_n \rrbracket \\ (\mu X \bullet start \longrightarrow A_n \; ; \; done \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix}$$

$$\equiv$$

$$\begin{pmatrix} (\mu X \bullet start \longrightarrow A_1 \; ; \; notify ! \; 1 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs_1 \setminus \{ \mid done \; \} \mid ns_2 \cup \ldots \cup ns_n \rrbracket \\ (\mu X \bullet start \longrightarrow A_2 \; ; \; notify ! \; 2 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_2 \mid cs_2 \setminus \{ \mid done \; \} \mid ns_3 \cup \ldots \cup ns_n \rrbracket \\ \ldots \\ \llbracket ns_{n-1} \mid cs_{n-1} \setminus \{ \mid done \; \} \mid ns_n \rrbracket \\ (\mu X \bullet start \longrightarrow A_n \; ; \; notify ! \; n \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix}$$

$$\llbracket ns_1 \cup \ldots \cup ns_n \mid \{ \mid start, notify, sync \; \} \mid \varnothing \rrbracket$$

$$\begin{pmatrix} \mu X \bullet start \longrightarrow \begin{pmatrix} (notify ! \; 1 \longrightarrow \mathbf{skip}) \; \Vert \\ (notify ! \; 2 \longrightarrow \mathbf{skip}) \; \Vert \\ \ldots \\ (notify ! \; n \longrightarrow \mathbf{skip}) \end{pmatrix} \; ; \; done \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X \end{pmatrix}$$

**provided**  $\{ | start, done, sync \} \subseteq cs_i \land \{ start, done, sync \} \cap usedC(A_i) = \emptyset \text{ for all } i:1..n$  and notify is a fresh channel of type  $\mathbb{N}$ 

### Proof of the Law

The proof of this law can be simplified by proceeding backwards (from the right-hand action) rather then forwards (from the left-hand action). The essential idea is once again to decompose the control action and collapse it with the handlers. The decomposition of the control fragment is sketched below.

$$\begin{pmatrix} \mu X \bullet start \longrightarrow \begin{pmatrix} (notify \, ! \, 1 \longrightarrow \mathbf{skip}) \, ||| \\ (notify \, ! \, 2 \longrightarrow \mathbf{skip}) \, ||| \\ \dots \\ (notify \, ! \, n \longrightarrow \mathbf{skip}) \end{pmatrix}; \ done \longrightarrow \mathbf{skip} \, ; \ sync \longrightarrow X \end{pmatrix}$$

≡ "distribution of recursion and application of suitable parallel step laws"

$$\begin{pmatrix} (\mu X \bullet start \longrightarrow notify \,! \, 1 \longrightarrow done \; ; \; sync \longrightarrow X) \\ \llbracket \varnothing \mid \{ \mid start, done, sync \, \} \mid \varnothing \rrbracket \\ (\mu X \bullet start \longrightarrow notify \,! \, 2 \longrightarrow done \; ; \; sync \longrightarrow X) \\ \dots \\ \llbracket \varnothing \mid \{ \mid start, done, sync \, \} \mid \varnothing \rrbracket \\ (\mu X \bullet start \longrightarrow notify \,! \, n \longrightarrow done \; ; \; sync \longrightarrow X) \end{pmatrix}$$

We omit a detailed account of the derivation. After the decomposed fragments are collapsed with the handler actions, the channel *notify* is subsequently removed since none of the parallel actions synchronise on it.

The second law is used to transform the control action that arises from sync-barrier-elim into a program design that uses new shared data.

# High-level Law 6 (sync-barrier-control)

$$\begin{pmatrix} \mu X \bullet start \longrightarrow \begin{pmatrix} (notify \,! \, 1 \longrightarrow \mathbf{skip}) \, || \\ (notify \,! \, 2 \longrightarrow \mathbf{skip}) \, || \\ \dots \\ (notify \,! \, n \longrightarrow \mathbf{skip}) \end{pmatrix}; \ done \longrightarrow \mathbf{skip} \,; \ sync \longrightarrow X \end{pmatrix}$$

$$\equiv \begin{pmatrix} (\mu X \bullet reset \longrightarrow start \longrightarrow X \,; \ sync \longrightarrow X) \\ \|\varnothing \mid \{ start, sync \} \mid \varnothing \| \\ (\mu X \bullet start \longrightarrow notify \,! \, 1 \longrightarrow \mathbf{skip} \,; \ sync \longrightarrow X) \\ \|\varnothing \mid \{ start, sync \} \mid \varnothing \| \\ (\mu X \bullet start \longrightarrow notify \,! \, 2 \longrightarrow \mathbf{skip} \,; \ sync \longrightarrow X) \\ \|\varnothing \mid \{ start, sync \} \mid \varnothing \| \\ \dots \\ \|\varnothing \mid \{ start, sync \} \mid \varnothing \| \\ (\mu X \bullet start \longrightarrow notify \,! \, n \longrightarrow \mathbf{skip} \,; \ sync \longrightarrow X) \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, n \longrightarrow \mathbf{skip} \,; \ sync \longrightarrow X) \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, n \longrightarrow \mathbf{skip} \,; \ sync \longrightarrow X) \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \} \mid \varnothing \| \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \} \mid \varnothing \| \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X) \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X) \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X) \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X) \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X) \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X) \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X) \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X) \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X) \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X) \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X \end{pmatrix} \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X \end{pmatrix} \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X \end{pmatrix} \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X \end{pmatrix} \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X \end{pmatrix} \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X \end{pmatrix} \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X \bullet start \longrightarrow notify \,! \, sync \longrightarrow X \end{pmatrix} \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{ reset \} \\ \| (\mu X$$

We note that apart from the right hand parallel branch encapsulating the shared data, we also have a left hand parallel branch that contains a parallelism of smaller control fragments. These fragments will have to be decomposed in the refinement strategy and give rise to another synchronisation on the *start* channel that initialises the *active* variable prior to starting execution of the parallel handlers. The fragments are very simple though and it should be possible to distribute them (mostly) automatically.

Finally, we consider a third law that combines the previous two laws. In this law, we implicitly already collapsed four of the five smaller control fragments emerging from the application of sync-barrier-control.

## High-level Law 7 (sync-barrier-design)

$$\begin{pmatrix} (\mu X \bullet start \longrightarrow A_1 \; ; \; done \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs_1 \mid ns_2 \cup \ldots \cup ns_n \rrbracket \\ (\mu X \bullet start \longrightarrow A_2 \; ; \; done \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_2 \mid cs_2 \mid ns_3 \cup \ldots \cup ns_n \rrbracket \\ \ldots \\ \llbracket ns_{n-1} \mid cs_{n-1} \mid ns_n \rrbracket \\ (\mu X \bullet start \longrightarrow A_n \; ; \; done \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix}$$

$$\sqsubseteq$$

$$\begin{pmatrix} (\mu X \bullet start \longrightarrow A_n \; ; \; done \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket \varnothing \mid \{ \; reset , start , sync \; \} \mid \varnothing \rrbracket \\ \end{pmatrix} \begin{pmatrix} (\mu X \bullet start \longrightarrow A_1 \; ; \; notify \; ! \; 1 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs_1 \setminus \{ \; done \; \} \mid ns_2 \cup \ldots \cup ns_n \rrbracket \\ \end{pmatrix} \\ (\mu X \bullet start \longrightarrow A_2 \; ; \; notify \; ! \; 2 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_2 \mid cs_2 \setminus \{ \; done \; \} \mid ns_3 \cup \ldots \cup ns_n \rrbracket \\ \ldots \\ \llbracket ns_{n-1} \mid cs_{n-1} \setminus \{ \; done \; \} \mid ns_n \rrbracket \\ \end{pmatrix} \\ \begin{pmatrix} (\mu X \bullet start \longrightarrow A_n \; ; \; notify \; ! \; n \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix} \\ \llbracket ns_1 \cup \ldots \cup ns_n \mid \{ \; start , notify \; ; \; sync \; \} \mid \varnothing \rrbracket \\ \end{pmatrix} \\ \begin{pmatrix} \mathbf{var} \; active : \mathbb{P} \; \{1 \ldots n\} \bullet \\ \end{pmatrix} \\ \begin{pmatrix} (reset \longrightarrow active : = 1 \ldots n) \\ \square \\ \end{pmatrix} \\ \begin{pmatrix} (reset \longrightarrow active : = 1 \ldots n) \\ \square \\ \end{pmatrix} \\ \begin{pmatrix} active : = active \setminus \{x\}; \\ \text{if } \; active = \varnothing \longrightarrow \mathbf{skip} \\ \text{fi} \end{pmatrix} \end{pmatrix}$$

**provided** { reset, start, done, sync }  $\subseteq cs_i \land \{reset, start, done, sync \} \cap usedC(A_i) = \emptyset$  for all i:1..n and notify is a fresh channel of type  $\mathbb{N}$ 

This law is less modular but more useful in terms of automation. The residual refinement effort consists of distributing the simple control fragment highlighted above. Intuitively, this corresponds to calling an initialisation method on the shared state. We note that one may envisage designs that do not require an initialisation (notify could cater for this too). For such designs the law sync-barrier-elim would still be useful, though sync-barrier-design is too specific to be applicable. This highlights a general trade-off between modularity and reuse and automation. It is an important insight and lesson learned in this case study.

We note that even in the design law sync-barrier-design, additional refinement is still required during the AR phase to data refine the *active* component into a class object and to turn the channels *start* and *notify* into method call channel pairs. Otherwise, we have now all ingredients to tackle the refinement stages for the SH phase in our example. We examine them in detail in the remainder of the section.

## 5.3.2 Stage 1

Our main objective in Stage 1 to Stage 3 is to encapsulate shared data and provide means for accessing it. In doing so, we tease out the *MArea* action, and Step 4 transforms it into the precise shape to match the program design. The essence of the refinement steps is mostly the application of the high-level patterns that have been presented in Section 5.3.1. Our starting point is the *System* action resulting from MH.

```
System =
                          (\mu X \bullet (next\_frame?frame)) \blacktriangleleft INP\_DL;
                           reduce! currentFrame! state \longrightarrow skip; sync \longrightarrow X
                              \llbracket \{\mathit{currentFrame}, \mathit{state}\} \mid \{ | \mathit{reduce}, \mathit{sync} \mid \} \mid \{ \mathit{voxel\_map}, \mathit{work} \} \rrbracket
                           \mu X \bullet reduce ? currentFrame ? state \longrightarrow

wait 0 . . RPW_{TB} ; ReduceAndPartitionWork;
                            detect ! work \longrightarrow \mathbf{skip} ; sync \longrightarrow X
                              [\{voxel\_map, work\} \mid \{|detect, sync|\} \mid \varnothing]
                           \mu X \bullet detect ? work \longrightarrow \mathbf{var} \ colls1 : int \bullet \mathbf{wait} \ 0 \dots CPC_{TB};
                           (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1);
(\exists colls! colls! colls1 \longrightarrow \mathbf{skip}; output?y \longrightarrow \mathbf{skip}; sync \longrightarrow X)
                               \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
                          (\mu X \bullet detect? work \longrightarrow \mathbf{var} \ colls2: int \bullet \mathbf{wait} \ 0... \ CPC_{TB}; \ (\exists \ i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land \ i? = 2); \ recColls! \ colls2 \longrightarrow \mathbf{skip}; \ output?y \longrightarrow \mathbf{skip}; \ sync \longrightarrow X)
                              \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
                           \mu X \bullet detect? work \longrightarrow \mathbf{var} \ colls3: int \bullet \mathbf{wait} \ 0... \ CPC_{TB};
                            (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3);
                           recColls! colls3 \longrightarrow \mathbf{skip}; output?y \longrightarrow \mathbf{skip}; sync \longrightarrow X
                               \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
                          (\mu X \bullet detect ? work \longrightarrow \mathbf{var} \ colls4 : int \bullet \mathbf{wait} \ 0 ... \ CPC_{TB}; \ (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4); \ (recColls! \ colls4 \longrightarrow \mathbf{skip}; \ output?y \longrightarrow \mathbf{skip}; \ sync \longrightarrow X)
                               \llbracket \varnothing \mid \{ \mid output, sync \mid \} \mid \varnothing \rrbracket
                           (\mu X \bullet output ? collisions \longrightarrow 

\mathbf{var}\ colls : int \bullet \mathbf{wait}\ 0 ... CC_{TB}\ ; \ Calc Collisions;

(output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL\ ; \ sync \rightarrow
                          [[currentFrame, state, voxel\_map, work]]
                                  \{|detect, output, recColls, sync|\} \mid \{collisions\}\}
                  Interaction Handlers
                     [\{currentFrame, state, voxel\_map, work, collisions\} \mid \{\{sync\}\} \mid \emptyset]
             Cycle
                \{| reduce, detect, output, recColls, sync |\}
```

We have flattened the calls to local actions for handlers using the copy rule, though for brevity we keep the calls to *InteractionHandlers* and *Cycle* until we require to expand their definitions in Stage 2.

The refinement in this stage applies the laws seq-share-1 and seq-share-2 to the synchronisations on reduce and detect, respectively. Above, we have highlighted the target for the first application of seq-share-1. This requires some reordering of parallel actions. After applying the law, we also extract channel hiding and isolate the action that contributes to MArea into a separate branch of the top-level parallelism. These

supplementary steps can be tedious on the paper but do not pose a challenge to automation.

= "reordering parallel actions and distributing the hiding of the reduce channel"

```
\mu X \bullet (next\_frame ? frame \longrightarrow (\mathbf{wait} \ 0 ... SF_{TB} \ ; \ StoreFrame)) \blacktriangleleft INP\_DL;
          reduce ! currentFrame ! state \longrightarrow \mathbf{skip} ; sync \longrightarrow X
         \setminus \{ | reduce | \}
         [\{currentFrame, state, voxel\_map, work\} \mid \{\{detect, sync\}\} \mid \emptyset]
       \mu X \bullet detect? work \longrightarrow \mathbf{var} \ colls1: int \bullet \mathbf{wait} \ 0.. \ CPC_{TB};
      (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1);
(\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1);
(\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1);
      [\{currentFrame, state, voxel\_map, work\}]
            \{|detect, output, recColls, sync|\} \mid \{collisions\}\}
 Interaction Handlers
   [{currentFrame, state, voxel\_map, work, collisions} | {sync} | \varnothing ] Cycle
\{|detect, output, recColls, sync|\}
```

 $\equiv$  "application of the law seq-share-1 introducing a new typeless channel reduce"

```
\mu X \bullet (next\_frame? frame \longrightarrow (wait 0 ... SF_{TB}; StoreFrame)) \blacktriangleleft INP\_DL;
          setFrameState! currentFrame! state \longrightarrow \mathbf{skip}; reduce \longrightarrow \mathbf{skip}; sync \longrightarrow X
             [{currentFrame, state} \mid {reduce, sync} \mid {voxel\_map, work}]
           \mu X \bullet reduce \longrightarrow \mathbf{skip}; getFrameState? currentFrame? state - \mathbf{wait} \ 0 \dots RPW_{TB}; ReduceAndPartitionWork;
           detect ! work \longrightarrow \mathbf{skip} ; sync \longrightarrow X
               \setminus \{ | reduce | \}
          [\{currentFrame, state, voxel\_map, work\} \mid \{\{setFrameState, getFrameState\}\} \mid \emptyset]
        \mathbf{var}\ currentFrame: RawFrame ullet
        \mathbf{var}\ state: StateTable ullet
                     \setminus \{ | setFrameState, getFrameState | \} 
       [[\{currentFrame, state, voxel\_map, work\} \mid \{[detect, sync]\} \mid \varnothing]]
    [\{currentFrame, state, voxel\_map, work\}]
         \{|detect, output, recColls, sync|\} | \{collisions\}|\}
Interaction Handlers \\
 [{currentFrame, state, voxel\_map, work, collisions} | {sync} | \varnothing ] Cycle
   \{|detect, output, recColls, sync|\}
```

We note that the fresh channel reduce, highlighted in blue, is different from the former existing channel reduce,

highlighted in red: whereas reduce is typeless, reduce is of type  $RawFrame \times StateTable$ . To emphasise the part of the action that contributes to MArea as well as the underlying channels for shared data access, we use a green highlight. Next, we reorder the parallelism once again to isolate the sharing action.

 $\equiv$  "reordering of parallelism and extraction of hiding using suitable laws"

```
\mu X \bullet (next\_frame? frame \longrightarrow (wait 0 ... SF_{TB}; StoreFrame)) \blacktriangleleft INP\_DL;
             \langle setFrameState! currentFrame! state \longrightarrow \mathbf{skip}; reduce \longrightarrow \mathbf{skip}; sync \longrightarrow X \rangle
                  [[\{currentFrame, state\} \mid \{[reduce, sync]\} \mid \{voxel\_map, work\}]]
               \mu X \bullet reduce \longrightarrow \mathbf{skip}; getFrameState? currentFrame? state \longrightarrow
               wait 0 ... RPW_{TB}; ReduceAndPartitionWork;
               detect ! work \longrightarrow \mathbf{skip} ; sync \longrightarrow X
                  [\{voxel\_map, work\} \mid \{|detect, sync|\} \mid \varnothing]
              \begin{pmatrix} \mu X \bullet detect? work \longrightarrow \mathbf{var} \ colls1: int \bullet \mathbf{wait} \ 0 ... \ CPC_{TB}; \\ (\exists i?: \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1); \\ recColls! \ colls1 \longrightarrow \mathbf{skip}; \ output?y \longrightarrow \mathbf{skip}; \ sync \longrightarrow X \end{pmatrix} 
                  \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
               \mu X \bullet detect ? work \longrightarrow \mathbf{var} \ colls2 : int \bullet \mathbf{wait} \ 0 \dots CPC_{TB};
               (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2);
              (recColls! colls2 \longrightarrow \mathbf{skip}; output?y \longrightarrow \mathbf{skip}; sync \longrightarrow X)
                  \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
               '\mu X \bullet detect? work \longrightarrow \mathbf{var}\ colls3: int \bullet \mathbf{wait}\ 0...\ CPC_{TB};
              (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3);

(\forall i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3);

(\forall i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3);
                  \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
              \begin{pmatrix} \mu X \bullet detect? work \longrightarrow \mathbf{var} \ colls4: int \bullet \mathbf{wait} \ 0.. \ CPC_{TB}; \\ (\exists i?: \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4); \\ recColls! \ colls4 \longrightarrow \mathbf{skip}; \ output? \ y \longrightarrow \mathbf{skip}; \ sync \longrightarrow X \end{pmatrix} 
                  \llbracket \varnothing \mid \{ \mid output, sync \mid \} \mid \varnothing \rrbracket
              \begin{pmatrix} \mu X \bullet output ? collisions \longrightarrow \\ \mathbf{var} \ colls : int \bullet \mathbf{wait} \ 0 ... \ CC_{TB} \ ; \ Calc Collisions; \\ (output\_collisions! \ colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL \ ; \ sync \longrightarrow X 
              [\{currentFrame, state, voxel\_map, work\}]
                      \{|detect, output, recColls, sync|\} \mid \{collisions\}|\}
      Interaction Handlers
         [{currentFrame, state, voxel\_map, work, collisions} | {sync} | \varnothing ] Cycle
    [\{currentFrame, state, voxel\_map, work, collisions\} \mid \{\{setFrameState, qetFrameState\}\} \mid \{\}\}
 \mathbf{var}\ currentFrame: RawFrame ullet
 var state : State Table • \mu X \bullet \begin{pmatrix} (setFrameState ? v_1 ? v_2 \longrightarrow currentFrame, state := v_1, v_2) & \Box \\ (getFrameState ! currentFrame ! state \longrightarrow \mathbf{skip}) \end{pmatrix}; X
{| reduce, detect, output, recColls, setFrameState, getFrameState, sync |}
```

This concludes the application of Pattern 1. We proceed exactly in the same way in order to encapsulate the data passed through the *detect* channel. The only difference is that we have to apply seq-share-2 rather than seq-share-1, that is the law for Pattern 2. We omit the low-level steps and just give the result here.

≡ "application of the law seq-share-2 including pre- and post-processing transformations"

```
(\mu X \bullet (next\_frame? frame \longrightarrow (wait 0 ... SF_{TB}; StoreFrame)) \blacktriangleleft INP\_DL;
             setFrameState! currentFrame! state \longrightarrow \mathbf{skip}; reduce \longrightarrow \mathbf{skip}; sync \longrightarrow X
                 [[\{currentFrame, state\} \mid \{| reduce, sync \}| \mid \{voxel\_map, work\}]]
              \mu X \bullet reduce \longrightarrow \mathbf{skip}; getFrameState? currentFrame? state \longrightarrow
              wait 0 ... RPW_{TB}; ReduceAndPartitionWork;
             setWork!work \longrightarrow \mathbf{skip}; detect \longrightarrow \mathbf{skip}; sync \longrightarrow X
                 [\{voxel\_map, work\} \mid \{|detect, sync|\} \mid \varnothing]
              \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork ? work \longrightarrow \mathbf{var} \; colls1 : int \bullet
             wait 0...CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1); (\neg cColls! colls1 \longrightarrow \mathbf{skip}; output?y \longrightarrow \mathbf{skip}; sync \longrightarrow X)
                 \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
            \begin{pmatrix} \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; \; getWork \; ? \; work \longrightarrow \mathbf{var} \; colls2 : int \bullet \\ \mathbf{wait} \; 0 \; .. \; CPC_{TB} \; ; \; (\exists \; i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land \; i? = 2); \\ recColls \; ! \; colls2 \longrightarrow \mathbf{skip} \; ; \; \; output \; ? \; y \longrightarrow \mathbf{skip} \; ; \; \; sync \longrightarrow X \end{pmatrix}
                 \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
             '\mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork ? work \longrightarrow \mathbf{var} \; colls3 : int \bullet
              wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3);
             recColls! colls3 \longrightarrow \mathbf{skip}; output?y \longrightarrow \mathbf{skip}; sync \longrightarrow X
                 \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
              \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork ? work \longrightarrow \mathbf{var} \; colls4 : int \bullet
              wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4);
             (recColls! colls4 \longrightarrow \mathbf{skip}; output?y \longrightarrow \mathbf{skip}; sync \longrightarrow X)
                 \llbracket \varnothing \mid \{ \mid output, sync \mid \} \mid \varnothing \rrbracket
             (\mu X \bullet output ? collisions \longrightarrow 

\mathbf{var}\ colls : int \bullet \mathbf{wait}\ 0 ...\ CC_{TB}\ ; \ CalcCollisions;

(output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL\ ; \ sync \longrightarrow
             [\{currentFrame, state, voxel\_map, work\}]
                    \{|detect, output, recColls, sync|\} \mid \{collisions\}|\}
      Interaction Handlers
        [{currentFrame, state, voxel\_map, work, collisions} | {sync} | \varnothing ] Cycle
   [{currentFrame, state, voxel\_map, work, collisions}]
           \{ | setFrameState, getFrameState, setWork, getWork | \} \mid \varnothing \}
      \mathbf{var}\ currentFrame: RawFrame
     var state : StateTable • \mu X \bullet \begin{pmatrix} (setFrameState ? v_1 ? v_2 \longrightarrow currentFrame, state := v_1, v_2) & \\ (getFrameState ! currentFrame ! state \longrightarrow \mathbf{skip}) \end{pmatrix}
     (\mathbf{var} \ work : Partition \bullet)
\mu X \bullet \begin{pmatrix} (setWork ? v \longrightarrow work := v) \ \Box \\ (getWork ! \ work \longrightarrow \mathbf{skip}) \end{pmatrix}; X
\{ reduce, detect, output, recColls, setFrameState, qetFrameState, qetWork, setWork, sync \} \}
```

We note that applying the law for Pattern 2, we have to take into account that *InteractionHandlers* also synchronises on the *detect* channel although it is not interested in the value communicated. Hence, for

InteractionHandler we obtain the following action as a result of the previous refinement pattern.

 $InteractionHandlers \stackrel{\frown}{=}$ 

```
 \begin{pmatrix} \mu X \bullet detect \longrightarrow getWork? y \longrightarrow \\ \mathbf{var}\ colls1, colls2, colls3, colls4: int \bullet \\ \left( (recColls? x \longrightarrow (\mathbf{wait}\ 0 \ldots RC_{TB}\ ;\ colls1:=x))\ ;\ (recColls? x \longrightarrow (\mathbf{wait}\ 0 \ldots RC_{TB}\ ;\ colls2:=x))\ ;\ (recColls? x \longrightarrow (\mathbf{wait}\ 0 \ldots RC_{TB}\ ;\ colls3:=x))\ ;\ (recColls? x \longrightarrow (\mathbf{wait}\ 0 \ldots RC_{TB}\ ;\ colls4:=x)) \end{pmatrix}, \\ \mathbf{wait}\ 0 \ldots SCFP_{TB}; \\ SetCollisionsFromParts([[\ colls1,\ colls2,\ colls3,\ colls4]]); \\ output!\ collisions \longrightarrow \mathbf{skip}\ ;\ sync \longrightarrow X
```

Clearly, the value y is not used in the action after the prefix getWork?  $y \longrightarrow A$  above. Since the channel getWork is concealed and InteractionHandlers only synchronises with the MArea fragment on this channel, we can use a noncompositional rule to remove this communication altogether. Intuitively, this is justified by getWork never being blocked and not having an affect on the action's behaviour.

 $\ldots \equiv$  "Specialised noncompositional rule to remove channel communication"

```
\begin{pmatrix} \mu X \bullet detect \longrightarrow \\ \mathbf{var} \ colls1, \ colls2, \ colls3, \ colls4: \ int \bullet \\ \begin{pmatrix} (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB} \ ; \ colls1:=x)) \ ; \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB} \ ; \ colls2:=x)) \ ; \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB} \ ; \ colls3:=x)) \ ; \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB} \ ; \ colls4:=x)) \end{pmatrix}, \\ \mathbf{wait} \ 0 \ldots SCFP_{TB}; \\ SetCollisionsFromParts(\llbracket \ colls1, \ colls2, \ colls3, \ colls4 \rrbracket); \\ output! \ collisions \longrightarrow \mathbf{skip}; \ sync \longrightarrow X \end{pmatrix}
```

Here, we shall not examine this rule further but merely identify the need for it. Alternatively, we could provide a more specialised version of the seq-share-2 law; such, however, may not be as reusable as the present version of the law. To compare and evaluate both options further investigation is necessary.

At this point, one may be tempted to apply Pattern 2 yet another time in order to replace the typed output channel. This indeed is possible, module a minor alteration of the law, but it does not produce the desired program design. More specifically, end up with a shared variable collisions that is accessed via get and set operations, modelled by the channels getColls and setColls, for instance. This results in a different design where InteractionHandlers collects the partial results but carries out the update to collisions in a single atomic operation. We, however, want to carry out this update incrementally and concurrently during the detection phase. For this reason, Stage 1 is completed here and we look at the refinement of InteractionHandlers in Stage 2 using a different Pattern 3.

### 5.3.3 Stage 2

In Stage 2 of the SH phase, we apply Pattern 3. This consists of applying the law par-share presented earlier on in Section 5.3.1. The law cannot be applied immediately; we first have to bring the action InteractionHandlers, which is the target for the law application, into a form that matches the left-hand action of the law. After application of the law, more work has to be done to eliminate residual parallel actions by distributing them into the handlers. This gives rise to localising the synchronisation that initialises the shared collisions variable prior to the parallel detection phase. We discuss each sub-step in detail.

## Transformation of Application Target

We first recall the current definition of InteractionHandlers to which we aim to apply the par-share law.

```
InteractionHandlers \cong \begin{cases} \mu X \bullet detect \longrightarrow \\ \mathbf{var} \ colls1, \ colls2, \ colls3, \ colls4: \ int \bullet \\ \left( (recColls? \ x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB}; \ colls1:=x)); \\ (recColls? \ x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB}; \ colls2:=x)); \\ (recColls? \ x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB}; \ colls3:=x)); \\ (recColls? \ x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB}; \ colls4:=x)) \end{cases}; \\ \mathbf{wait} \ 0 \ldots SCFP_{TB}; \\ SetCollisionsFromParts([[\ colls1, \ colls2, \ colls3, \ colls4]]); \\ output \ ! \ collisions \longrightarrow \mathbf{skip}; \ sync \longrightarrow X \end{cases}
```

The left-hand side of the par-share law is recaptured below.

```
 \begin{pmatrix} \mathbf{var} \ v : T \bullet \\ \mu X \bullet \ start \longrightarrow \mathbf{wait} \ 0 \dots Init_{TB} \ ; \ InitOp; \\ \mathbf{var} \ x_1, x_2, \dots, x_n : T \bullet \\ \left( (record \ ? \ x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB} \ ; \ x_1 := x)); \\ (record \ ? \ x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB} \ ; \ x_2 := x)); \\ \dots \\ (record \ ? \ x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB} \ ; \ x_n := x)); \\ \mathbf{wait} \ 0 \dots Merge_{TB} \ ; \ MergeOp([[x_1, x_2, \dots, x_n]]); \\ output \ ! \ v \longrightarrow \mathbf{skip} \ ; \ sync \longrightarrow X \end{pmatrix}
```

Most notable, SetCollisionsFromParts has to be decomposed. In Section 5.3.1 (Pattern 3), we have already illustrated the decomposition of SetCollisionsFromParts by way of the following two schema operations.

We have changed their names here into InitColls and RecColls. We have that SetCollsionsFromParts(cb) = InitColls; RecColls(cb). We also have the following algorithmic refinements used later on.

```
\mathit{InitColls} \sqsubseteq \mathit{collisions} := 0 \quad \text{and} \quad \mathit{RecColls}(\llbracket \mathit{colls} \rrbracket) \sqsubseteq \mathit{collision} := \mathit{collisions} + \mathit{colls}
```

The refinement of *InteractionHandlers* thus yields.

... 

"decomposition of SetCollisionsFromParts into an initialisation and merge operation"

```
Interaction Handlers =
```

```
\begin{pmatrix} \mu X \bullet detect \longrightarrow \\ \mathbf{var} \ colls1, \ colls2, \ colls3, \ colls4: \ int \bullet \\ \begin{pmatrix} (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB} \ ; \ colls1:=x)); \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB} \ ; \ colls2:=x)); \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB} \ ; \ colls3:=x)); \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB} \ ; \ colls4:=x)) \end{pmatrix}; \\ \mathbf{wait} \ 0 \ldots SCFP_{TB}; \\ \mathbf{InitColls}; \ \ \mathbf{RecColls}([[\ colls1, \ colls2, \ colls3, \ colls4]]); \\ output! \ collisions \longrightarrow \mathbf{skip}; \ \ sync \longrightarrow X \end{pmatrix}
```

... \( \pi \) "multiple applications of the law seq-op-comm to move InitColls through the sequence"

 $Interaction Handlers \stackrel{\frown}{=}$ 

```
\begin{pmatrix} \mu X \bullet detect \longrightarrow \\ \mathbf{var} \ colls1, \ colls2, \ colls3, \ colls4: \ int \bullet \ \textit{InitColls}; \\ \left( (recColls? x \longrightarrow (\mathbf{wait} \ 0 ... RC_{TB}; \ colls1:=x)); \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 ... RC_{TB}; \ colls2:=x)); \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 ... RC_{TB}; \ colls3:=x)); \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 ... RC_{TB}; \ colls4:=x)) \end{pmatrix}; \\ \mathbf{wait} \ 0 ... SCFP_{TB}; \\ RecColls([[colls1, colls2, colls3, colls4]]); \\ output! \ collisions \longrightarrow \mathbf{skip}; \ sync \longrightarrow X \end{pmatrix}
```

... 

"introduction of spurious wait statement using the law zero-wait-intro"

InteractionHandlers =

```
 \begin{pmatrix} \mu X \bullet detect \longrightarrow \\ \mathbf{var} \ colls1, \ colls2, \ colls3, \ colls4: \ int \bullet \mathbf{wait} \ 0 \ ; \ \ InitColls; \\ \begin{pmatrix} (recColls? \ x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB} \ ; \ colls1:=x)) \ ; \\ (recColls? \ x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB} \ ; \ colls2:=x)) \ ; \\ (recColls? \ x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB} \ ; \ colls3:=x)) \ ; \\ (recColls? \ x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB} \ ; \ colls4:=x)) \end{pmatrix} , \\ \mathbf{wait} \ 0 \ldots SCFP_{TB}; \\ RecColls([[\ colls1, \ colls2, \ colls3, \ colls4][]); \\ output \ ! \ collisions \longrightarrow \mathbf{skip} \ ; \ sync \longrightarrow X
```

Above we use a special commutativity law for data operations in the second sub-step.

Circus Law 35 (seq-op-comm)

```
A \; ; \; Op \equiv Op \; ; \; A

provided usedV(Op) \cup wrtV(A) = \emptyset and usedV(A) \cup wrtV(Op) = \emptyset
```

We also require a law to introduce a spurious **wait** 0 statement in order to align the shape of the action to match the left-hand side of the law. The respective law is **zero-wait-intro**, included in Appendix B.1.

We observe that the action InteractionHandlers now has the correct shape to apply the par-share law. Altogether the most challenging task in the above sub-steps is the decomposition of the SetCollisionsFromParts operation. In practical terms, it may be possible to define tactics that target particular schema operation shapes. The above sub-steps rely on the fact that InitColls is instantaneous (does not consume time). Dealing with cases where this assumption is not given is part of our future work.

## Application of Pattern 3

The application of Pattern 3 is entailed by the following refinement.

```
Interaction Handlers =
                                       \begin{array}{c} \mathbf{var} \  \, \mathbf{var} \  \, \mathbf{valt} \  \, \mathbf{volls1}, \  \, \mathbf{colls2}, \  \, \mathbf{colls3}, \  \, \mathbf{colls4} : int \bullet \mathbf{wait} \  \, \mathbf{0} \  \, ; \  \, InitColls; \\ \left( \begin{matrix} (recColls? \, x \longrightarrow (\mathbf{wait} \  \, 0 \, ... \, RC_{TB} \  \, ; \  \, \mathbf{colls1} := x)) \, ; \\ (recColls? \, x \longrightarrow (\mathbf{wait} \  \, 0 \, ... \, RC_{TB} \  \, ; \  \, \mathbf{colls2} := x)) \, ; \\ (recColls? \, x \longrightarrow (\mathbf{wait} \  \, 0 \, ... \, RC_{TB} \  \, ; \  \, \mathbf{colls3} := x)) \, ; \\ (recColls? \, x \longrightarrow (\mathbf{wait} \  \, 0 \, ... \, RC_{TB} \  \, ; \  \, \mathbf{colls4} := x)) \\ \mathbf{wait} \  \, 0 \, ... \, SCFP_{TB}; \end{array}
                                           RecColls([colls1, colls2, colls3, colls4]);
                                           output ! collisions \longrightarrow \mathbf{skip} ; sync \longrightarrow X
\dots \equiv "application of the law par-share"
```

```
\left(\begin{pmatrix} \mu X \bullet initColls \longrightarrow detect \longrightarrow \begin{pmatrix} (recColls\,?\,y \longrightarrow \mathbf{skip}) \parallel \\ (recColls\,?\,y \longrightarrow \mathbf{skip}) \parallel \\ (recColls\,?\,y \longrightarrow \mathbf{skip}) \parallel \\ (recColls\,?\,y \longrightarrow \mathbf{skip}) \end{pmatrix}; \ output\,?\,y \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X \right)
  \begin{bmatrix} \varnothing \mid \{ | initColls, recColls, output \} \mid \varnothing ] \\ (\mu_X \bullet \begin{pmatrix} (initColls \longrightarrow (\mathbf{wait} \ 0 \ ; \ InitColls)) \ \Box \\ (recColls ? x \longrightarrow (\mathbf{wait} \ 0 \ .. \ RC_{TB} \ ; \ RecColls([[x]]))) \ \Box \end{pmatrix} ; X \\ (output ! collisions \longrightarrow \mathbf{skip}) \end{bmatrix} 
                \setminus \{ initColls \}
```

This introduces a new channel initColls which corresponds to the method call that initialises collisions.

Although this might be an issue for the AR phase, we carry out further refinement that transforms InitColls as well as the call RecColls([x]) into simple assignments, as previously suggested.

 $\ldots \equiv$  "algorithmic refinement of InitColls and RecColls( $[\![x]\!]$ ) and elimination of spurious wait"

```
\left( \begin{pmatrix} \mu X \bullet initColls \longrightarrow detect \longrightarrow \begin{pmatrix} (recColls\,?\,y \longrightarrow \mathbf{skip}) \parallel \\ (recColls\,?\,y \longrightarrow \mathbf{skip}) \parallel \\ (recColls\,?\,y \longrightarrow \mathbf{skip}) \parallel \\ (recColls\,?\,y \longrightarrow \mathbf{skip}) \end{pmatrix}; \ output\,?\,y \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X \right)
 \left[ \varnothing \mid \{ | initColls, recColls, output \} \mid \varnothing ] \right] 
 \left( \mu X \bullet \begin{pmatrix} (initColls \longrightarrow collisions := 0) \ \Box \\ (recColls? x \longrightarrow (wait \ 0 \ldots RC_{TB}; collisions := collisions + x)) \ \Box \end{pmatrix}; X \right) 
              \setminus \{ | initColls | \}
```

The underlying MArea fragment, highlighted as before in green, now has the desired shape in the program. The only remaining issue is the decomposition and distribution of the left-hand control action.

### **Elimination of Parallel Control Action**

In order to eliminate the control action that emerged from the application of the law par-share, we may envisage two possible approaches. First, we may carry out manual elementary refinement steps that achieve the decomposition and distribution of the resulting smaller parallel fragments. Or otherwise, we may use a law that already entails the collapsing of (most of) the parallelism in the context of the handlers that record the results. The first approach is sketched by the refinement below that decomposes the control fragment.

$$\begin{pmatrix} \mu X \bullet initColls \longrightarrow detect \longrightarrow \begin{pmatrix} (recColls\,?\,y \longrightarrow \mathbf{skip}) \parallel \\ (recColls\,?\,y \longrightarrow \mathbf{skip}) \parallel \\ (recColls\,?\,y \longrightarrow \mathbf{skip}) \parallel \\ (recColls\,?\,y \longrightarrow \mathbf{skip}) \end{pmatrix}; \ output\,?\,y \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X \end{pmatrix}$$

... 

"application of the law distr-rec-par-1 and elementary parallel step laws for decomposition"

The five smaller fragments are now collapsed with suitable handlers. Whereas the first action is collapsed with the reducer handler, the remaining four actions are collapsed with the four detection handlers. Only the first action leaves a trace, namely a prefix  $initColls \longrightarrow \mathbf{skip}$ ; the other actions are simply absorbed. Thus, we could encompass the collapsing of the other four fragments directly into a law that refines the control fragment that arises from the application of the par-share law in the context of the detection handlers. This is exactly along the lines of the modularisation in Pattern 4 via a sharing, control and design law.

Below we present the result after injecting the refinement of *InteractionHandlers* and subsequently distributing the residual parallel control action (we omit the detailed steps for decomposition and collapsing).

```
\mu X \bullet (next\_frame? frame \longrightarrow (\mathbf{wait} \ 0 \dots SF_{TB}; StoreFrame)) \blacktriangleleft INP\_DL;
                 setFrameState! currentFrame! state \longrightarrow \mathbf{skip}; reduce \longrightarrow \mathbf{skip}; sync \longrightarrow X
                              [[\{currentFrame, state\} \mid \{[reduce, sync]\} \mid \{voxel\_map, work\}]]
                (\mu X \bullet reduce \longrightarrow \mathbf{skip}; getFrameState? currentFrame? state \longrightarrow \mathbf{wait} \ 0 \dots RPW_{TB}; ReduceAndPartitionWork; 
 <math>(setWork! work \longrightarrow \mathbf{skip}; initColls \longrightarrow \mathbf{skip}; detect \longrightarrow \mathbf{skip}; sync \longrightarrow \mathbf{skip}; sync \longrightarrow \mathbf{skip}; detect \longrightarrow \mathbf{skip}; sync \longrightarrow \mathbf
                              [\{voxel\_map, work\} \mid \{|detect, sync|\} \mid \varnothing]
           \begin{pmatrix} \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; \; getWork \; ? \; work \longrightarrow \mathbf{var} \; colls1 : int \; \bullet \\ \mathbf{wait} \; 0 \; ... \; CPC_{TB} \; ; \; (\exists \; i? : \mathbb{Z} \bullet \; CalcPartCollisions[colls1/pcolls!] \land \; i? = 1); \\ recColls! \; colls1 \longrightarrow \mathbf{skip} \; ; \; output \; ? \; y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X \end{pmatrix}
                              \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
                \begin{pmatrix} \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork \; ? \; work \longrightarrow \mathbf{var} \; colls \; 2 : int \; \bullet \\ \mathbf{wait} \; 0 \; ... \; CPC_{TB} \; ; \; (\exists \; i? \; : \; \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \wedge i? = 2); \\ recColls \; ! \; colls \; 2 \longrightarrow \mathbf{skip} \; ; \; output \; ? \; y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X 
                              \llbracket \varnothing \mid \{ \mid detect, output, sync \} \mid \varnothing \rrbracket
               \begin{array}{l} \left(\mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork \, ? \, work \longrightarrow \mathbf{var} \; colls \, 3 : int \; \bullet \\ \mathbf{wait} \; 0 \; . \; CPC_{TB} \; ; \; (\exists \; i? : \mathbb{Z} \bullet CalcPartCollisions[colls \, 3/pcolls \, !] \; \land \; i? = 3); \\ \left(recColls \, ! \; colls \, 3 \longrightarrow \mathbf{skip} \; ; \; output \, ? \; y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X \end{array}\right)
                                \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
              \begin{pmatrix} \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork \; ? \; work \longrightarrow \mathbf{var} \; colls \; 4 : int \; \bullet \\ \mathbf{wait} \; 0 \; ... \; CPC_{TB} \; ; \; (\exists \; i? : \mathbb{Z} \bullet \; CalcPartCollisions[colls \; 4/pcolls \; !] \; \land \; i? = 4); \\ recColls \; ! \; colls \; 4 \longrightarrow \mathbf{skip} \; ; \; output \; ? \; y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X 
                              \llbracket \varnothing \mid \{ \mid output, sync \mid \} \mid \varnothing \rrbracket
               \begin{array}{l} \left(\mu X \bullet output ? collisions \longrightarrow \right. \\ \mathbf{var} \ colls : int \bullet \mathbf{wait} \ 0 \ldots CC_{TB} \ ; \ CalcCollisions; \\ \left(output\_collisions ! \ colls \longrightarrow \mathbf{skip}\right) \blacktriangleleft OUT\_DL \ ; \ \ sync \longrightarrow \mathbf{skip} \end{array}
               [\{currentFrame, state, voxel\_map, work\} \mid \{\} \ sync \} \mid \emptyset \ ] Cycle
[{currentFrame, state, voxel\_map, work}] | {setFrameState, getFrameState,}
                        setWork, getWork, initColls, recColls, output [ ] { collisions } ]
        \mathbf{var}\ currentFrame: RawFrame ullet
                                                                            : State Table ullet (setFrameState? v_1? v_2 \longrightarrow currentFrame, state := v_1, v_2) \Box (getFrameState! currentFrame! state \longrightarrow skip)
        \mathbf{var} \ state : StateTable \bullet
   (\mathbf{var} \ work : Partition \bullet \\ \mu X \bullet \begin{pmatrix} (setWork? v \longrightarrow work := v) \ \Box \\ (getWork! \ work \longrightarrow \mathbf{skip}) \end{pmatrix}; \ X \end{pmatrix} \parallel \\ \mu X \bullet \begin{pmatrix} (initColls \longrightarrow collisions := 0) \ \Box \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 \ldots RC_{TB}; \ collisions := collisions + x)) \ \Box \end{pmatrix};
```

 $\{ reduce, detect, output, setFrameState, getFrameState, getWork, setWork, initColls, recColls, sync \} \}$ 

The highlighted communication has been inserted into the reducer handler during the collapsing of one of the control fragments. We also observe that right-hand action now encapsulates the shared data for the collisions variable. The variable declaration, however, has not been localised yet by the law.

... 

"application of elementary distribution laws to localised variable declarations"

```
(\mu X \bullet (next\_frame?frame \rightarrow (wait 0 ... SF_{TB}; StoreFrame)) \blacktriangleleft INP\_DL;
        setFrameState! currentFrame! state \longrightarrow \mathbf{skip}; reduce \longrightarrow \mathbf{skip}; sync \longrightarrow X
              [[\{currentFrame, state\} \mid \{| reduce, sync |\} \mid \{voxel\_map, work\}]]
         \mu X \bullet reduce \longrightarrow \mathbf{skip}; getFrameState? currentFrame? state \longrightarrow
         wait 0 ... RPW_{TB}; ReduceAndPartitionWork;
         setWork!work \longrightarrow skip; initColls \longrightarrow skip; detect \longrightarrow skip; sync \longrightarrow X
              [\{voxel\_map, work\} \mid \{|detect, sync|\} \mid \varnothing]
         \mu X \bullet detect \longrightarrow \mathbf{skip}; getWork? work \longrightarrow \mathbf{var} \ colls1: int \bullet
         wait 0.. CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1);
       \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
        \mu X \bullet detect \longrightarrow \mathbf{skip}; \ qetWork? work \longrightarrow \mathbf{var} \ colls2: int \bullet
        wait 0.. CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2);
        recColls! colls2 \longrightarrow \mathbf{skip}; output? y \longrightarrow \mathbf{skip}; sync \longrightarrow X
              \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
         \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork ? work \longrightarrow \mathbf{var} \; colls3 : int \bullet \mathbf{var} \; colls4 : int \bullet \mathbf{var}
         wait 0.. CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3);
        \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
         (\mu X \bullet detect \longrightarrow \mathbf{skip}; getWork? work \longrightarrow \mathbf{var} \ colls4: int \bullet)
         wait 0 \dots CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4);
        recColls ! colls 4 \longrightarrow \mathbf{skip} ; output ? y \longrightarrow \mathbf{skip} ; sync \longrightarrow X
              \llbracket \varnothing \mid \{ \mid output, sync \mid \} \mid \varnothing \rrbracket
        '\mu X \bullet output? collisions \longrightarrow
         var colls: int • wait 0 ... CC_{TB}; CalcCollisions;
         (output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL ; sync \longrightarrow X
       [\{currentFrame, state, voxel\_map, work\} \mid \{\{sync\}\} \mid \emptyset [] Cycle\}
[[currentFrame, state, voxel\_map, work]]
           \{ | setFrameState, qetFrameState, setWork, qetWork, initColls, recColls, output \} \mid \emptyset \}
   \mathbf{var}\ currentFrame: RawFrame ullet
   \mathbf{var} \ state : StateTable \bullet
             \mu X \bullet \begin{pmatrix} (setFrameState~?~v_1~?~v_2 \longrightarrow currentFrame, state := v_1, v_2) \ \square \\ (getFrameState~!~currentFrame~!~state \longrightarrow \mathbf{skip}) \end{pmatrix}
  (\mathbf{var}\ work : Partition \bullet) \\ \mu X \bullet \begin{pmatrix} (setWork ? v \longrightarrow work := v) \ \Box \\ (getWork ! work \longrightarrow \mathbf{skip}) \end{pmatrix}; X \end{pmatrix} \parallel
  \mathbf{var}\ collisions: int ullet
```

This concludes Stage 2 of the SH phase. The refinement strategy is more involved here, requiring several auxiliary steps before and after application of the respective high-level law. Whereas the application of Pattern 1 and Pattern 2 generally lend themselves fairly well for automation, some of the steps during the application of Pattern 3 are expected to require more guidance by and refinement effort by the developer.

The next stage targets the synchronisations on the channel *output*. This on one hand releases the output handler but also acts as a barrier for the detection handlers.

## 5.3.4 Stage 3

Stage 3 of SH introduces shared data to refine control mechanisms that still may exist in the model. In the example, this is the the barrier-like synchronisation on  $output?y \longrightarrow \mathbf{skip}$  within the detection handlers. As in the previous stage, it turns out that we cannot apply the respective law barrier-sync-design immediately but have to perform some pre-processing to transform the System action into the right shape. Below we recapture the current parallelism of handlers.

```
(\mu X \bullet (next\_frame ? frame \longrightarrow (\mathbf{wait} \ 0 ... SF_{TB} \ ; \ StoreFrame)) \blacktriangleleft INP\_DL; \ setFrameState ! currentFrame ! state \longrightarrow \mathbf{skip} \ ; \ reduce \longrightarrow \mathbf{skip} \ ; \ sync \longrightarrow X)
            [{currentFrame, state} \mid {reduce, sync} \mid {voxel\_map, work}]
        \mu X \bullet reduce \longrightarrow \mathbf{skip}; \ getFrameState? currentFrame? state \longrightarrow
       wait 0...RPW_{TB}; ReduceAndPartitionWork;

setWork!work \longrightarrow skip; initColls \longrightarrow skip; detect \longrightarrow skip; sync \longrightarrow X)
            [\{voxel\_map, work\} \mid \{|detect, sync|\} \mid \varnothing]
      (\mu X \bullet detect \longrightarrow \mathbf{skip}; getWork? work \longrightarrow \mathbf{var} \ colls1: int \bullet \mathbf{wait} \ 0... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1); (recColls! colls1 \longrightarrow \mathbf{skip}; output? y \longrightarrow \mathbf{skip}; sync \longrightarrow X)
            \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
      \begin{pmatrix} \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork \; ? \; work \longrightarrow \mathbf{var} \; colls \; 2 : int \bullet \\ \mathbf{wait} \; 0 \; ... \; CPC_{TB} \; ; \; (\exists \; i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \wedge i? = 2); \\ recColls \; ! \; colls \; 2 \longrightarrow \mathbf{skip} \; ; \; \; \underbrace{output} \; ? \; y \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X 
            \llbracket \varnothing \mid \{ \mid detect, output, sync \} \mid \varnothing \rrbracket
      (\mu X \bullet detect \longrightarrow \mathbf{skip}; getWork? work \longrightarrow \mathbf{var} \ colls3: int \bullet \mathbf{wait} \ 0... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3); \\ (recColls! colls3 \longrightarrow \mathbf{skip}; output? y \longrightarrow \mathbf{skip}; sync \longrightarrow X)
            \llbracket \varnothing \mid \{ | detect, output, sync \mid \} \mid \varnothing \rrbracket
       (\mu X \bullet detect \longrightarrow \mathbf{skip}; getWork? work \longrightarrow \mathbf{var} \ colls4: int \bullet \mathbf{wait} \ 0... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4); \\ (recColls! colls4 \longrightarrow \mathbf{skip}; output? y \longrightarrow \mathbf{skip}; sync \longrightarrow X
            \llbracket \varnothing \mid \{ \mid output, sync \mid \} \mid \varnothing \rrbracket
       [\{currentFrame, state, voxel\_map, work\} \mid \{\} \ sync \} \mid \emptyset \ [] \ Cycle
[\{currentFrame, state, voxel\_map, work\}]
          \{ | setFrameState, qetFrameState, setWork, qetWork, initColls, recColls, output \} \mid \varnothing \}
                 X \bullet \begin{pmatrix} (initColls \longrightarrow collisions := 0) \ \Box \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 ... RC_{TB}; \ collisions := collisions + x)) \ \Box \\ (\underbrace{output! \ collisions \longrightarrow \mathbf{skip}})
```

 $\{ reduce, detect, output, setFrameState, getFrameState, getWork, setWork, initColls, recColls, sync \} \}$ 

All synchronisations on the *output* channel are highlighted in red. This channel, like *reduce* and *detect* in Stage 1, fulfils a dual purpose of communicating the number of detected collisions to the output handler as well as acting as a synchronisation barrier for the detector handlers and output handler to ensure that

the collisions are only communicated once all four detector handlers have committed their results. Hence, output controls when the output handler is release.

Thought the scenario is somewhat similar to the one for Pattern 1, here we cannot simply apply the seq-share-1 law in order to introduce the control channel for the software event that releases the output handler. For one this is because the data communicated through *output* has already been encapsulated in Stage 2. We therefore use a different strategy outlined below.

- 1. Introduce a new typeless channel *output* to replace the originial channel *output* of type *int* in all places where we are merely interested in the control aspect and rename *output* into *getColls*. The new channel isolates the control aspect where *getColls* provides the means for accessing the data.
- 2. Eliminate the residual parallel fragment results from the above introduction.
- 3. Use the synch-barrier-design law to refine the barrier synchronisation mechanism.

To proceed with (1) we require a specialised channel replacement law. Possibly, this law can be specified in a more general manner, for instance, by some inductively-defined substitution procedure. Essentially, it replaces occurrences of prefixes of the form  $output ? x \longrightarrow A$  by simple prefixes  $outout \longrightarrow A$  where x is not used in A and otherwise by  $getColls ? x \longrightarrow A$ . This replacement is justified by two facts.

- 1. The original channel *output* is concealed in the system action.
- 2. Inclusion of an additional parallel control fragment:

```
(\mu X \bullet output \longrightarrow getColls ? y \longrightarrow skip ; sync \longrightarrow X)
```

The control fragment is expected to be eliminated in the usual way. Here, this is collapsing it with the handler that outputs the collisions. The local refinement for this transformation is given below.

```
\begin{pmatrix} \mu X \bullet getColls ? collisions \longrightarrow \\ \mathbf{var} \ colls : int \bullet \mathbf{wait} \ 0 \dots CC_{TB} \ ; \ CalcCollisions; \\ (output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL \ ; \ sync \longrightarrow X \end{pmatrix} \\ \llbracket \varnothing \mid \{ \mid getColls \mid \} \mid \varnothing \rrbracket \\ (\mu X \bullet output \longrightarrow getColls ? y \longrightarrow \mathbf{skip} \ ; \ sync \longrightarrow X ) \end{pmatrix}
```

⊑ "distributing recursion in the parallel actions and collapsing the parallelism using step laws"

```
\begin{pmatrix} \mu X \bullet output \longrightarrow getColls ? collisions \longrightarrow \\ \mathbf{var} \ colls : int \bullet \mathbf{wait} \ 0 .. \ CC_{TB} \ ; \ CalcCollisions; \\ (output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL \ ; \ sync \longrightarrow X \end{pmatrix}
```

The collapsing of parallelism introduces the prefix  $output \longrightarrow ...$  ' into the output handler and thereby establishes the desired design in which output acts as a software event that releases this handler.

The above refinement, as noted, rests on a channel replacement law that, however, will not be discussed in more detail here; instead, we just present the *System* action that we expect to result from its application.

⊑ "specialised channel replacement law and distributing residual parallel control action"

```
(\mu X \bullet (next\_frame? frame \longrightarrow (\mathbf{wait} \ 0 \dots SF_{TB}; StoreFrame)) \blacktriangleleft INP\_DL;
     setFrameState! currentFrame! state \longrightarrow skip; reduce \longrightarrow skip; sync \longrightarrow X
        [[\{currentFrame, state\} \mid \{| reduce, sync \}| \mid \{voxel\_map, work\}]]
     \mu X \bullet reduce \longrightarrow \mathbf{skip}; getFrameState? currentFrame? state \longrightarrow
      wait 0 ... RPW_{TB}; ReduceAndPartitionWork;
     setWork!work \longrightarrow \mathbf{skip} \; ; \; initColls \longrightarrow \mathbf{skip} \; ; \; detect \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X \; ,
        [\{voxel\_map, work\} \mid \{|detect, sync|\} \mid \varnothing]
     \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork ? work \longrightarrow \mathbf{var} \; colls1 : int \bullet
     \mathbf{wait} \ 0 \dots CPC_{TB} \ ; \ (\exists \ i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land \ i? = 1);
     recColls! colls1 \longrightarrow \mathbf{skip}; output \longrightarrow \mathbf{skip}; sync \longrightarrow X
        \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
     \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork ? work \longrightarrow \mathbf{var} \; colls2 : int \bullet
     wait 0 \dots CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2);
    recColls! colls2 \longrightarrow \mathbf{skip}; output \longrightarrow \mathbf{skip}; sync \longrightarrow X
        \llbracket \varnothing \mid \{ \mid detect, output, sync \} \mid \varnothing \rrbracket
     \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork ? work \longrightarrow \mathbf{var} \; colls3 : int \bullet
     wait 0 \dots CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3);
    recColls ! colls 3 \longrightarrow \mathbf{skip} ; output \longrightarrow \mathbf{skip} ; sync \longrightarrow X
        \llbracket \varnothing \mid \{ \mid detect, output, sync \} \mid \varnothing \rrbracket
     (\mu X \bullet detect \longrightarrow \mathbf{skip}; getWork? work \longrightarrow \mathbf{var} \ colls4: int \bullet)
     wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4);
     recColls ! colls 4 \longrightarrow \mathbf{skip} ; output \longrightarrow \mathbf{skip} ; sync \longrightarrow X
        \llbracket \varnothing \mid \{ \mid output, sync \} \mid \varnothing \rrbracket
     '\mu X ullet output \longrightarrow getColls ? collisions \longrightarrow
     \mathbf{var}\ colls: int\ \bullet\ \mathbf{wait}\ 0\dots CC_{TB}\ ;\ \ Calc Collisions;
     (output\_collisions! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL; sync -
    [\{currentFrame, state, voxel\_map, work\} \mid \{\{sync\}\} \mid \emptyset ] Cycle
[[currentFrame, state, voxel\_map, work]]
      \{|[set/get]FrameState, [set/get]Work, initColls, recColls, getColls, output]\}| \varnothing \|
  \mathbf{var}\ currentFrame: RawFrame ullet
  \mathbf{var} \ state : StateTable ullet
        \mu X \bullet \begin{pmatrix} (setFrameState~?~v_1~?~v_2 \longrightarrow currentFrame, state := v_1, v_2) \ \square \\ (getFrameState~!~currentFrame~!~state \longrightarrow \mathbf{skip}) \end{pmatrix}
(\mathbf{var}\ work : Partition \bullet) \\ \mu X \bullet \begin{pmatrix} (set Work ? v \longrightarrow work := v) \ \Box \\ (get Work ! work \longrightarrow \mathbf{skip}) \end{pmatrix}; X \end{pmatrix} \parallel
 \mathbf{var}\ collisions: int ullet
                        (initColls \longrightarrow collisions := 0) \square

(recColls ? x \longrightarrow (\mathbf{wait} \ 0 ... RC_{TB} \ ; \ collisions := collisions + x)) \square

(getColls ! collisions \longrightarrow \mathbf{skip})
```

 $\{|reduce, detect, output, [set/get]FrameState, [set/get]Work, initColls, recColls, getColls, sync |\}$ 

We observe the new channels *output* and *getColls*. Moreover, all channels used int the right-hand parallel

action that encapsulates shared data are now channels that model either method calls or direct access to shared variables. We now proceed applying the law sync-barrier-design.

```
\mu X \bullet (next\_frame? frame \longrightarrow (wait 0 ... SF_{TB}; StoreFrame)) \blacktriangleleft INP\_DL;
                  setFrameState ! currentFrame ! state \longrightarrow \mathbf{skip} \; ; \; reduce \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X 
                      [[\{currentFrame, state\} \mid \{|reduce, sync|\} \mid \{voxel\_map, work\}]]
                  \mu X \bullet reduce \longrightarrow \mathbf{skip}; getFrameState? currentFrame? state \longrightarrow
                 wait 0..RPW_{TB}; ReduceAndPartitionWork;

\langle setWork!work \longrightarrow skip ; initColls \longrightarrow skip ; detect \longrightarrow skip ; sync \longrightarrow X \rangle
                      [\{voxel\_map, work\} \mid \{|detect, sync|\} \mid \varnothing]
                   \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork ? work \longrightarrow \mathbf{var} \; colls1 : int \bullet
                  wait 0.. CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1);
                 \backslash recColls! colls1 \longrightarrow \mathbf{skip}; \ \textit{notify}! 1 \longrightarrow \mathbf{skip}; \ \textit{sync} \longrightarrow X
                      \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
                   \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork ? work \longrightarrow \mathbf{var} \; colls2 : int \bullet
                  wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2);
                  recColls! colls2 \longrightarrow \mathbf{skip}; notify! 2 \longrightarrow \mathbf{skip}; sync \longrightarrow X
                      \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
                  \begin{pmatrix} \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork \; ? \; work \longrightarrow \mathbf{var} \; colls \; 3 : int \bullet \\ \mathbf{wait} \; 0 \; ... \; CPC_{TB} \; ; \; (\exists \; i? : \mathbb{Z} \bullet \; CalcPartCollisions[colls \; 3/pcolls \; !] \wedge i? = 3); \\ recColls \; ! \; colls \; 3 \longrightarrow \mathbf{skip} \; ; \; \; notify \; ! \; 3 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X 
                      \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
                  '\mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork ? work \longrightarrow \mathbf{var} \; colls4 : int \bullet
                wait 0.. CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4); recColls! colls4 \longrightarrow \mathbf{skip}; notify! 4 \longrightarrow \mathbf{skip}; sync \longrightarrow X
                      [\![\varnothing\mid\{\mid output, sync\mid\}\mid\varnothing]\!]
                  (\mu X \bullet output \longrightarrow getColls ? collisions \longrightarrow 
\mathbf{var}\ colls : int \bullet \mathbf{wait}\ 0 ...\ CC_{TB}\ ; \ CalcCollisions;
(output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL\ ; \ sync \longrightarrow
                 [\{currentFrame, state, voxel\_map, work\} \mid \{\} \ sync \} \mid \emptyset \ ] Cycle
            [\{currentFrame, state, voxel\_map, work\} \mid \{\{detect, start, sync\}\} \mid \emptyset]
  (\mu X \bullet start \longrightarrow detect \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X)
       [\{currentFrame, state, voxel\_map, work\}]
               \{|[set/get]FrameState, [set/get]Work, initColls, recColls, getColls]\} | \varnothing |
\mu X \bullet \begin{pmatrix} (start \longrightarrow active := 1 \dots n) \\ \square \\ (notify? x \longrightarrow \begin{pmatrix} active := active \setminus \{x\}; \\ \text{if } active = \varnothing \longrightarrow output \longrightarrow skip} \\ \square \\ \neg active = \varnothing \longrightarrow skip \end{pmatrix}) ; X
```

 $\{ | reduce, detect, output, start, notify, [set/get] FrameState, [set/get] Work, initColls, recColls, getColls, sync ] \}$ Once again a control fragment arises as a consequence of applying the law. We distribute it into the reducer

handler. This gives rise to the following *System* action.

⊑ "distribution of control fragment using parallel step laws"

```
\mu X \bullet (next\_frame?frame \rightarrow (wait 0 ... SF_{TB}; StoreFrame)) \blacktriangleleft INP\_DL;
          setFrameState! currentFrame! state \longrightarrow \mathbf{skip}; reduce \longrightarrow \mathbf{skip}; sync \longrightarrow X
               \llbracket \{\mathit{currentFrame}, \mathit{state}\} \mid \{ | \mathit{reduce}, \mathit{sync} | \} \mid \{ \mathit{voxel\_map}, \mathit{work} \} \rrbracket
          (\mu X \bullet reduce \longrightarrow \mathbf{skip}; getFrameState? currentFrame? state \longrightarrow
           wait 0...RPW_{TB}; ReduceAndPartitionWork; setWork! work \longrightarrow skip; initColls \longrightarrow skip; start \longrightarrow skip; detect \longrightarrow skip; sync \longrightarrow X
                [\{voxel\_map, work\} \mid \{|detect, sync|\} \mid \varnothing]
       \begin{pmatrix} \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork \; ? \; work \longrightarrow \mathbf{var} \; colls1 : int \bullet \\ \mathbf{wait} \; 0 \; ... \; CPC_{TB} \; ; \; (\exists \; i? : \mathbb{Z} \bullet \; CalcPartCollisions[colls1/pcolls!] \land \; i? = 1); \\ recColls! \; colls1 \longrightarrow \mathbf{skip} \; ; \; notify! \; 1 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X \end{pmatrix}
                \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
         \mu X \bullet detect \longrightarrow \mathbf{skip}; getWork? work \longrightarrow \mathbf{var}\ colls2: int \bullet \mathbf{vait}\ 0... CPC_{TB}; (\exists i?: \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2);
          recColls! colls2 \longrightarrow \mathbf{skip}; notify! 2 \longrightarrow \mathbf{skip}; sync \longrightarrow X
                \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
          \begin{pmatrix} \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork \; ? \; work \longrightarrow \mathbf{var} \; colls \; 3 : int \; \bullet \\ \mathbf{wait} \; 0 \; ... \; CPC_{TB} \; ; \; (\exists \; i? \; : \; \mathbb{Z} \bullet CalcPartCollisions[colls \; 3/pcolls \; !] \; \land \; i? \; = \; 3); \\ \langle recColls \; ! \; colls \; 3 \longrightarrow \mathbf{skip} \; ; \; \; notify \; ! \; 3 \longrightarrow \mathbf{skip} \; ; \; \; sync \longrightarrow X 
                \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
        \begin{pmatrix} \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork \; ? \; work \longrightarrow \mathbf{var} \; colls4 : int \bullet \\ \mathbf{wait} \; 0 \ldots CPC_{TB} \; ; \; (\exists \; i? : \mathbb{Z} \bullet \; CalcPartCollisions[colls4/pcolls!] \wedge i? = 4); \\ recColls ! \; colls4 \longrightarrow \mathbf{skip} \; ; \; notify ! \; 4 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X \end{pmatrix} 
               \llbracket \varnothing \mid \{ \mid output, sync \mid \} \mid \varnothing \rrbracket
         \begin{pmatrix} \mu X \bullet output \longrightarrow getColls ? collisions \longrightarrow \\ \mathbf{var} \ colls : int \bullet \mathbf{wait} \ 0 \dots CC_{TB} \ ; \ CalcCollisions; \\ (output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL \ ; \ sync \longrightarrow X 
         [{currentFrame, state, voxel\_map, work} | {sync} | \varnothing ] Cycle
  [\{currentFrame, state, voxel\_map, work\}]
             \{|[set/get]FrameState, [set/get]Work, initColls, recColls, getColls, output]\} \mid \varnothing||
\mu X \bullet \begin{pmatrix} (start \longrightarrow active := 1 \dots n) \\ \square \\ (notify ? x \longrightarrow \begin{pmatrix} active := active \setminus \{x\}; \\ \mathbf{if} \ active = \varnothing \longrightarrow output \longrightarrow \mathbf{skip} \\ \square \neg \ active = \varnothing \longrightarrow \mathbf{skip} \end{pmatrix}) \end{pmatrix}; X
```

 $\{|reduce, detect, output, start, notify, [set/get]FrameState, [set/get]Work, initColls, recColls, getColls, sync]\}$ 

This concludes Stage 3. All remaining control behaviour has been removed and as a result, additional shared data was introduced. In the next and last stage of the SH phase we factor out the *MArea* action and collapse parallelism between actions that encapsulate shared data in the same object in the program.

### 5.3.5 Stage 4

MArea =

The encapsulated data that has emerged is now extracted in a separate local action MArea.

 $\begin{aligned} & \text{MArea} \; \widehat{=} \\ & \left( \begin{array}{c} \text{var } \textit{currentFrame} : \textit{RawFrame} \; \bullet \\ \text{var } \textit{state} : \textit{StateTable} \; \bullet \\ & \mu X \; \bullet \; \left( \begin{array}{c} (\textit{setFrameState} \, ? \, v_1 \, ? \, v_2 \longrightarrow \textit{currentFrame}, \textit{state} := v_1, v_2) \; \square \right) \; ; \; X \right) \\ & \left( \begin{array}{c} \text{var } \textit{work} : \textit{Partition} \; \bullet \\ & \mu X \; \bullet \; \left( \begin{array}{c} (\textit{setWork} \, ? \, v \longrightarrow \textit{work} := v) \; \square \\ & (\textit{getWork} \, ! \, \textit{work} \longrightarrow \textit{skip}) \end{array} \right) \; ; \; X \right) \\ & \left( \begin{array}{c} \text{var } \textit{collisions} : \text{int} \; \bullet \\ & (\textit{getWork} \, ! \, \textit{work} \longrightarrow \textit{skip}) \end{array} \right) \; ; \; X \right) \\ & \left( \begin{array}{c} \text{var } \textit{collisions} : \text{int} \; \bullet \\ & (\textit{recColls} \, ? \, x \longrightarrow (\textit{wait} \, 0 \ldots \textit{RC}_{TB} \; ; \; \textit{collisions} := \textit{collisions} + x)) \; \square \right) \; ; \; X \right) \\ & \left( \begin{array}{c} \text{var } \textit{active} : \; \mathbb{P} \left( 1 \ldots n \right) \; \bullet \\ & (\textit{getColls} \, ! \, \textit{collisions} \longrightarrow \textit{skip} \end{array} \right) \\ & \left( \begin{array}{c} \text{var } \textit{active} : \; \mathbb{P} \left( 1 \ldots n \right) \; \bullet \\ & (\textit{start} \longrightarrow \textit{active} := 1 \ldots n) \\ & \left( \begin{array}{c} \textit{active} := \textit{active} \setminus \{x\}; \\ \text{if } \textit{active} = \varnothing \longrightarrow \textit{output} \longrightarrow \textit{skip} \\ & \left( \begin{array}{c} i \\ notify \; ? \; x \longrightarrow \begin{pmatrix} \textit{active} := \textit{active} \setminus \{x\}; \\ & \left( \begin{array}{c} \textit{active} := \mathscr{O} \longrightarrow \textit{output} \longrightarrow \textit{skip} \\ & \left( \begin{array}{c} i \\ notify \; ? \; x \longrightarrow \begin{pmatrix} \textit{active} := \mathscr{O} \longrightarrow \textit{output} \longrightarrow \textit{skip} \\ & \left( \begin{array}{c} i \\ notify \; ? \; x \longrightarrow \begin{pmatrix} \textit{active} := \mathscr{O} \longrightarrow \textit{output} \longrightarrow \textit{skip} \\ & \left( \begin{array}{c} i \\ notify \; ? \; x \longrightarrow \begin{pmatrix} \textit{active} := \mathscr{O} \longrightarrow \textit{output} \longrightarrow \textit{skip} \\ & \left( \begin{array}{c} i \\ notify \; ? \; x \longrightarrow \begin{pmatrix} \textit{active} := \mathscr{O} \longrightarrow \textit{output} \longrightarrow \textit{skip} \\ & \left( \begin{array}{c} i \\ notify \; ? \; x \longrightarrow \begin{pmatrix} \textit{active} := \mathscr{O} \longrightarrow \textit{output} \longrightarrow \textit{skip} \\ & \left( \begin{array}{c} i \\ notify \; ? \; x \longrightarrow \begin{pmatrix} \textit{active} := \mathscr{O} \longrightarrow \textit{output} \longrightarrow \textit{skip} \\ & \left( \begin{array}{c} i \\ notify \; ? \; x \longrightarrow \begin{pmatrix} \textit{active} := \mathscr{O} \longrightarrow \textit{output} \longrightarrow \textit{skip} \\ & \left( \begin{array}{c} i \\ notify \; ? \; x \longrightarrow \begin{pmatrix} \textit{active} := \mathscr{O} \longrightarrow \textit{output} \longrightarrow \textit{skip} \\ & \left( \begin{array}{c} i \\ notify \; ? \; x \longrightarrow \begin{pmatrix} \textit{active} := \mathscr{O} \longrightarrow \textit{output} \longrightarrow \textit{skip} \\ & \left( \begin{array}{c} i \\ notify \; ? \; x \longrightarrow \begin{pmatrix} \textit{active} := \mathscr{O} \longrightarrow \textit{output} \longrightarrow \textit{skip} \\ & \left( \begin{array}{c} i \\ notify \; ? \; x \longrightarrow \begin{pmatrix} \textit{active} := \mathscr{O} \longrightarrow \textit{skip} \\ & \left( \begin{array}{c} i \\ notify \; ? \; x \longrightarrow \begin{pmatrix} \textit{active} := \mathscr{O} \longrightarrow \textit{skip} \\ & \left( \begin{array}{c} i \\ notify \; ? \; x \longrightarrow \begin{pmatrix} \textit{active} := \mathscr{O} \longrightarrow \textit{skip} \\ & \left( \begin{array}{c} i \\ notify \; ? \; x \longrightarrow \begin{pmatrix} \textit{ac$ 

Because the shared variables *currentFrame*, *state*, *work* and *collisions* are fields of the same object in the program (class CDxMission), we collapse the parallelism of recursions into a single recursion to reflect that the underlying methods synchronise on a common lock.

 $\sqsubseteq$  "specialised laws to collapse parallelisms of recursions in MArea"

 $\mathbf{var}\ currentFrame: RawFrame ullet$ 

 $\begin{array}{c} \mathbf{var} \ state : StateTable \bullet \\ \mathbf{var} \ work : Partition \bullet \\ \mathbf{var} \ collisions : int \bullet \\ & \begin{pmatrix} (setFrameState? v_1? v_2 \longrightarrow currentFrame, state := v_1, v_2) \ \square \\ (getFrameState! \ currentFrame! \ state \longrightarrow \mathbf{skip}) \ \square \\ (setWork? v \longrightarrow work := v) \ \square \\ (getWork! \ work \longrightarrow \mathbf{skip}) \ \square \\ (initColls \longrightarrow collisions := 0) \ \square \\ (recColls? x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB}; \ collisions := collisions + x)) \ \square \\ (getColls! \ collisions \longrightarrow \mathbf{skip}) \\ \end{pmatrix} \\ \begin{pmatrix} \mathbf{var} \ active : \mathbb{P} \ (1 \dots n) \bullet \\ (start \longrightarrow active := 1 \dots n) \\ \square \\ (notify? x \longrightarrow \begin{pmatrix} active := active \setminus \{x\}; \\ \mathbf{if} \ active = \varnothing \longrightarrow output \longrightarrow \mathbf{skip} \\ \square \\ - active = \varnothing \longrightarrow \mathbf{skip} \end{pmatrix}) \\ \vdots \ X \end{pmatrix}$ 

The precise laws needed for the above refinement are future work and furthermore they are in general likely to be non-compositional. Namely, because collapsing parallelism above results in an action that is less willing

to communicate in that nondeterministic waits may be introduced. Above, that is **wait**  $0 ... RC_{TB}$ . The refinement may be justified by reducing time budgets in other places but this is future work.

For the *System* action we obtain the following definition.

```
System =
                       \mu X \bullet (next\_frame ? frame \longrightarrow (wait 0 ... SF_{TB}; StoreFrame)) \blacktriangleleft INP\_DL;
                       setFrameState! currentFrame! state \longrightarrow \mathbf{skip}; reduce \longrightarrow \mathbf{skip}; sync \longrightarrow X
                          [[\{currentFrame, state\} \mid \{[reduce, sync]\} \mid \{voxel\_map, work\}]]
                       \mu X \bullet reduce \longrightarrow \mathbf{skip}; getFrameState? currentFrame? state \longrightarrow
                       wait 0 ... RPW_{TB}; ReduceAndPartitionWork; setWork!work \longrightarrow skip;
                       initColls \longrightarrow \mathbf{skip} \; ; \; start \longrightarrow \mathbf{skip} \; ; \; detect \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X
                          \llbracket \{voxel\_map, work\} \mid \{\mid detect, sync \mid\} \mid \varnothing \rrbracket
                       \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork ? work \longrightarrow \mathbf{var} \; colls1 : int \bullet
                      wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1); (\forall i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1); (\forall i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1);
                          \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
                       \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork ? work \longrightarrow \mathbf{var} \; colls2 : int \bullet \mathbf{wait} \; 0 \ldots CPC_{TB} \; ; \; (\exists \; i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land \; i? = 2);
                      recColls! colls2 \longrightarrow \mathbf{skip}; notify!2 \longrightarrow \mathbf{skip}; sync \longrightarrow X
                          \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
                       \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork ? work \longrightarrow \mathbf{var} \; colls3 : int \bullet
                       wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3);
                       recColls! colls3 \longrightarrow \mathbf{skip}; notify! 3 \longrightarrow \mathbf{skip}; sync \longrightarrow X
                          \llbracket \varnothing \mid \{ \mid detect, output, sync \mid \} \mid \varnothing \rrbracket
                       \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork ? work \longrightarrow \mathbf{var} \; colls4 : int \bullet
                       wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4);
                       recColls! colls4 \longrightarrow \mathbf{skip}; notify! 4 \longrightarrow \mathbf{skip}; sync \longrightarrow X
                          \llbracket \varnothing \mid \{ \mid output, sync \} \mid \varnothing \rrbracket
                       (\mu X \bullet output \longrightarrow getColls? collisions \longrightarrow 
\mathbf{var}\ colls: int \bullet \mathbf{wait}\ 0 ...\ CC_{TB};\ CalcCollisions;
(output\_collisions! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL;\ sync \rightarrow \mathbf{skip})
                      [[\{currentFrame, state, voxel\_map, work\} \mid \{[sync]\} \mid \varnothing]] Cycle
                 [\{currentFrame, state, voxel\_map, work\}]
                        \{ [set/get] FrameState, [set/get] Work, initColls, recColls, getColls, output] \} \mid \varnothing [
           MArea
             \{|reduce, detect, output, start, notify, [set/get] FrameState, [set/get] Work, initColls, recColls, \}
```

getColls, sync  $\}$ 

This concludes Stage 4 and thereby the SH phase. We finally present the entire process for SH.

### 5.3.6 Process

The complete process for the SH phase is presented below. Its state and data operations are in fact the same as those of  $CDxE\_MH$ , apart from SetCollisionsFromParts having been removed.

```
system CDxE\_SH \cong \mathbf{begin}
\mathbf{state}\ \mathit{CDxSHState}\ ==\ \mathit{CDxMHState}
       Init.
         CDxSHState'
        currentFrame' = \mathbf{new} RawFrame
        state' = \mathbf{new} \, State Table
        voxel\_map' = \mathbf{new} \ HashMap[Vector2d, List[Motion]]
        work' = \mathbf{new} \, Partition(4)
         collisions' = 0
       StoreFrame
        \Delta CDxSHState
        frame?: Frame
        \exists posns, posns' : Frame; motions, motions' : Frame
                  dom\ posns = dom\ motions \land dom\ posns' = dom\ motions' \bullet
              posns' = frame? \land
              motions' =
                        \{a: \operatorname{dom} posns' \bullet a \mapsto \operatorname{if} a \in \operatorname{dom} posns \operatorname{then} (posns' a) -_V (posns a) \operatorname{else} ZeroV\} \land
              posns = F(currentFrame) \land motions = G(currentFrame, state) \land
              posns' = F(currentFrame') \land motions' = G(currentFrame', state')
       Reduce And Partition Work
        \Delta CDxSHState
        currentFrame' = currentFrame \land state' = state
        \exists posns : Frame; motions : Frame \mid dom posns = dom motions \bullet
              posns = F(currentFrame) \land motions = G(currentFrame, state) \land formula = for
                    \forall a_1, a_2 : Aircraft \mid \{a_1, a_2\} \subseteq \text{dom } posns \bullet
                             (a_1, a_2) \in CalcCollisionSet(posns, motions) \Rightarrow
                                         \exists l : List[Motion] \mid l \in voxel\_map' . values() . elems() \bullet
                                                     MkMotion(a_1, posns\ a_1 - V\ motions\ a_1, posns\ a_1) \in l\ .\ elems() \land l
                                                      MkMotion(a_2, posns \ a_2 - V \ motions \ a_2, posns \ a_2) \in l \ . \ elems()
       CalcPartCollisions
        \Xi CDxSHState
        pcolls!:int
        i? : 1 . . 4
        pcolls! =
                              a_1 : Aircraft; \ a_2 : Aircraft \mid
                                            \exists \ l : List[Motion] \mid l \in work \ . \ getDetectorWork(i?). \ elems() \bullet lems() 
                                            \exists v_1, v_2 : Vector; w_1, w_2 : Vector \bullet
                                                      MkMotion(a_1, v_1, w_1) \in l . elems() \land
                                                       MkMotion(a_2, v_2, w_2) \in l . elems() \land
                                                       collide((v_1, w_1 - V v_1), (v_2, w_2 - V v_2))
```

```
Calc Collisions
     \Xi CDxSHState
     colls! : \mathbb{N}
     \exists posns : Frame; motions : Frame \mid dom posns = dom motions \bullet
         posns = F(currentFrame) \land motions = G(currentFrame, state) \land
         \exists \ collset : \mathbb{F}(Aircraft \times Aircraft) \mid collset = CalcCollisionSet(posns, motions) \bullet
               (\# collset = 0 \land colls! = 0) \lor (\# collset > 0 \land colls! \ge (\# collset) \operatorname{div} 2)
InputFrameHandler \stackrel{\frown}{=}
       \begin{pmatrix} \mu X \bullet (next\_frame? frame \longrightarrow (\mathbf{wait} \ 0 \dots SF_{TB} \ ; \ StoreFrame)) \blacktriangleleft INP\_DL; \\ setFrameState! currentFrame! state \longrightarrow \mathbf{skip} \ ; \ reduce \longrightarrow \mathbf{skip} \ ; \ sync \longrightarrow X \end{pmatrix}
ReducerHandler \stackrel{\frown}{=}
         \mu X \bullet reduce \longrightarrow \mathbf{skip}; getFrameState? currentFrame? state \longrightarrow
         wait 0 ... RPW_{TB}; ReduceAndPartitionWork; setWork! work \longrightarrow skip;
         initColls \longrightarrow \mathbf{skip} \; ; \; start \longrightarrow \mathbf{skip} \; ; \; detect \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X
DetectorHandler1 =
         \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork ? work \longrightarrow \mathbf{var} \; colls1 : int \bullet
         wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls1/pcolls!] \land i? = 1);
       \backslash recColls! colls1 \longrightarrow \mathbf{skip}; notify! 1 \longrightarrow \mathbf{skip}; sync \longrightarrow X
DetectorHandler2 \stackrel{\frown}{=}
         \mu X \bullet detect \longrightarrow \mathbf{skip} \; ; \; getWork ? work \longrightarrow \mathbf{var} \; colls2 : int \bullet
         wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls2/pcolls!] \land i? = 2);
        DetectorHandler3 =
         (\mu X \bullet detect \longrightarrow \mathbf{skip}; getWork? work \longrightarrow \mathbf{var} \ colls3: int \bullet)
         wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls3/pcolls!] \land i? = 3);
       \backslash recColls! colls3 \longrightarrow \mathbf{skip}; notify! 3 \longrightarrow \mathbf{skip}; sync \longrightarrow X
DetectorHandler4 =
         (\mu X \bullet detect \longrightarrow \mathbf{skip}; getWork? work \longrightarrow \mathbf{var} \ colls4: int \bullet)
         wait 0 ... CPC_{TB}; (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4); (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4); (\exists i? : \mathbb{Z} \bullet CalcPartCollisions[colls4/pcolls!] \land i? = 4);
OutputCollisionsHandler \stackrel{\frown}{=}
         '\mu X \bullet output \longrightarrow getColls? collisions \longrightarrow
         \mathbf{var}\ colls: int\ \bullet\ \mathbf{wait}\ 0\dots CC_{TB}\ ;\ \ Calc\ Collisions;
        (output\_collisions ! colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL; sync \longrightarrow X
Cycle \ \widehat{=} \ (\mu X \bullet \mathbf{wait} \ FRAME\_PERIOD ; \ sync \longrightarrow X)
```

```
System =
                 Input Frame Handler
                        [[\{currentFrame, state\} \mid \{| reduce, sync |\} \mid \{voxel\_map, work\}]]
                   (Reducer Handler)
                         [\{voxel\_map, work\} \mid \{|detect, sync|\} \mid \varnothing]
                  (Detector Handler 1
                        \llbracket \varnothing \mid \{ \mid detect, sync \mid \} \mid \varnothing \rrbracket
                  (Detector Handler 2)
                         \llbracket \varnothing \mid \{ \mid detect, sync \mid \} \mid \varnothing \rrbracket
                  (Detector Handler 3)
                         \llbracket \varnothing \mid \{ \mid detect, sync \mid \} \mid \varnothing \rrbracket
                  (Detector Handler 4)
                         \llbracket \varnothing \mid \{ | sync \} \mid \varnothing \rrbracket
                   OutputCollisionsHandler)))))
                 [\{currentFrame, state, voxel\_map, work\} \mid \{\{sync\}\} \mid \emptyset [] Cycle\}
             { reduce, detect, output, start, notify, sync,
                   setFrameState, getFrameState, setWork, getWork, initColls, recColls, getColls \}
MArea ≘
             '\mathbf{var}\ currentFrame: RawFrameullet
              \mathbf{var}\ state: StateTable ullet
              \mathbf{var}\ work: Partition ullet
              \mathbf{var}\ collisions: int ullet
                                  /(setFrameState?v1?v2 \longrightarrow (currentFrame := v1; state := v2)) \square \setminus
                                 (getFrameState ! currentFrame ! state \longrightarrow \mathbf{skip}) \square
(setWork ? v \longrightarrow work := v) \square
(getWork ! work \longrightarrow \mathbf{skip}) \square
(initColls \longrightarrow collisions := 0) \square
                                   (recColls?x \longrightarrow (\mathbf{wait} \ 0 .. RC_{TB}; \ collisions := collisions + x)) \ \Box
                                  (getColls ! collisions \longrightarrow \mathbf{skip})
              \mathbf{var}\ active: \mathbb{P}\left(1...4\right) ullet
                \mu X \bullet \begin{pmatrix} (start \longrightarrow active := \{1, 2, 3, 4\}) \\ \square \\ (notify ? x \longrightarrow \begin{pmatrix} active := active \setminus \{x\}; \\ \mathbf{if} \ active = \varnothing \longrightarrow output \longrightarrow \mathbf{skip} \\ \parallel \neg \ active = \varnothing \longrightarrow \mathbf{skip} \end{pmatrix}) \end{pmatrix}; X
                         [\![\{currentFrame, state, voxel\_map, work\}\mid
                        \{ \} setFrameState, getFrameState, setWork, getWork, \}
                                      initColls, recColls, getColls, start, notify, sync [ ] | \varnothing [ ]
                   MArea
end
```

Parts of the process that could not be parsed due to limitations of the Circus parser in CZT are highlighted.

# 5.4 Phase AR

In the AR phase we carry out algorithmic refinement. This also replaces (class) values by references to objects. We will not discussed this phase in as much detail as the previous phases for our example. There are, however, some refinements which are noteworthy and we shall briefly examine.

### Refinement of CalcCollisions

The CalcCollisions data operation is used by OutputCollisionsHandler to calculate the number of collisions from the other shared variables. We refine it by simple returning the value of collisions. For this, we have to prove the following operation refinement to discharge.

This refinement is used to simplify the Output Collisions Handler action as follows.

Output Collisions Handler

 $\sqsubseteq$  "refinement of *CalcCollisions* and copy rule"

```
 \begin{pmatrix} \mu X \bullet output \longrightarrow getColls ? collisions \longrightarrow \\ \mathbf{var} \ colls : int \bullet \mathbf{wait} \ 0 \dots CC_{TB} \ ; \ \ colls := collisions; \\ (output\_collisions ! \ colls \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL \ ; \ \ sync \longrightarrow X \end{pmatrix}
```

 $\sqsubseteq$  "elimination of local variable colls using a symmetric version of the law var-intro"

```
 \begin{pmatrix} \mu X \bullet output \longrightarrow getColls ? collisions \longrightarrow \mathbf{wait} \ 0 \dots CC_{TB}; \\ (output\_collisions ! \underline{collisions} \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL; \ sync \longrightarrow X \end{pmatrix}
```

 $\sqsubseteq$  "elimination of time budget using the law narrow-time-budget-1"

```
 \begin{pmatrix} \mu X \bullet output \longrightarrow getColls ? collisions \longrightarrow \\ (output\_collisions ! collisions \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL ; sync \longrightarrow X \end{pmatrix}
```

We thus obtain the simplified operation below for the handler outputting the collisions.

```
\begin{array}{ll} OutputCollisionsHandler \; \widehat{=} \\ \left( \begin{matrix} \mu X \bullet output \longrightarrow getColls ? \ collisions \longrightarrow \\ (output\_collisions ! \ collisions \longrightarrow \mathbf{skip}) \blacktriangleleft OUT\_DL \; ; \ \ sync \longrightarrow X \end{matrix} \right) \end{array}
```

Its behaviour is now simply to read the value of the shared collisions variable and output it on the channel  $output\_collisions$ .

### Refinement of MArea

A second algorithmic refinement worth mentioning a (data) refinement of part of the MArea action.

The highlighted action above uses and abstract variable *active* to retain information about handlers that are still active in calculating their collisions result. In Appendix A.6, we include a model for the DetectorControl class used to record this data in the program. With it, we refine *MArea* as follows.

```
 \begin{aligned} & \text{MArea} \; \widehat{=} \\ & \left( \begin{array}{c} \text{var } \textit{currentFrame} : \textit{RawFrame} \; \bullet \\ \text{var } \textit{state} : \textit{StateTable} \; \bullet \\ \text{var } \textit{work} : \textit{Partition} \; \bullet \\ \text{var } \textit{collisions} : \textit{int} \; \bullet \\ & \left( \begin{array}{c} (\textit{setFrameState} ? \textit{v1} ? \textit{v2} \longrightarrow (\textit{currentFrame} := \textit{v1} ; \; \textit{state} := \textit{v2})) \; \square \\ (\textit{getFrameState} ! \textit{currentFrame} ! \textit{state} \longrightarrow \textit{skip}) \; \square \\ (\textit{setWork} ? \textit{v} \longrightarrow \textit{work} := \textit{v}) \; \square \\ (\textit{getWork} ! \textit{work} \longrightarrow \textit{skip}) \; \square \\ (\textit{initColls} \longrightarrow \textit{collisions} := 0) \; \square \\ (\textit{recColls} ? \textit{x} \longrightarrow (\textit{wait} 0 . . \textit{RC}_{TB} ; \; \textit{collisions} := \textit{collisions} + \textit{x})) \; \square \\ (\textit{getColls} ! \textit{collisions} \longrightarrow \textit{skip}) \\ \end{array} \right) \\ & \left( \begin{array}{c} \text{var } \textit{control} : \textit{DetectorControl} \bullet \textit{control} := \textit{newM DetectorControl}; \\ (\textit{start} \longrightarrow \textit{control} . \textit{start}()) \\ \square \\ \textit{notify} ? \textit{i} \longrightarrow \\ (\textit{control} . \textit{notify}(\textit{i}); \\ \text{if } \textit{control} . \textit{done}() = \textit{jtrue} \longrightarrow \textit{output} \longrightarrow \textit{skip} \\ \square \cap \textit{control} . \textit{done}() = \textit{jtrue} \longrightarrow \textit{skip} \\ \end{array} \right) \right) \\ ; \; X \end{aligned}
```

The *control* object of type *DetectorControl* retains the number of active handlers by way of a boolean array. Synchronisations on *start* and *notify* result in calling the corresponding methods on the data object. The right-hand branch of the parallelism in fact models an *active* object now: that is a data object that interacts

through synchronisations with other processes. The actual model for the class is thus a mixture of the OhCircus class specification of the data object as well as the active part in the MArea action.

# 6 Anchor S

In this section, we sketch the S Anchor model for our case study. Since the  $\mathsf{CD}_x$  is more complex than our previous example in [6], we shall not attempt to specify the model in full detail here. Its exact shape moreover depends on the precise definition of  $\mathsf{SCJ}$  circus, which is still work in progress. Each type of  $\mathsf{SCJ}$  circus paragraph is discussed in a separate subsection in the remainder of this section.

# 6.1 $CD_x$ Safelet

In the original  $CD_x$  program, the setUp() method is defined as follows.

```
public void setup() {
    Constants.PRESIMULATE = true;
    new ImmortalEntry().run();
    new Simulator().generate();
}
```

The first and third statements configure and instantiate a simulator and thus can be ignored. The run() method of the ImmortalEntry class, called in the second line, merely initialises a static field frameBuffer of this class. This is also part of the simulation, hence the actual content of setup() is void terms of our model. The implementation of tearDown() in the original program calls a static method dumpResults() but this merely reports results of the benchmark and thus is not relevant for the model either.

Because of the above the safelet SCJCircus paragraph takes the same trivial shape as in [6].

```
\begin{array}{l} \mathbf{safelet} \ \ \mathbf{CDxSafelet} \ \ \widehat{=} \ \ \mathbf{begin} \\ \mathbf{setUp} \ \ \widehat{=} \ \ \mathbf{skip} \\ \mathbf{tearDown} \ \ \widehat{=} \ \ \mathbf{skip} \\ \mathbf{end} \end{array}
```

## 6.2 Mission Sequencer

We first have to introduce a mission identifier for the single mission of the parallel  $\mathsf{CD}_x$ .

```
CDxMissionId: MissionId
```

The model of the mission sequencer is likewise identical to the one for the serial line.

```
\begin{array}{lll} \mathbf{sequencer} & \mathbf{CDxMissionSequencer} \ \widehat{=} & \mathbf{begin} \\ \mathbf{state} & CDxMissionSequencerState \ == \ [ \ mission\_done : boolean \ ] \\ \mathbf{initial} \ \widehat{=} & mission\_done := jfalse \\ \mathbf{getNextMission} \ \widehat{=} & \mathbf{res} \ \mathbf{ret} : MissionId \ \bullet \\ \begin{pmatrix} \mathbf{if} & mission\_done \ = \ jfalse \ \longrightarrow \\ \begin{pmatrix} mission\_done \ := \ jtrue; \\ \mathbf{ret} := \ CDxMissionId \ \end{pmatrix} \\ \widehat{=} & mission\_done \ = \ jfalse \ \longrightarrow \mathbf{ret} := nullMId \\ \mathbf{fi} \end{pmatrix} \\ \mathbf{end} \\ \end{array}
```

The only difference in comparison to [6] is the use of the name *CDxMissionId* rather than *ProtocolMission*. Strictly, we could even make our job easier here since <code>getNextMission()</code> is only called once in our program, as the mission does not terminate. Because of this we have no obligation to return **null** with the second call.

# 6.3 $CD_x$ Mission

The SCJ Circus paragraph for the CDxMission class is more interesting as it takes care of the construction the various handlers and software events as well as encapsulates shared data via its MArea action.

```
mission CDxMission = begin
          state CDxMissionState
            currentFrame : \mathbf{ref} \ RawFrame
           state: \mathbf{ref}\ State\ Table
            work: \mathbf{ref}\ Partition
            collisions:int
            control: \mathbf{ref}\ Detector Control
Init ≘
                    currentFrame := \mathbf{newM} RawFrame;
                    state := \mathbf{newM} \ State Table;
                    work := \mathbf{newM} \ Partition(4);
                    collisions := 0
initialize \hat{=}
                    \mathbf{var}\ reduce : AperiodicEvent \bullet reduce := \mathbf{newEvent}\ AperiodicEvent();
                    \mathbf{var}\ detect: AperiodicEvent \bullet detect:= \mathbf{newEvent}\ AperiodicEvent();
                    \mathbf{var}\ output: AperiodicEvent \bullet output := \mathbf{newEvent}\ AperiodicEvent();
                    control := \mathbf{newM} \ DetectorControl(output, 4); \ DetectorControlInit! \ control \longrightarrow \mathbf{skip}
                    \operatorname{var} h_1 : InputFrameHandler \bullet h_1 := \operatorname{newHandler} InputFrameHandler(self, reduce);
                    \operatorname{var} h_2 : ReducerHandler \bullet h_2 := \operatorname{newHandler}(reduce) ReducerHandler(\operatorname{self}, detect, control);
                    \operatorname{var} h_3 : DetectorHandler \bullet h_3 := \operatorname{newHandler}(detect) DetectorHandler(\operatorname{self}, control, 1));
                    \operatorname{var} h_4 : DetectorHandler \bullet h_4 := \operatorname{newHandler}(detect) DetectorHandler(\operatorname{self}, control, 2));
                    \operatorname{var} h_5 : DetectorHandler \bullet h_5 := \operatorname{newHandler}(detect) DetectorHandler(\operatorname{self}, control, 3));
                    \operatorname{var} h_6 : DetectorHandler \bullet h_6 := \operatorname{newHandler}(detect) DetectorHandler(\operatorname{self}, control, 4));
                    \mathbf{var} h_7 : Output Collisions Handler \bullet h_7 := \mathbf{newHandler}(output) Output Collisions Handler (\mathbf{self});
                    register h_1; register h_2; register h_3; register h_4; register h_5; register h_6; register h_7
cleanup \hat{=} skip
MArea =
                            \mathbf{var}\ currentFrame: \mathbf{ref}\ RawFrame ullet
                            \mathbf{var} \ state : \mathbf{ref} \ StateTable \bullet
                            \mathbf{var}\ work: \mathbf{ref}\ Partition ullet
                            \mathbf{var}\ collisions: int
                                                        (setFrame ? value \longrightarrow currentFrame := value) \square
                                                          (getFrame ! currentFrame \longrightarrow \mathbf{skip}) \square
                                                        (\textit{setState} : \textit{current} rame \longrightarrow \mathbf{skip}) \ \Box \\ (\textit{setState} : \textit{value} \longrightarrow \textit{state} := \textit{value}) \ \Box \\ (\textit{getState} ! \textit{state} \longrightarrow \mathbf{skip}) \ \Box \\ (\textit{setWork} : \textit{value} \longrightarrow \textit{work} := \textit{value}) \ \Box \\ (\textit{getWork} ! \textit{work} \longrightarrow \mathbf{skip}) \ \Box \\ (\textit{initCollsC} \longrightarrow \textit{collisions} := 0 ; \textit{initCollsR} \longrightarrow \mathbf{skip}) \ \Box \\ (\textit{recCollsC} : x \longrightarrow \begin{pmatrix} \mathbf{wait} \ 0 ... \ RC_{TB} ; \textit{collisions} := \textit{collisions} + x); \\ (\textit{retColls} | \textit{collisions} \longrightarrow \mathbf{skip}) \\ (\textit{cotColls} \longrightarrow
end
```

Due to limitations of the tools for *Circus* it is at present not possible to parse the **initialize** action, indicated

by the highlight above. The model corresponds directly to the SCJ code of this class recaptured below.

```
public class CDxMission extends Mission {
  /* Shared objects in mission memory. */
  public RawFrame currentFrame;
  public StateTable state;
  public Partition work;
  public int collisions;
  public DetectorControl control;
  /* Constructor of the class. */
  public CDxMission() {
    currentFrame = new RawFrame();
   state = new StateTable();
   work = new Partition(4);
    collisions = 0;
  /* Initialisation method call by the SCJ infrastructure. */
  public void initialize() {
    AperiodicEvent reduce = new AperiodicEvent();
    AperiodicEvent detect = new AperiodicEvent();
    AperiodicEvent output = new AperiodicEvent();
    control = new DetectorControl(output, 4);
    InputFrameHandler h1 = new InputFrameHandler(this, reduce);
   ReducerHandler h2 = new ReducerHandler(this, detect, control, reduce);
   DetectorHandler h3 = new DetectorHandler(this, control, 1, detect);
   DetectorHandler h4 = new DetectorHandler(this, control, 2, detect);
   DetectorHandler h5 = new DetectorHandler(this, control, 3, detect);
   DetectorHandler h6 = new DetectorHandler(this, control, 4, detect);
   OutputCollisionsHandler h7 = new OutputCollisionsHandler(this, output);
   h1.register()
   h2.register();
   h3.register();
   h4.register();
   h5.register();
   h6.register();
   h7.register();
  /* Clean-up method call by the SCJ infrastructure. */
  public void cleanup() { }
  /* Specifies the memory requirements of the mission (not modelled). */
  public long missionMemorySize() {
   return Constants.MISSION_MEMORY_SIZE;
```

```
/* Methods to access shared data, modelled by the MArea action. */
public RawFrame getFrame() {
  return currentFrame;
public void setFrame(RawFrame frame) {
  currentFrame = frame;
public StateTable getState() {
  return state;
}
public void setState(StateTable state) {
  this.state = state;
public Partition getWork() {
  return work;
public void setWork(Partition work) {
 this.work = work;
public synchronized void initColls() {
  collisions = 0;
public synchronized void recColls(int n) {
  collisions += n;
public synchronized int getColls() {
  return collisions;
}
```

The only deviation is the additional method missionMemorySize() which we do not model as we are not concerned with resource issues. The synchronized identifiers are implicit in the specification of *MArea*. We note that the above code is from the 'clean' version of the program which, unlike the runnable version, exclude any simulation code and is compliant with Version 0.78 of the SCJ Technology Specification.

# 6.4 $CD_x$ Handlers

The SCJ program of the parallel  $\mathsf{CD}_x$  consists of seven handlers.

- 1 x InputFrameHandler (periodic, running at maximal priority)
- 1 x ReducerHandler (aperiodic, running at normal priority)
- 4 x DetectorHandler (aperiodic, running at normal priority)
- 1 x OutputCollisionsHandler (aperiodic, running at maximal priority)

In terms of control, InputFrameHandler is released periodically by a timer and releases ReducerHandler by virtue of a software event. ReducerHandler releases all DetectorHandler instances, and the last active DetectorHandler releases OutputCollisionsHandler indirectly by calling notify(int) when finishing its work. We now discuss the SCJ Circus models for the four types of handlers in more detail.

## 6.4.1 InputFrameHandler

This is the only periodic handler of the application. It reads the next frame and deposits it in the global variable *currentFrame*. Before doing so it copies the content of the current frame into the *state* data structure. In fundamental terms *InputFrameHandler* is similar to *Handler1* of the serial line example in [5].

```
\mathbf{periodic}(\mathit{FRAME\_PERIOD}) handler \mathit{InputFrameHandler} \ \widehat{=}\ \mathbf{begin}
```

```
 \begin{array}{l} \textbf{state } \textit{InputFrameHandlerState} \\ \textit{mission} : \textit{MissionId} \\ \textit{reduce} : \textit{AperiodicEvent} \\ \\ \textbf{initial } \textit{InputFrameHandlerInit}(m: \textit{MissionId}, \textit{evt} : \textit{AperiodicEvent}) \triangleq \\ \textit{mission} := m \ ; \quad \textit{reduce} := \textit{evt} \\ \textbf{handleAsyncEvent} \triangleq \\ & \left( (\textit{next\_frame} ? \textit{frame} \longrightarrow (\textbf{wait} \ 0 \ldots \textit{ST}_{TB} \ ; \quad \textit{StoreFrame}(\textit{frame})))) \blacktriangleleft \textit{INP\_DL}; \right) \\ \textbf{StoreFrame}(\textit{frame} : \textit{RawFrame}) \triangleq \\ & \left( \begin{array}{c} \textbf{var } \textit{currentFrame} : \textbf{ref } \textit{RawFrame} \bullet \\ \textit{getFrame} ? \textit{f} \longrightarrow \textit{currentFrame} := \textit{f}; \\ \textbf{var } \textit{state} : \textbf{ref } \textit{StateTable} \bullet \\ \textit{getState} ? \textit{s} \longrightarrow \textit{state} := \textit{s}; \\ \dots \\ & \{ * \ \textbf{Update } \textit{currentFrame} \ \text{and } \textit{state} \ \text{according to } \textit{frame}. \ * \} \right) \\ \\ \textbf{dispatch handleAsyncEvent} \\ \textbf{end} \\ \end{array}
```

Since the handler is release periodically by a timer, the dispatch actions takes a simple form of just calling handleAsyncEvent. The behaviour of the handler action is to wait for communication on next\_frame and then invoke StoreFrame while passing the frame object read from the hardware. The communication must occur within INP\_DL time units from the start of each cycle. The fire construct is an extension of SCJCircus used to fire a software event. It corresponds to a respective call to the fire() method of AperiodicEvent. The computation carried out by StoreFrame is mostly omitted; it emerges during the AR phase.

### 6.4.2 ReducerHandler

We now sketch the S model for ReducerHandler.

# aperiodic handler $ReducerHandler \stackrel{\frown}{=} \mathbf{begin}$

```
{f state}\ Reducer Handler State \ \_
    mission: Mission Id \\
    detect: Aperiodic Event
    control: \mathbf{ref}\ Detector Control
initial ReducerHandlerInit(m:MisisonId, evt:AperiodicEvent, c:\mathbf{ref}\ DetectorControl) \cong
    mission := m; detect := evt; control := c
handleAsyncEvent =
       \{\mathbf{var}\ currentFrame : \mathbf{ref}\ RawFrame \bullet getFrame? f \longrightarrow currentFrame := f; \}
       \mathbf{var} \ state : \mathbf{ref} \ State \ Table \bullet \ get \ State \ ? \ s \longrightarrow state := s;
       \mathbf{var}\ work : \mathbf{ref}\ Partition \bullet getWork ? w \longrightarrow work := w;
       \mathbf{var}\ voxel\_map: HashMap[Vector2d, List[Motion]] \bullet
       wait 0 \dots RPW_{TB};
       voxel\_map := \mathbf{newP} \; HashMap();
       {* Execute algorithm for voxel hashing and populate voxel_map. *}
       voxel\_map.put(...);
       work . clear();
       for i = 0 to voxel\_map.values().size() - 1 \bullet
       initCollsC \longrightarrow \mathbf{skip}; initCollsR \longrightarrow \mathbf{skip};
       startC \longrightarrow \mathbf{skip}; startR \longrightarrow \mathbf{skip};
      fire detect
```

 $\mathbf{dispatch}\ \mathit{release\_handler}\ .\ \mathit{ReducerHandlerId} \longrightarrow \mathbf{handleAsyncEvent}$   $\mathbf{end}$ 

The difference to, for instance, Handler2 of the serial line example in [6] is that this handler is released by a software event rather than an external event. The synchronisation on  $release\_handler$ . ReducerHandlerId highlights this. In the P model of software events, the  $release\_handler$  channel is used to cause the periodic release of a handler. The channel is parametrised by the id of the handler to be released. Details of the voxel hashing algorithm are again omitted; they are a concern for AR.

### 6.4.3 DetectorHandler

The aperiodic detection handler is specified below. Since we have four instances of this handler in the program, the process is parametrised by an identifier of type *int*.

aperiodic handler  $DetectorHandler \stackrel{\frown}{=} hdl : HandlerId \bullet begin$ 

```
{f state}\ Detector Handler State .
    mission: Mission Id
     control: \mathbf{ref}\ Detector Control
    id:int
initial DetectorHandlerInit(m:MissionId, c: \mathbf{ref}\ DetectorControl, n: int) \ \widehat{=} \ 
     mission := m \; ; \; control := c \; ; \; id := n
CalcPartCollisions \stackrel{\frown}{=} \mathbf{res} \ pcolls : int \bullet
        pcolls := 0;
        \mathbf{var}\ work : Partition \bullet getWork ? w \longrightarrow work := w;
        for i = 0 to work \cdot getDetectorWork(id) \cdot size() - 1 \bullet
               determineCollisions \cong val\ motions : List[Motion]; \ res\ ret : int \bullet
     {* Algorithm for counting collisions. *}
     \mathbf{ret} := \dots
handleAsyncEvent =
              colls: int •
\begin{pmatrix} \mathbf{wait} \ 0 \dots CPC_{TB} \ ; \ CalcPartCollisions(colls); \\ recCollsC ! colls \longrightarrow recCollsR \longrightarrow \mathbf{skip}; \\ notifyC ! id \longrightarrow notifyR \longrightarrow \mathbf{skip} \end{pmatrix}
\mathbf{dispatch}\ release\ .\ Detector Handler Id \longrightarrow \mathbf{handle AsyncEvent}()
```

Details have been omitted concerning the algorithm that counts collisions in a voxel motion list (this is done inside the method *determineCollisions*). We have four instances of this process in the S anchor:

 $DetectorHandler(1) \parallel DetectorHandler(2) \parallel DetectorHandler(3) \parallel DetectorHandler(4)$ 

## 6.4.4 OutputCollisionsHandler

end

end

This is a simple aperiodic handler that outputs the collisions.

```
aperiodic handler OutputCollisionsHandler \stackrel{\frown}{=} \mathbf{begin}
```

```
\begin{array}{c} \mathbf{state} \ Output Collisions Handler State \\ \hline mission: Mission Id \\ \\ \mathbf{initial} \ Output Collisions Handler Init (m: Mission Id) \ \widehat{=} \ mission := m \\ \mathbf{handle A sync E vent} \ \widehat{=} \ \mathbf{var} \ colls: int \ \bullet \\ \left( \begin{matrix} get Colls? \ c \longrightarrow colls := c; \\ (output\_collisions! \ colls \longrightarrow \mathbf{skip} \ \blacktriangleleft \ OUT\_DL) \end{matrix} \right) \\ \mathbf{dispatch} \ release\_handler. \ Output Collisions Handler Id \longrightarrow \mathbf{handle A sync E vent} \\ \end{array}
```

The handler method first obtains the detected collisions using the getColls method provided by the mission class to access the shared collisions variable. It then outputs the collisions on the output\_collisions channel, imposing a deadline on the communication to ensure that the hardware accepts the output within the required time interval.

# 6.4.5 Active Objects

We note that the Oh Circus class model of Detector Control does not contain a fire statement. This, however, is needed to give a faithful model of this class. Below we capture the active behaviour of the Detector Control instance used by the program by way of a an SCJ Circus paragraph active. It is also part of the S anchor.

```
\begin{array}{l} \textbf{state } \textit{DetectorControl} \ \cong \textbf{begin} \\ & \textbf{state } \textit{DetectorHandlerState} \\ & \textit{control} : \textbf{ref } \textit{DetectorControl} \\ \\ \textit{Init} \ \cong \ \textit{DetectorControlInit?} \ c \longrightarrow \textit{control} := c \\ & \textit{MArea} \ \cong \\ & \left( \begin{array}{c} (\textit{startC} \longrightarrow \textit{control} . \textit{startR} \longrightarrow \textbf{skip}) \\ & \\ (\textit{notifyC?} \ i \longrightarrow \\ & \\ (\textit{control} . notify(i); \\ & \\ (\textit{control} . done() \otimes \textbf{fire } \textit{control} . \textit{event} \longrightarrow \textbf{skip}) \\ & \\ & \\ (\neg \ \textit{control} . done() \otimes \textbf{skip}) \\ & \\ & \textit{notifyR} \longrightarrow \textbf{skip} \\ \end{array} \right); \ X \\ \\ \textbf{end} \end{array}
```

The state includes a reference to the detector control class object whose active behaviour is wrapped by the process. The *Init* action connects the process to this object via an input prefix on the *DetectorControlInit* channel. The communication is raised inside the *CDxMission* paragraph when the *control* object is created.

# A Class Definitions

In this section we present the specification of Oh Circus classes of the program relevant to the models.

# A.1 RawFrame class

```
class RawFrame \stackrel{\frown}{=} begin
   {\bf statics} \ Raw Frame Statics
   private MAX\_PLANES : int;
   private MAX\_SIGNS : int
   sinit RawFrameSInit
   RawFrameStatics'
   MAX\_PLANES' = 1000
   MAX\_SIGNS' = 10 * MAX\_PLANES'
   {f state}\ RawFrameState
   public lengths: intArray
   public\ call signs: byteArray
   public positions : floatArray
   public planeCnt: int
   lengths \neq \mathbf{null} \land callsigns \neq \mathbf{null} \land positions \neq \mathbf{null}
   lengths . length() = MAX\_PLANES
   call signs. length() = MAX\_SIGNS
   positions.length() = 3 * MAX\_PLANES
   0 \le planeCnt \le MAX\_PLANES
   {\bf initial} \ Raw Frame Init
   RawFrameState'
   lengths' = \mathbf{newM} \ intArray(MAX\_PLANES)
   call signs' = \mathbf{newM} \ byteArray(MAX\_SIGNS)
   positions' = \mathbf{newM} \ floatArray(3 * MAX\_PLANES)
   planeCnt' = 0
   logical\ function\ get Call Sign Offset
   \Xi RawFrameState
   plane?:int
   result!:int
   0 \le plane? < planeCnt
   result! = \Sigma \{i : 0 ... plane? -1 \bullet i \mapsto lengths. getA(i)\}
   logical function getCallSign
   \Xi RawFrameState
   plane?:int
   result! : seq byte
   0 \le plane? < planeCnt
   \# result! = lengths . getA(plane?)
   \forall i: 1... lengths.getA(plane?) \bullet
       result!(i) = callsigns.getA(\mathbf{self}.getCallSignOffset(plane?) + i - 1)
```

end

This class has only one non-logical method which is used to initialise the instance variables from a given set of arrays. It also introduces two static variables which, however, are merely used as constants. The remainder of the SCJ program accesses the fields of the class directly to obtain the position data of the aircrafts (all instance variables of the class are public).

# A.2 State Table class

#### Abstract version

The abstract State Table class is not concerned with memory allocation issues.

The posnMap member variable of type HashMap is used to store Vector3d objects under keys being CallSign objects. The model for the HashMap class is included in Appendix ??.

## Concrete version

end

The concrete  $\mathit{StateTable}$  class in comparison considers memory areas.

```
class StateTableC \cong \mathbf{begin}
   statics State Table Statics
   MAX\_AIRPLANES: int
   \mathbf{sinit}\ StateTableSInit
   StateTableStatics'
   MAX\_AIRPLANES' = 10000
   State Table State
   private posnMap : HashMap[CallSign, Vector3d]
   private allocated Vectors: Vector3dArray
   private used Vectors: int
   private r: StateTable\_R
   posnMap \neq \mathbf{null} \land allocatedVectors \neq \mathbf{null}
   0 \leq used Vectors \leq allocated Vectors . length()
initial Init =
      r := \mathbf{newM} \, StateTable\_R(\mathbf{self});
      allocated Vectors := \mathbf{newM} \ Vector 3 dArray(MAX\_AIRPLANES);
        f for index = 0 to allocated Vectors . length() - 1 ullet
           allocated Vectors . setA(index, \mathbf{newM} \ Vector3d()));
```

```
\begin{aligned} \mathbf{public} \ put(callsign: CallSign, x: float, y: float, z: float) \ \widehat{=} \\ \left(\begin{matrix} r \ . \ callsign: = callsign; \\ r \ . \ x: = \ x; \\ r \ . \ y: = \ y; \\ r \ . \ z: = \ z; \\ MemoryArea \ . \ getMemoryArea(\mathbf{self}) \ . \ executeInArea(r) \end{matrix}\right) \\ \mathbf{public} \ get(callsign: CallSign) \ \widehat{=} \ \mathbf{ret} := posnMap \ . \ get(callsign) \\ \mathbf{end} \end{aligned}
```

This class introduces mechanisms to solve memory allocation issues ensued by the dynamic allocation of data in mission memory. This is, in particular, the allocation of Vector3d objects. It utilises an inner class  $StateTable\_R$  to execute code in mission memory that updates the HashMap.

### Inner class R of StateTable

The inner class below is used to ensure that put operations carried out on the HashMap are executed in mission memory. This should, in principle, not be necessary, however, I suspect that adding elements to the HashMap causes dynamic allocation of data, too, in the original program of the  $\mathsf{CD}_x$ . This is fundamentally an issue with the memory behaviour of libraries and subject to future research.

```
class StateTable\_R \cong \mathbf{begin}

StateTable\_RState

\mathbf{private}\ outer: StateTable}

\mathbf{public}\ callsign: CallSign}

\mathbf{public}\ x, y, z: float

outer \neq \mathbf{null}

initial Init(o: StateTable) \cong outer:= o

\mathbf{public}\ run \cong

\begin{pmatrix} \mathbf{var}\ v: Vector3d \bullet v:= outer.posnMap.get(callsign); \\ \mathbf{if}\ v = \mathbf{null} \longrightarrow \\ & v:= outer.allocatedVectors.get(usedVectors); \\ & usedVectors:= usedVectors+1; \\ & outer.posnMap.put(callsign,v); \\ & \| \neg\ v = \mathbf{null} \longrightarrow \mathbf{skip} \\ \mathbf{fi}; \\ & v.\ x:= x; \\ & v.\ y:= y; \\ & v.\ z:= z \end{pmatrix}

end
```

Other than the potential problem of HashMap internally allocating data, I do not see why the content of the run() method cannot be executed in per-release memory. Are there downward references? Another issue is how we make explicit in SCJCircus that a piece of code should run in a particular memory area. We might not want to do this via a class and data object as above. These are still open issues for the language.

# A.3 CallSign class

The class CallSign is used to represent call sign objects in the program.

```
class CallSign \cong \mathbf{begin}

state CallSignState = \mathbf{private} \ val : byteArray

val \neq \mathbf{null}

initial Init \cong \mathbf{val} \ v : byteArray \bullet val := v

public hashCode \cong \mathbf{ret} : boolean \bullet

\left( \begin{array}{c} \mathbf{var} \ h \bullet h := 0; \\ (\mathbf{for} \ i = 0 \ \mathbf{to} \ val . \, length() - 1 \bullet h := h + val . \, getA(i)); \\ \mathbf{ret} := h \end{array} \right)

public equals \cong \mathbf{val} \ obj : Object; \ \mathbf{res} \ \mathbf{ret} : boolean \bullet

\mathbf{ret} := \mathbf{if} \ (\mathbf{self} = obj) \ \mathbf{then} \ jtrue \ \mathbf{else} \ jfalse

public compareTo \cong \mathbf{val} \ obj : Object \bullet \dots

end
```

The definition of the compareTo(obj : Object) method has been omitted; it is not central to the models presented in the report. Objects of CallSign are used as map keys in StateTable. This class is immutable.

## A.4 Vector2d class

The class Vector2d is used to index the map that results from voxel hashing.

```
class Vector2d \ \widehat{=} \ \mathbf{begin}
\begin{array}{c} \mathbf{state} \ Vector2dState \\ x: \mathbb{R}; \\ y: \mathbb{R} \end{array}
\mathbf{initial} \ Init \ \widehat{=} \ \mathbf{val} \ v_{-}x: \mathbb{R}; \ \mathbf{val} \ v_{-}y: \mathbb{R} \bullet x:= v_{-}x\ ; \ y:= v_{-}y \\ \mathbf{end} \end{array}
```

The class Vector2d is immutable too.

#### **A.5** Partition class

```
class Partition \stackrel{\frown}{=} begin
   {f state}\ PartitionState
    private parts : ListArray[List[Motion]];
    private counter: int
    parts \neq \mathbf{null} \land 0 \leq counter < parts.length()
initial Init = val n : int \bullet
        parts := \mathbf{newM} \ ListArray(n);
        \begin{pmatrix} \mathbf{for} \ index = 0 \ \mathbf{to} \ parts \ . \ length() - 1 \bullet \\ parts \ . \ setA(index, \mathbf{newM} \ LinkedList()) \end{pmatrix}; \\ counter := 0 
public sync clear \stackrel{\frown}{=}
      (for index = 0 to parts . length() - 1   parts . clear()); 
      \setminus counter := 0
public sync recordVoxelMotions(motions : List[Motions]) \cong
       'parts.getA(counter).add(motions);
      counter := (counter + 1) \mod parts \cdot length()
public sync getDetectorWork \cong valid : int;
     \mathbf{res}\ \mathbf{ret}: List[List[Motion]] \bullet \mathbf{ret} := parts \,.\, getA(id-1)
\mathbf{end}
```

#### **A.6** DetectorControl class

```
class DetectorControl \cong \mathbf{begin}
    {f state}\ Detector Control State
     private idle: booleanArray
     idle \neq null
initial DetectorControlInit \cong val n : int \bullet idle := newM booleanArray(n)
public sync start \stackrel{\frown}{=}
     for index = 0 to idle. length() - 1 \bullet idle. setA(index, jfalse)
public sync notify \triangleq \mathbf{val}\ id : int \bullet idle . setA(id - 1, jtrue);
function sync done \stackrel{\frown}{=}
        'ret := jtrue;
        for index = 0 to idle \cdot length() - 1 \bullet

if idle \cdot getA(index) = jfalse \longrightarrow \mathbf{ret} := jfalse

[ \neg idle \cdot getA(index) = jfalse \longrightarrow \mathbf{skip} ]
end
```

We note that the specification of the done() method is not complete, only capturing changes made to data. As explained in [6], we require a process / action model to give a full account of the active behaviour.

# B Refinement Laws

This appendix summarises all significant refinement laws that are used throughout the refinement strategy.

#### B.1 Circus Laws

Circus Law 1 (distr-prefix-seq)

$$c \longrightarrow (A_1; A_2) \equiv (c \longrightarrow A_1); A_2$$

Circus Law 2 (seq-to-par-1)

$$A_1 \; ; \; A_2 \equiv ((A_1 \; ; \; c \longrightarrow \mathbf{skip}) \, \llbracket \; wrtV(A_1) \mid \{ \! \mid c \! \mid \} \mid wrtV(A_2) \, \rrbracket \, (c \longrightarrow A_2)) \setminus \{ \! \mid c \! \mid \}$$
 provided  $wrtV(A_1) \cap wrtV(A_2) = \varnothing \text{ and } wrtV(A_1) \cap usedV(A_2) = \varnothing \text{ and } c \notin usedC(A_1) \cup usedC(A_2)$ 

Circus Law 3 (seq-to-par-2)

Circus Law 4 (conj-to-par)

$$Op_1 \wedge Op_2 \equiv Op_1 \parallel wrtV(Op_1) \mid \varnothing \mid wrtV(Op_2) \parallel Op_2 \text{ provided } wrtV(Op_1) \cap wrtV(Op_2) = \varnothing$$

Circus Law 5 (distr-var-hide)

$$\mathbf{var} x : T \bullet (A \setminus cs) \equiv (\mathbf{var} x : T \bullet A) \setminus cs$$

Circus Law 6 (distr-var-par)

$$\operatorname{var} x : T \bullet (A_1 \llbracket \dots \rrbracket A_2) \equiv (\operatorname{var} x : T \bullet A_1) \llbracket \dots \rrbracket (\operatorname{var} x : T \bullet A_2)$$

Circus Law 7 (remove-var)

$$\operatorname{var} x : T \bullet A \equiv A \operatorname{provided} x \notin FV(A)$$

Circus Law 8 (compact-write-sets-par)

$$A_1 \llbracket ns_1 \mid cs \mid ns_2 \rrbracket A_2 \equiv A_1 \llbracket ns_1' \mid cs \mid ns_2' \rrbracket A_2$$
  
**provided**  $(ns_1 \setminus ns_1') \cap wrtV(A_1) = \emptyset$  **and**  $(ns_2 \setminus ns_2') \cap wrtV(A_2) = \emptyset$ 

Circus Law 9 (distr-prefix-par-1)

$$c ? x \longrightarrow (A_1 \llbracket ns_1 \mid cs \mid ns_2 \rrbracket A_2) \equiv (c ? x \longrightarrow A_1 \llbracket ns_1 \mid cs \cup \{c\} \mid ns_2 \rrbracket c ? x \longrightarrow A_2)$$
  
**provided**  $c \notin usedC(A_1)$  and  $c \notin usedC(A_2)$ 

Circus Law 10 (distr-prefix-par-2)

$$(A_1 \ \llbracket \ ns_1 \mid cs \mid ns_2 \ \rrbracket \ A_2) \ ; \ c! x \longrightarrow \mathbf{skip} \equiv (A_1 \ ; \ c? y \longrightarrow \mathbf{skip}) \ \llbracket \ ns_1 \mid cs \cup \{ \ c \ \} \mid ns_2 \ \rrbracket \ (A_2 \ ; \ c! x \longrightarrow \mathbf{skip})$$
**provided**  $c \not\in usedC(A_1)$  and  $c \not\in usedC(A_2)$  and  $x \not\in ns_1$ 

#### Circus Law 11 (lockstep-intro)

$$(\mu X \bullet (A_1 \llbracket ns_1 \mid cs \mid ns_2 \rrbracket A_2); X) \equiv \begin{pmatrix} (\mu X \bullet A_1; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs \cup \{ Sync \} \mid ns_2 \end{bmatrix} \setminus \{ sync \} \\ (\mu X \bullet A_2; sync \longrightarrow X) \end{pmatrix} \setminus \{ sync \}$$

**provided**  $sync \notin usedC(A_1) \cup usedC(A_2)$  and  $wrtV(A_1) \cap usedV(A_2) = \emptyset$  and  $wrtV(A_2) \cap usedV(A_1) = \emptyset$ 

#### Circus Law 12 (replace-sync-chan-seq)

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c \, ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs \mid ns_2 \rrbracket \\ (\mu X \bullet c \, ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \end{pmatrix} \setminus \{ \mid c \mid \}$$

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c_1 ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs \setminus \{ \mid c \mid \} \mid ns_2 \rrbracket \\ (\mu X \bullet c_2 ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \end{pmatrix} \setminus \{ \mid c_1, c_2 \mid \} \\ \llbracket ns_1 \cup ns_2 \mid \{ \mid c_1, c_2, sync \mid \} \mid \varnothing \rrbracket \\ (\mu X \bullet c_1 ? \, x \longrightarrow c_2 ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix}$$

**provided**  $\{c, sync\}\subseteq cs \land c \notin usedC(A_1) \cup usedC(A_2) \text{ and } c_1 \text{ and } c_2 \text{ are fresh channels}$ 

## Circus Law 13 (var-intro)

$$A(x) \equiv \mathbf{var} \ v : T \bullet v := x \ ; \ A(v)$$
  
**provided**  $v$  is not free in  $A$ 

#### Circus Law 14 (extract-var-prefix)

$$c?x \longrightarrow (\mathbf{var}\ v: T \bullet A) \equiv \mathbf{var}\ v: T \bullet c?x \longrightarrow A$$
  
**provided**  $x$  and  $v$  are distinct variables

#### Circus Law 15 (extract-var-seq)

(var 
$$v: T \bullet A_1$$
);  $A_2 \equiv (\text{var } v: T \bullet A_1; A_2)$   
provided  $v$  is not free in  $A_2$ 

#### Circus Law 16 (extract-var-rec)

$$\mu X \bullet (\mathbf{var} \ v : T \bullet A) \equiv \mathbf{var} \ v : T \bullet (\mu X \bullet A)$$
  
**provided**  $v$  is initialised before use in  $A$ 

#### Circus Law 17 (distr-prefix-par-3)

$$c \longrightarrow \mathbf{skip}$$
;  $(\mathbf{skip} \llbracket ns_1 \mid cs \mid ns_2 \rrbracket A) \equiv \mathbf{skip} \llbracket ns_1 \mid cs \mid ns_2 \rrbracket (c \longrightarrow A)$  **provided**  $c \notin cs$ 

```
Circus Law 18 (distr-prefix-par-4)
```

$$c! x \longrightarrow \mathbf{skip}$$
;  $(A_1 \llbracket ns_1 \mid cs \mid ns_2 \rrbracket A_2) \equiv (c! x \longrightarrow \mathbf{skip}; A_1) \llbracket ns_1 \mid cs \mid ns_2 \rrbracket (c? y \longrightarrow A_2)$   
**provided**  $c \in cs$  and  $y$  is not free in  $A_2$ 

#### Circus Law 19 (distr-prefix-par-5)

$$c ? x \longrightarrow v := x ; (A_1 \llbracket ns_1 \mid cs \mid ns_2 \rrbracket A_2) \equiv$$

$$(c ? x \longrightarrow v := x ; A_1) \llbracket ns_1 \cup \{v\} \mid cs \mid ns_2 \rrbracket (c ? y \longrightarrow A_2)$$
**provided**  $c \in cs$  **and**  $v$  and  $y$  are not free in  $A_2$ 

#### Circus Law 20 (extchoice-par-intro)

$$((c \longrightarrow A_1); A_2) \llbracket ns_1 \mid cs \mid ns_2 \rrbracket (c \longrightarrow A_3) \equiv$$
  
 $(((c \longrightarrow A_1) \square (c_1 \longrightarrow B_1) \square ... \square (c_n \longrightarrow B_n)); A_2) \llbracket ns_1 \mid cs \mid ns_2 \rrbracket (c \longrightarrow A_3)$   
**provided**  $c \in cs$  **and**  $c$  is distinct from all  $c_i$  (the  $B_i$  can be chosen arbitrarily)

#### Circus Law 21 (seq-skip-left-intro)

$$A \equiv \mathbf{skip}; A$$

#### Circus Law 22 (par-skip-intro)

$$\mathbf{skip} \equiv (\mathbf{skip} \, \llbracket \, \varnothing \, | \, \varnothing \, | \, \varnothing \, \rrbracket \, \mathbf{skip})$$

### Circus Law 23 (extend-sync-par)

$$A_1 \llbracket ns_1 \mid cs \mid ns_2 \rrbracket A_2 \equiv A_1 \llbracket ns_1 \mid cs \cup cs' \mid ns_2 \rrbracket A_2$$
  
**provided**  $cs' \cap (usedC(A_1) \cup usedC(A_2)) = \emptyset$ 

### Circus Law 24 (extchoice-comm)

$$A_1 \square A_2 \equiv A_2 \square A_1$$

#### Circus Law 25 (distr-rec-par-1)

$$\mu X \bullet (A_1 \llbracket ns_1 \mid cs \mid ns_2 \rrbracket A_2) \; ; \; c \longrightarrow X \equiv$$

$$(\mu X \bullet A_1 \; ; \; c \longrightarrow X) \llbracket ns_1 \mid cs \mid ns_2 \rrbracket (\mu X \bullet A_2 \; ; \; c \longrightarrow X)$$
**provided**  $c \in cs$  and  $c \notin usedC(A_1) \cup usedC(A_2)$  and  $wrtV(A_1) \cap usedV(A_2) = \varnothing$  and  $wrtV(A_2) \cap usedV(A_1)$ 

### Circus Law 26 (distr-rec-par-2)

$$\mu X \bullet (((c_1 \longrightarrow A_1 \square c_2 \longrightarrow A_2) ; A_3) \llbracket ns_1 \mid cs \mid ns_2 \rrbracket (c_1 \longrightarrow A_4)) ; X \equiv (\mu X \bullet (c_1 \longrightarrow A_1 \square c_2 \longrightarrow A_2) ; A_3 ; X) \llbracket ns_1 \mid cs \mid ns_2 \rrbracket (\mu X \bullet c_1 \longrightarrow A_4 \longrightarrow X)$$

$$\mathbf{provided} \ \{c_1, c_2\} \subseteq cs \ \mathbf{and} \ c_1 \not\in usedC(A_i) \ \text{for all} \ i \in \{1, 2, 3, 4\} \ \mathbf{and}$$

$$(wrtV(A_1) \cup wrtV(A_2) \cup wrtV(A_3)) \cap usedV(A_4) = \varnothing \ \mathbf{and}$$

$$wrtV(A_4) \cap (usedV(A_1) \cup usedV(A_2) \cup usedV(A_3)) = \varnothing$$

#### Circus Law 27 (elim-repeated-seq-rec)

$$\mu X \bullet A$$
;  $A$ ; ...;  $A$ ;  $X \equiv \mu X \bullet A$ ;  $X$ 

Circus Law 28 (var-elim)

$$(\mathbf{var} \ v : T \bullet A) \equiv A$$
  
**provided**  $v$  is not free in A

Circus Law 29 (hidden-sync-intro)

$$A \equiv (c \longrightarrow A) \setminus \{ c \} \text{ provided } c \notin usedC(A)$$

Circus Law 30 (extract-hide-prefix)

$$c \longrightarrow (A \setminus cs) \equiv (c \longrightarrow A) \setminus cs$$
  
provided  $c \notin cs$ 

Circus Law 31 (extract-hide-rec)

$$\mu X \bullet (A \setminus cs) \equiv (\mu X \bullet A) \setminus cs$$

Circus Law 32 (extract-hide-par-left)

$$(A_1 \setminus cs) \llbracket ns_1 \mid cs' \mid ns_2 \rrbracket A_2 \equiv (A_1 \llbracket ns_1 \mid cs' \mid ns_2 \rrbracket A_2) \setminus cs$$
  
**provided**  $cs \cap cs' = \emptyset$  and  $cs \cap usedC(A_2) = \emptyset$ 

Circus Law 33 (extract-hide-par-right)

$$A_1 \llbracket ns_1 \mid cs' \mid ns_2 \rrbracket (A_2 \setminus cs) \equiv (A_1 \llbracket ns_1 \mid cs' \mid ns_2 \rrbracket A_2) \setminus cs$$
  
**provided**  $cs \cap cs' = \emptyset$  and  $cs \cap usedC(A_1) = \emptyset$ 

Circus Law 34 (idem-par)

$$A \equiv (A \llbracket \varnothing \mid usedC(A) \mid \varnothing \rrbracket A)$$
 provided  $wrtV(A) = \varnothing$  and A is deterministic

Circus Law 35 (seq-op-comm)

$$A \; ; \; \mathit{Op} \equiv \mathit{Op} \; ; \; A$$
 provided  $\mathit{usedV}(\mathit{Op}) \cup \mathit{wrtV}(A) = \varnothing \; \text{and} \; \mathit{usedV}(A) \cup \mathit{wrtV}(\mathit{Op}) = \varnothing$ 

#### B.2 Circus Time Laws

Circus Time Law 1 (narrow-time-budget-1)

wait 
$$t_1 \dots t_2 \sqsubseteq$$
 wait  $t_1' \dots t_2'$  provided  $t_1 \le t_1'$  and  $t_2' \le t_2$ 

Circus Time Law 2 (narrow-time-budget-2)

wait 
$$w: t_1 \dots t_2 \bullet A \sqsubseteq \text{wait } w: t_1' \dots t_2' \bullet A \text{ provided } t_1 \leq t_1' \text{ and } t_2' \leq t_2$$

Circus Time Law 3 (time-prefix-elim)

$$(c \circ t \longrightarrow \mathbf{wait} \ t_1 - t) \blacktriangleleft d \equiv ((c \longrightarrow \mathbf{skip}) \blacktriangleleft d) \parallel \mathbf{wait} \ t_1 \ \mathbf{provided} \ d \leq t_1$$

Circus Time Law 4 (extract-inter-wait-seq)

$$Op$$
;  $(A \parallel \mathbf{wait}\ t) \equiv (Op;\ A) \parallel \mathbf{wait}\ t$   
**provided**  $Op$  is a data operation and  $wrtV(Op) \cap FV(t) = \emptyset$ 

Circus Time Law 5 (extract-inter-wait-var)

$$\operatorname{var} x : T \bullet (A \parallel \operatorname{wait} t) \equiv (\operatorname{var} x : T \bullet A) \parallel \operatorname{wait} t$$
  
provided  $x \notin FV(t)$ 

Circus Time Law 6 (extract-inter-wait-waitblock)

wait 
$$w: t_1 ... t_2 \bullet (A(w) \parallel \mathbf{wait} \ t - w) \equiv (\mathbf{wait} \ w: t_1 ... t_2 \bullet A(w)) \parallel \mathbf{wait}$$
 provided  $t_2 \leq t$ 

Circus Time Law 7 (extract-inter-wait-prefix)

$$(c \circ t \longrightarrow (A(t) \parallel \mathbf{wait} (t_1 - t))) \blacktriangleleft d \equiv ((c \circ t \longrightarrow A(t)) \blacktriangleleft d) \parallel \mathbf{wait} t_1$$
 provided  $d \leq t_1$ 

Circus Time Law 8 (remove-unused-time-prefix)

$$c \circ t \longrightarrow A \equiv c \longrightarrow A$$
 provided  $t \notin FV(A)$ 

Circus Time Law 9 (remove-unused-wait-block)

wait 
$$w: T \bullet A \equiv \text{wait } T$$
; A provided  $w \notin FV(A)$ 

Circus Time Law 10 (distr-sync-deadline-seq)

$$(c \longrightarrow (A_1; A_2)) \triangleleft d \equiv ((c \longrightarrow A_1) \triangleleft d); A_2$$

Circus Time Law 11 (split-time-budget-1)

wait 
$$0 ... t \equiv$$
wait  $0 ... t_1$ ; wait  $0 ... t_2$  provided  $t = t_1 + t_2$ 

Circus Time Law 12 (split-time-budget-2)

wait 
$$0 ... t \subseteq$$
wait  $0 ... t_1$ ; wait  $0 ... t_2$  provided  $t_1 + t_2 \le t$ 

#### Circus Time Law 13 (time-budget-op-comm)

```
P(Op; \mathbf{wait} t_1 \dots t_2) \equiv P(\mathbf{wait} t_1 \dots t_2; Op) provided Op is a data operation
```

This law is in fact non-compositional: it is a law about processes rather than actions. Hence, it only holds if the underlying action Op; wait  $t_1 cdots t_2$  is embedded in a process P. The justification for the law comes from the structure and semantics of processes that prevents one from observing the precise time at which an (internal) state change takes place. It is proved by induction over the structure of processes.

# Circus Time Law 14 (distr-wait-seq-var)

```
wait t_1 ... t_2; var x : T \bullet A \equiv \text{var } x : T \bullet (\text{wait } t_1 ... t_2 ; A)
provided x \notin FV(t_1) and x \notin FV(t_2)
```

# Circus Time Law 15 (distr-wait-seq-par)

```
wait t_1 
ldots t_2; (Op_1 \llbracket \dots \rrbracket Op_2) \equiv (\text{wait } t_1 
ldots t_2 ; Op_1) \llbracket \dots \rrbracket (\text{wait } t_1 
ldots t_2 ; Op_2)
provided Op_1 and Op_2 are data operations
```

#### Circus Time Law 16 (zero-wait-intro)

```
A \equiv \mathbf{wait} 0; A
```

### B.3 High-level Patterns

High-level Law 1 (seq-share-1)

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c \! ! \; x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs \mid ns_2 \rrbracket \\ (\mu X \bullet c \; ? \; x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \end{pmatrix} \setminus \{ \mid c \mid \}$$

$$\equiv$$

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c_1 \! ! \; x \longrightarrow \mathbf{skip} \; ; \; c_3 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid (cs \setminus \{ \mid c \mid \}) \cup \{ \mid c_3 \mid \} \mid ns_2 \rrbracket \\ (\mu X \bullet c_3 \longrightarrow \mathbf{skip} \; ; \; c_2 \; ? \; x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \cup ns_2 \mid \{ \mid c_1, c_2 \mid \} \mid \varnothing \rrbracket \\ \begin{pmatrix} \mathbf{var} \; v : \; T \bullet \\ \mu X \bullet \begin{pmatrix} (c_1 \; ? \; x \longrightarrow v := x) \; \Box \\ (c_2 \! ! \; v \longrightarrow \mathbf{skip}) \end{pmatrix} \; ; \; X \end{pmatrix}$$

**provided**  $\{c, sync\} \subseteq cs \land c \notin usedC(A_1) \cup usedC(A_2)$  and  $c_1, c_2$  and  $c_3$  are fresh channels

#### High-level Law 2 (seq-share-2)

$$\begin{pmatrix} (\mu X \bullet A_1 \; ; \; c \! ! \, x \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs_1 \mid ns_2 \cup ns_3 \cup \ldots \cup ns_n \rrbracket \\ (\mu X \bullet c \; ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \\ \llbracket ns_2 \mid cs_2 \mid ns_3 \cup ns_4 \cup \ldots \cup ns_n \rrbracket \\ (\mu X \bullet c \; ? \, x \longrightarrow A_3 \; ; \; sync \longrightarrow X) \\ \ldots \\ \llbracket ns_{n-1} \mid cs_{n-1} \mid ns_n \rrbracket \\ (\mu X \bullet c \; ? \, x \longrightarrow A_n \; ; \; sync \longrightarrow X) \end{pmatrix} \\ \equiv \\ \begin{pmatrix} (\mu X \bullet A_1 \; ; \; c_1 \! ! \, x \longrightarrow \mathbf{skip} \; ; \; c_3 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X \\ \llbracket ns_1 \mid (cs_1 \setminus \{\!\! \mid c \,\!\! \}\!\!) \cup \{\!\! \mid c_3 \,\!\! \} \mid ns_2 \cup ns_3 \cup \ldots \cup ns_n \rfloor \\ (\mu X \bullet c_3 \longrightarrow \mathbf{skip} \; ; \; c_2 \; ? \, x \longrightarrow A_2 \; ; \; sync \longrightarrow X) \end{pmatrix}$$

$$\begin{bmatrix}
\left((\mu X \bullet A_{1}; c_{1}! x \longrightarrow \mathbf{skip}; c_{3} \longrightarrow \mathbf{skip}; sync \longrightarrow X)\right) \\
\left[[ns_{1} \mid (cs_{1} \setminus \{ \{ c \} \}) \cup \{ \{ c_{3} \} \mid ns_{2} \cup ns_{3} \cup \ldots \cup ns_{n} ]\right]\right] \\
(\mu X \bullet c_{3} \longrightarrow \mathbf{skip}; c_{2}? x \longrightarrow A_{2}; sync \longrightarrow X) \\
\left[[ns_{2} \mid (cs_{2} \setminus \{ \{ c \} \}) \cup \{ \{ c_{3} \} \mid ns_{3} \cup ns_{4} \cup \ldots \cup ns_{n} ]\right]\right] \\
(\mu X \bullet c_{3} \longrightarrow \mathbf{skip}; c_{2}? x \longrightarrow A_{3}; sync \longrightarrow X) \\
\ldots \\
\left[[ns_{n-1} \mid (cs_{n-1} \setminus \{ \{ c \} \}) \cup \{ \{ c_{3} \} \mid ns_{n} ]\right] \\
(\mu X \bullet c_{3} \longrightarrow \mathbf{skip}; c_{2}? x \longrightarrow A_{n}; sync \longrightarrow X)
\right] \\
\left[[ns_{1} \cup ns_{2} \cup \ldots \cup ns_{n} \mid \{ \{ c_{1}, c_{2} \} \mid \varnothing ]\right] \\
\left(\mathbf{var} \ v : T \bullet \\
\mu X \bullet \begin{pmatrix} (c_{1}? x \longrightarrow v := x) \square \\ (c_{2}! v \longrightarrow \mathbf{skip}) \end{pmatrix}; X
\right)$$

**provided**  $\{c, sync\} \subseteq cs_i \land c \notin usedC(A_i)$  for  $i \in 1...n$  and  $c_1, c_2$  and  $c_3$  are fresh channels

#### High-level Law 3 (par-share)

```
 \begin{array}{c} \mathbf{r} \ v : T \bullet \\ \left( \begin{matrix} \mu X \bullet \ start \longrightarrow \mathbf{wait} \ 0 \ .. \ Init_{TB} \ ; \ InitOp; \\ \mathbf{var} \ x_1, x_2, \ldots, x_n : T \bullet \\ \left( \begin{matrix} (record ? \ x \longrightarrow (\mathbf{wait} \ 0 \ .. \ RC_{TB} \ ; \ x_1 := x)); \\ (record ? \ x \longrightarrow (\mathbf{wait} \ 0 \ .. \ RC_{TB} \ ; \ x_2 := x)); \\ \ldots \\ (record ? \ x \longrightarrow (\mathbf{wait} \ 0 \ .. \ RC_{TB} \ ; \ x_n := x)); \\ \mathbf{wait} \ 0 \ .. \ Merge_{TB} \ ; \ MergeOp([[x_1, x_2, \ldots, x_n]]); \\ output ! \ v \longrightarrow \mathbf{skip} \ ; \ sync \longrightarrow X \\ \end{array} \right) \right) 
         \left(\begin{array}{c} \mu X \bullet \begin{pmatrix} init \longrightarrow (\mathbf{wait} \ 0 \dots Init_{TB} \ ; \ InitOp) \ \Box \\ record \ ? \ x \longrightarrow (\mathbf{wait} \ 0 \dots RC_{TB} \ ; \ MergeOp(\llbracket x \rrbracket)) \ \Box \\ output \ ! \ v \longrightarrow \mathbf{skip} \\ \llbracket \varnothing \mid \{ \mid init, record, output \} \mid \varnothing \rrbracket \\ \end{pmatrix} \right)
\left(\mu X \bullet init \longrightarrow start \longrightarrow \begin{pmatrix} (record ? y \longrightarrow \mathbf{skip}) & ||| \\ (record ? y \longrightarrow \mathbf{skip}) & ||| \\ \vdots \\ (record ? y \longrightarrow \mathbf{skip}) & ||| \\ \vdots \\ (record ? y \longrightarrow \mathbf{skip}) \end{pmatrix}; \ output ? y \longrightarrow \mathbf{skip}; \ sync \longrightarrow X
            \{|init|\}
```

**provided** InitOp and MergeOp are data operations and  $wrtV(InitOp) = \{v\} = wrtV(MerqeOp)$  and  $MerqeOp(b_1 \uplus b_2) = MerqeOp(b_1)$ ;  $MerqeOp(b_2)$ 

#### High-level Law 4 (par-share-control)

```
 \begin{pmatrix} \left( \mu X \bullet A; \ start \longrightarrow \mathbf{skip}; \ sync \longrightarrow X \right) \\ \left[ ns \mid \left\{ \ start, sync \right\} \mid \varnothing \right] \\ \left( \mu X \bullet start \longrightarrow \mathbf{var} \ v : \ T \bullet A_1; \ record ! \ v \longrightarrow \mathbf{skip}; \ output ? \ y \longrightarrow \mathbf{skip}; \ sync \longrightarrow X \right) \\ \left[ \varnothing \mid \left\{ \ start, output, sync \right\} \mid \varnothing \right] \\ \left( \mu X \bullet start \longrightarrow \mathbf{var} \ v : \ T \bullet A_2; \ record ! \ v \longrightarrow \mathbf{skip}; \ output ? \ y \longrightarrow \mathbf{skip}; \ sync \longrightarrow X \right) \\ \ldots \\ \left[ \varnothing \mid \left\{ \ start, output, sync \right\} \mid \varnothing \right] \\ \left( \mu X \bullet start \longrightarrow \mathbf{var} \ v : \ T \bullet A_n; \ record ! \ v \longrightarrow \mathbf{skip}; \ output ? \ y \longrightarrow \mathbf{skip}; \ sync \longrightarrow X \right) \\ \left[ ns \mid \left\{ \ start, record, output, sync \right\} \mid \varnothing \right] \\ \left( \mu X \bullet init \longrightarrow start \longrightarrow \left( \begin{matrix} (record ? \ y \longrightarrow \mathbf{skip}) \ | \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ? \ y \longrightarrow \mathbf{skip}) \ | \ (record ?
```

#### High-level Law 5 (sync-barrier-elim)

$$\begin{pmatrix} (\mu X \bullet start \longrightarrow A_1 \; ; \; done \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs_1 \mid ns_2 \cup \ldots \cup ns_n \rrbracket \\ (\mu X \bullet start \longrightarrow A_2 \; ; \; done \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_2 \mid cs_2 \mid ns_3 \cup \ldots \cup ns_n \rrbracket \\ \ldots \\ \llbracket ns_{n-1} \mid cs_{n-1} \mid ns_n \rrbracket \\ (\mu X \bullet start \longrightarrow A_n \; ; \; done \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix}$$

$$\equiv$$

$$\begin{pmatrix} (\mu X \bullet start \longrightarrow A_1 \; ; \; notify ! \; 1 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs_1 \setminus \{ \mid done \; \} \mid ns_2 \cup \ldots \cup ns_n \rrbracket \\ (\mu X \bullet start \longrightarrow A_2 \; ; \; notify ! \; 2 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_2 \mid cs_2 \setminus \{ \mid done \; \} \mid ns_3 \cup \ldots \cup ns_n \rrbracket \\ \ldots \\ \llbracket ns_{n-1} \mid cs_{n-1} \setminus \{ \mid done \; \} \mid ns_n \rrbracket \\ (\mu X \bullet start \longrightarrow A_n \; ; \; notify ! \; n \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix}$$

$$\llbracket ns_1 \cup \ldots \cup ns_n \mid \{ \mid start, notify, sync \; \} \mid \varnothing \rrbracket$$

$$\begin{pmatrix} (notify \; ! \; 1 \longrightarrow \mathbf{skip}) \parallel \\ (notify \; ! \; 2 \longrightarrow \mathbf{skip}) \parallel \\ \ldots \\ (notify \; ! \; n \longrightarrow \mathbf{skip}) \end{pmatrix} \; ; \; done \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X \end{pmatrix}$$

**provided**  $\{ start, done, sync \} \subseteq cs_i \land \{ start, done, sync \} \cap usedC(A_i) = \emptyset \text{ for all } i:1..n$  and notify is a fresh channel of type  $\mathbb{N}$ 

# High-level Law 6 (sync-barrier-control)

$$\begin{pmatrix} \mu X \bullet start \longrightarrow \begin{pmatrix} (notify\,!\,1 \longrightarrow \mathbf{skip}) \, ||| \\ (notify\,!\,2 \longrightarrow \mathbf{skip}) \, ||| \\ \dots \\ (notify\,!\,n \longrightarrow \mathbf{skip}) \end{pmatrix}; \ done \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X \end{pmatrix}$$

$$\equiv \begin{pmatrix} (\mu X \bullet reset \longrightarrow start \longrightarrow X\,; \ sync \longrightarrow X) \\ \|\varnothing\,|\, \{\{start, sync\,\}\,|\,\varnothing\| \\ (\mu X \bullet start \longrightarrow notify\,!\,1 \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X) \\ \|\varnothing\,|\, \{\{start, sync\,\}\,|\,\varnothing\| \\ (\mu X \bullet start \longrightarrow notify\,!\,2 \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X) \\ \|\varnothing\,|\, \{\{start, sync\,\}\,|\,\varnothing\| \\ \dots \\ \|(\mu X \bullet start \longrightarrow notify\,!\,n \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X)) \end{pmatrix} \begin{pmatrix} \{\{reset\,\}, sync\,\}\,|\,\varnothing\| \\ (\mu X \bullet start \longrightarrow notify\,!\,n \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X) \end{pmatrix} \begin{pmatrix} \{\{start, sync\,\}\,|\,\varnothing\| \\ (\mu X \bullet start \longrightarrow notify\,!\,n \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X) \end{pmatrix} \begin{pmatrix} \{\{start, sync\,\}\}\,|\,\varnothing\| \\ (\mu X \bullet start \longrightarrow notify\,!\,n \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X) \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{\{start, sync\,\}\,|\,\varnothing\| \\ (\mu X \bullet start \longrightarrow notify\,!\,n \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X) \end{pmatrix} \begin{pmatrix} \{\{start, sync\,\}\}\,|\,\varnothing\| \\ (\mu X \bullet start \longrightarrow notify\,!\,n \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X) \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{\{start, sync\,\}\}\,|\,\varnothing\| \\ (\mu X \bullet start \longrightarrow notify\,!\,n \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X) \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{\{start, sync\,\}\}\,|\,\varnothing\| \\ (\mu X \bullet start \longrightarrow notify\,!\,n \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X) \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{\{start, sync\,\}\}\,|\,\varnothing\| \\ (\mu X \bullet start \longrightarrow notify\,!\,n \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X) \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{\{start, sync\,\}\}\,|\,\varnothing\| \\ (\mu X \bullet start \longrightarrow notify\,!\,n \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X) \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{\{start, sync\,\}\}\,|\,\varnothing\| \\ (\mu X \bullet start \longrightarrow notify\,!\,n \longrightarrow \mathbf{skip}\,; \ sync \longrightarrow X) \end{pmatrix} \end{pmatrix} \begin{pmatrix} \{\{start, sync\,\}\}\,|\,\varnothing\| \\ \{\{start, sync\,\}\}$$

#### High-level Law 7 (sync-barrier-design)

$$\begin{pmatrix} (\mu X \bullet start \longrightarrow A_1 \; ; \; done \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs_1 \mid ns_2 \cup \ldots \cup ns_n \rrbracket \\ (\mu X \bullet start \longrightarrow A_2 \; ; \; done \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_2 \mid cs_2 \mid ns_3 \cup \ldots \cup ns_n \rrbracket \\ \ldots \\ \llbracket ns_{n-1} \mid cs_{n-1} \mid ns_n \rrbracket \\ (\mu X \bullet start \longrightarrow A_n \; ; \; done \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix}$$

$$\Box$$

$$\begin{pmatrix} (\mu X \bullet reset \longrightarrow start \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket \varnothing \mid \{ reset, start, sync \} \mid \varnothing \rrbracket \\ (\mu X \bullet start \longrightarrow A_1 \; ; \; notify ! \, 1 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_1 \mid cs_1 \setminus \{ \{ done \} \mid ns_2 \cup \ldots \cup ns_n \rrbracket \\ (\mu X \bullet start \longrightarrow A_2 \; ; \; notify ! \, 2 \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \\ \llbracket ns_2 \mid cs_2 \setminus \{ \{ done \} \mid ns_3 \cup \ldots \cup ns_n \rrbracket \\ \ldots \\ \llbracket ns_{n-1} \mid cs_{n-1} \setminus \{ \{ done \} \mid ns_n \rrbracket \\ (\mu X \bullet start \longrightarrow A_n \; ; \; notify ! \; n \longrightarrow \mathbf{skip} \; ; \; sync \longrightarrow X) \end{pmatrix}$$

$$\llbracket ns_1 \cup \ldots \cup ns_n \mid \{ \text{start}, \text{notify}, \text{sync} \} \mid \varnothing \rrbracket$$

$$\langle \text{var active} : \mathbb{P} \{1 \ldots n \} \bullet$$

$$\langle \text{(reset} \longrightarrow \text{active} := 1 \ldots n \rangle$$

$$\Box$$

$$\langle \text{(notify} ? x \longrightarrow \begin{pmatrix} \text{active} := \text{active} \setminus \{x\}; \\ \text{if active} = \varnothing \longrightarrow \text{skip} \end{pmatrix} )$$

$$\rbrace ; X$$

**provided** { reset, start, done, sync }  $\subseteq$  cs<sub>i</sub>  $\land$  { reset, start, done, sync }  $\cap$  used  $C(A_i) = \emptyset$  for all i:1..n and notify is a fresh channel of type  $\mathbb N$ 

# C Mock Objects

Dummy definitions that enable the parsing and type-checking of the models.

# C.1 Unit Type

```
[unit]
```

# **Empty Tuple**

```
%%Zword \emptytuple emptytuple
```

```
emptytuple:unit
```

# C.2 Array Types

```
Array[X]
getA: int \to X
setA: int \times X \to X
length: unit \to int

intArray == Array[int]

byteArray == Array[byte]

floatArray == Array[float]

booleanArray == Array[boolean]
```

# C.3 Classes Types

```
[Object] \\ List[X] \\ = lems: unit \rightarrow \mathbb{P} X \\ \\ -HashMap[X, Y] \\ = get: X \rightarrow Y \\ values: unit \rightarrow List[Y] \\ \\ -CallSign \\ \\ -Vector2d \\ = x: \mathbb{R} \\ y: \mathbb{R} \\ \\ \end{bmatrix}
```

	$\_Vector 3d$
	$x:\mathbb{R}$
	$y:\mathbb{R}$
	$z:\mathbb{R}$
	RawFrame
	planeCnt:int
	$positions: Array[\mathbb{R}]$
	getCallSign: int  ightarrow Aircraft
	$getCallSignOffset: int \rightarrow int$
	$find: Aircraft \rightarrow int$
	FrameBuffer
	State Table
	$position\_map: HashMap[CallSign, Vector 3d]$
	_ Motion
	_ Partition
	$getDetectorWork: int \rightarrow List[List[Motion]]$
	$\_Detector Control\_\_\_$
<b>C.4</b>	To foother true Classes
C.4	Infrastructure Classes
	Aperiodic Event
	AperioaicEvent
	_ InputHandler
	_ OutputHandler
	ReducerHandler
	_ DetectorHandler
	Detectoriumune
	Detected II and II and America
C.5	Auxiliary Functions
	$MkCallSign: Aircraft \rightarrow CallSign$
ı	$M(M, t) = (A^*, b) \cdot M(t)$
1	$MkMotion: (Aircraft \times Vector \times Vector) \rightarrow Motion$

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