

Safety-Critical Java in *Circus*

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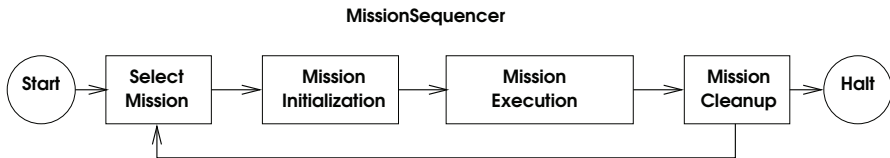
Safety-Critical Java

- Nothing about design techniques
- Level 1

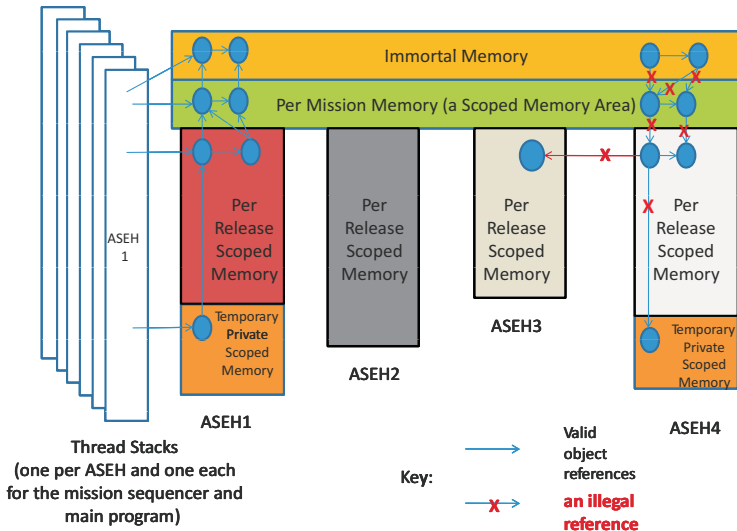
Refinement technique for SCJ

- Timing requirements and their decomposition
- Value-based specification and class-based designs
- SCJ memory model

Application structure



Scoped memory area

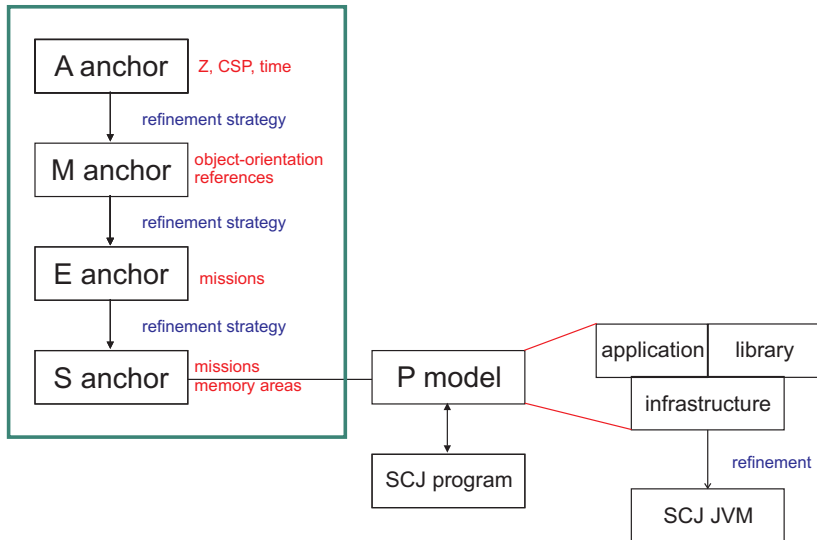


- *Circus*: $Z + \text{CSP} + \text{ZRC}$
- Language for **refinement**
- Target programming languages: occam, Handel-C, SPARK Ada
- Processes: encapsulate state + behaviour
 - State: Z
 - Actions: $\text{CSP} + Z + \text{guarded command language}$
 - Communication: through channels
- Semantic model: Unifying Theories of Programming

Circus variants

- *Circus Time*
- *OhCircus*
- ...

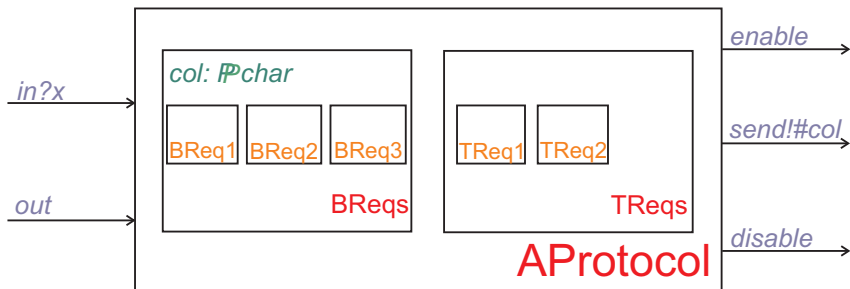
Development of SCJ programs



Example: simple protocol



Example: A Anchor



Example: A Anchor

process *BReqs* $\hat{=}$ **begin**

state *APState* $==$ $[col : \mathbb{P} \text{ char}]$

Init $==$ $[APState' \mid col' = \emptyset]$

Insert $==$ $[\Delta APState; x? : \text{char} \mid col' = col \cup \{x?\}]$

InsS(*w*) $\hat{=}$ (**wait** 0..*w* ; *Insert*) $\square$ (*send*!($\# col$)@*t* $\longrightarrow$ *InsS*(*w* - *t*))

BReq1 $\hat{=}$ (*in*?*x*@*t* $\longrightarrow$ *InsS*(100 - *t*) $\square$ *send*!($\# col$) $\longrightarrow$ **Skip**) ; *BReq1*

BReq2 $\hat{=}$ *out* $\longrightarrow$ *enable* $\longrightarrow$ *send*?*x* $\longrightarrow$ *BReq2*

BReq3 $\hat{=}$ *send*?*x* $\longrightarrow$ *disable* $\longrightarrow$ *BReq3*

- **wait** 0 .. 3 ; *Init*;
 $(BReq1 \parallel \{col\} \mid \{ send \} \mid \{\} \parallel (BReq2 \parallel \{ send \} \parallel BReq3))$

end

Example: A Anchor

process $TReqs \triangleq$ **begin**

$TReq1 \triangleq ((in?x \longrightarrow \mathbf{Skip}) \blacktriangleright 5 \parallel \mathbf{wait} 100) ; TReq1$

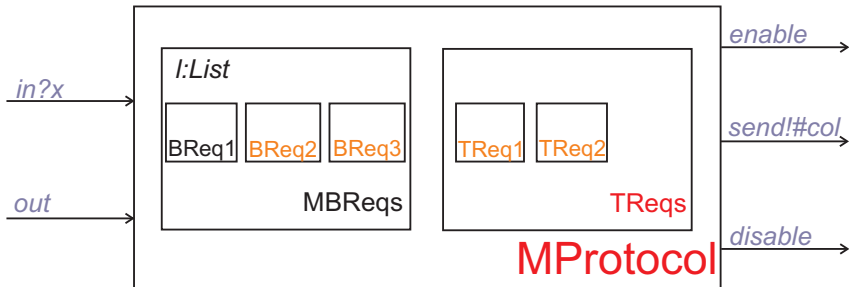
$TReq2 \triangleq out \longrightarrow \mathbf{wait} 0..7 ; enable \longrightarrow (disable \longrightarrow \mathbf{Skip}) \blacktriangleright 15 ;$
 $TReq2$

• $TReq1 \parallel TReq2$

end

system $AProtocol \triangleq BReqs \llbracket \{ in, out, enable, disable \} \rrbracket TReqs$

Example: M Anchor



Memory allocation

- Java memory model
- Language: *OhCircus* with references
- Data refinement
- Automation: not possible in general

Example: M Anchor

class *List* $\hat{=}$

state *LState* $==$ [*val* : char; *next* : *List*; *empty* : *Bool* | ...]

initial *Init* $==$ [*LState'* | *empty'* = *true*]

synchronized public *insert* _____
 $\Delta LState$; *x?* : char

let *col* $==$ **self**.*elems*(); *col'* $==$ **self'**.*elems*() • *col'* = *col* $\cup$ {*x?*}

logical *elems* $\hat{=}$ **res** *col* : $\mathbb{P}$ char •

if *empty* = *true* $\longrightarrow$ *col* := $\emptyset$

$\square$ *empty* = *false* $\longrightarrow$ *col* := *next*.*elems*() $\cup$ {*val*}

fi

synchronized public *size* $==$ [$\exists LState$; *s!* : $\mathbb{Z}$ | ...]

end

Example: M Anchor



process *MBReqs* $\hat{=}$ **begin**

state *MPState* $==$ [*I* : *List*]

Init $\hat{=}$ (*I* := **new** *List*)

InsS(*w*) $\hat{=}$ (**wait** 0..*w* ; *I.insert*(*x*)) $\square$ (*send!*(*I.size*())@*t* $\longrightarrow$ *InsS*(*w* - *t*))

BReq1 $\hat{=}$ (*in?**x*@*t* $\longrightarrow$ *InsS*(100 - *t*) $\square$ *send!*(*I.size*()) $\longrightarrow$ **Skip**) ; *BReq1*

...

Design of missions and handlers

- Language: no change
- Four phases of refinement
 - CP: collapse parallelism
 - SH: sharing
 - MH: missions and handlers
 - AR: algorithmic refinement
- Automation
 - Interface of the handlers?
 - Sharing among handlers?

Example: CP phase



Example: CP phase

system *EProtocol* $\hat{=}$ **begin**

state *MPState* $==$ [*I* : *List*]

Init $\hat{=}$ (*I* := **new** *List*)

InPending(*t*, *d*) $\hat{=}$ (*in?**x*@*u* $\longrightarrow$ *AfterInPinsert*(*t* + *u*, 100 − (*t* + *u*), *x*)) $\blacktriangleleft$ *d*
 $\square$
out@*u* $\longrightarrow$ *InAfterOut*(*t* + *u*, *d* − *u*, 7)

AfterInPinsert(*t*, *wins*, *x*) $\hat{=}$
 $\sqcap d : 0 \dots wins \bullet (out@u \longrightarrow \dots$

...

• **wait** 0 .. 3 ; *Init* ; *InPending*(0, 5)

end

Example: CP phase

system *EProtocol* $\hat{=}$ **begin**

state *MPState* $==$ [*I* : *List*]

Init $\hat{=}$ (*I* := **new** *List*)

InPending(*t*, *d*) $\hat{=}$ (*in*?*x*@*u* $\longrightarrow$ *AfterInPinsert*(*t* + *u*, 100 − (*t* + *u*), *x*)) $\blacktriangleleft$ *d*
 $\square$
out@*u* $\longrightarrow$ *InAfterOut*(*t* + *u*, *d* − *u*, 7)

AfterInPinsert(*t*, *wins*, *x*) $\hat{=}$
 $\square$ *d* : 0 .. *wins* • (*out*@*u* $\longrightarrow$...

...

• **wait** 0 .. 3 ; *Init* ; *InPending*(0, 5)

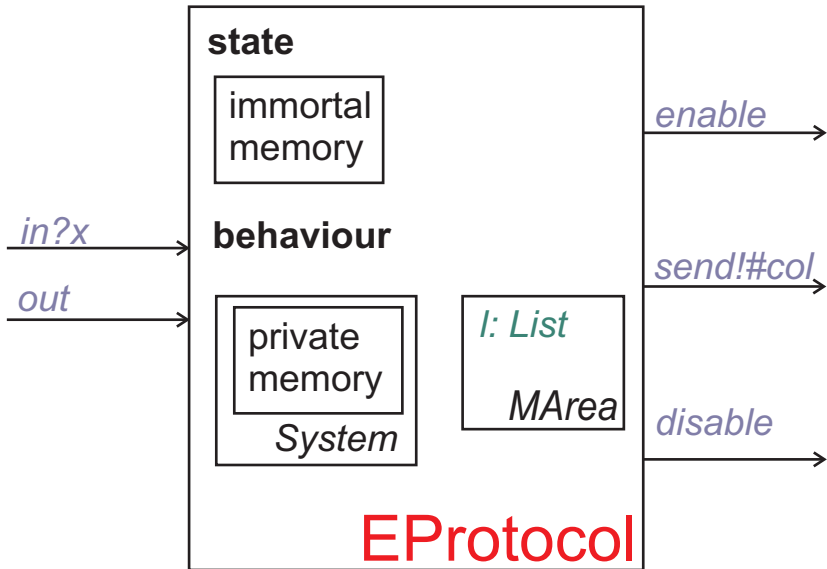
end

Splitting the state

Components in

- Immortal memory: stay where they are
- Per-release and temporary areas: become local to the main action
- Mission memory: become local to a new separate parallel action

Example: SH phase



Example: SH phase

system *EProtocol* $\hat{=}$ **begin**

...
InPending(*t*, *d*) $\hat{=}$...

...
System $\hat{=}$ *InPending*(0, 5)

MArea $\hat{=}$

$$\left(\text{var } l : \text{List} \bullet \text{Init}; \right. \\ \left. \left(\mu X \bullet \left(\begin{array}{l} \text{insertLC?}x \longrightarrow l.\text{insert}(x); \text{insertLR} \longrightarrow X \\ \square \\ \text{sizeLC} \longrightarrow \text{sizeLR}!(l.\text{size}(x)) \longrightarrow X \end{array} \right) \right) \right)$$

• **wait** 0 .. 3;
 (*System* [{ *insertLC*, *insertLR*, ... }] *MArea*) \ { *insertLC*, *insertLR*, ... }

end

Example: SH phase

system *EProtocol* $\hat{=}$ **begin**

...

InPending(*t*, *d*) $\hat{=}$...

...

System $\hat{=}$ *InPending*(0, 5)

MArea $\hat{=}$

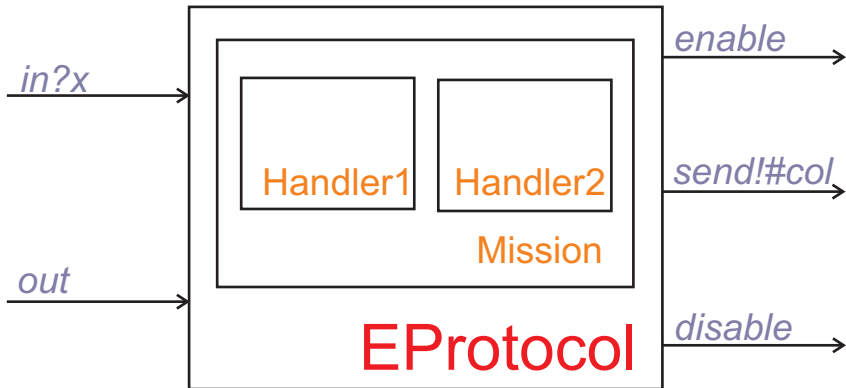
$$\left(\begin{array}{l} \text{var } l : \text{List} \bullet \text{Init}; \\ \left(\mu X \bullet \left(\begin{array}{l} \text{insertLC?}x \longrightarrow l.\text{insert}(x) ; \text{insertLR} \longrightarrow X \\ \square \\ \text{sizeLC} \longrightarrow \text{sizeLR}!(l.\text{size}(x)) \longrightarrow X \end{array} \right) \right) \end{array} \right)$$

• **wait** 0 .. 3;

(*System* [{ *insertLC*, *insertLR*, ... }] *MArea*) \ { *insertLC*, *insertLR*, ... }

end

Example: MH phase



Example: MH phase

system *EProtocol* $\hat{=}$ **begin**

...

Handler1 $\hat{=}$

$((in?x@t \longrightarrow \mathbf{wait} \ 0..(100 - t) ; \ \mathit{insertLC}!x \dots$

Handler2 $\hat{=}$

$out \longrightarrow \mathit{sizeLC} \longrightarrow \mathit{sizeLR}?x \longrightarrow \mathbf{wait} \ 0..7;$

$enable \longrightarrow (\mathit{send}!x \longrightarrow \mathit{disable} \longrightarrow \mathbf{Skip}) \blacktriangleright 15 ; \ \mathit{Handler2}$

Mission $\hat{=}$ (*Handler1* ||| *Handler2*)

System $\hat{=}$ *Mission*

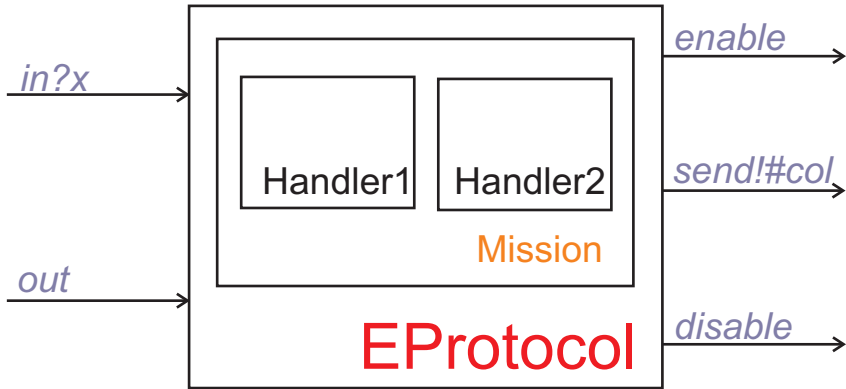
MArea $\hat{=}$...

• **wait** 0 .. 3;

$(\mathit{System} \llbracket \{ \mathit{insertLC}, \mathit{insertLR}, \dots \} \rrbracket \mathit{MArea}) \backslash \{ \mathit{insertLC}, \mathit{insertLR}, \dots \}$

end

Example: AR phase



SCJ framework

- Language: *SCJ-Circus*
- Abbreviations
- Underlying: same language + SCJ memory model
- Refinement laws for new constructs

Example: S Anchor

```

sequencer MainMissionSequencer  $\hat{=}$  begin
  state MainMissionSequencerState  $==$  [mission_done : Bool]
  initial  $\hat{=}$  mission_done := false
  getNextMission  $\hat{=}$ 
    if mission_done = false  $\longrightarrow$ 
      mission_done := true; ret := ProtocolMission
    [] mission_done = true  $\longrightarrow$  ret := null
    fi
  end
mission ProtocolMission  $\hat{=}$  begin
  state MState  $==$  [I : List]
  initialize  $\hat{=}$ 
    I := newList ; (newHandlerHandler1(I)) ; (newHandlerHandler2(I))
  cleanup  $\hat{=}$  Skip
end

```

Example: S Anchor

```

periodic(100) handler Handler1  $\hat{=}$  begin
state Handler1_State  $==$  [l : List]
initial Handler1_Init  $\hat{=}$  val list? : List • l := list?
handleAsyncEvent(x, w)  $\hat{=}$  wait 0..w ; l.insert(x)
dispatch  $\hat{=}$  (in? x @ t  $\longrightarrow$  handleAsyncEvent(x, 100 - t))  $\blacktriangleleft$  5
end

```

```

aperiodic handler Handler2  $\hat{=}$  begin
state Handler2_State  $==$  [l : List]
initial Handler2_Init  $\hat{=}$  val list? : List • l := list?
handleAsyncEvent  $\hat{=}$ 
    var size :  $\mathbb{N}$  • size := l.size() ; wait 0 .. 7;
    enable  $\longrightarrow$  (send ! size  $\longrightarrow$  disable  $\longrightarrow$  Skip)  $\blacktriangleright$  15
dispatch  $\hat{=}$  (out  $\longrightarrow$  handleAsyncEvent())
end

```

S Anchor: applications



- Use *Circus* and the UTP for reasoning
- Automatic generation of SCJ programs
- Conversely: automatic generation of S models
 - Programming patterns
 - Refactoring
 - Examples?
- Identification of good programming practices?
- Basis to verify an SCJ implementation

Challenges ahead

Theory

- Integration of languages and theories
- Mechanisation
- Refinement laws and detailed strategies
- Modular reasoning about libraries

Practice

- Case studies
- Design patterns
- Generation of abstract models
- Automation

And beyond

- Certification, Resources, ...