Marie R. Pistilli Women in EDA Achievement Award

Mary Jane Irwin - A. Robert Noll Chair of Engineering, Penn State Univ., University Park, PA
For her significant contributions in helping women advance in the field of EDA technology.

The P. O. Pistilli Undergraduate Scholarships for Advancement in Computer Science and Electrical Engineering

The objective of the P. O. Pistilli Scholarship program is to increase the pool of professionals in Electrical Engineering, Computer Engineering and Computer Science from under-represented groups (women, African American, Hispanic, Native American, and physically challenged). In 1989, the ACM Special Interest Group on Design Automation (SIGDA) established the program. Beginning in 1993, the Design Automation Conference provided the funds for the scholarship, and SIGDA continues to administer the program for DAC. DAC normally funds two or more $4000 scholarships, renewable up to 5 years, to graduating high school seniors. In 1999 the IEEE Circuits and Systems Society also began to sponsor these scholarships.

The 2003 winners were:

Julie Louise Sandberg - attending Univ. of Wyoming for computer engineering.
Krystal Vernce Watson - attending Cornell Univ. for computer engineering.

For more information about the P. O. Pistilli scholarship, please contact Dr. Cherrice Traver, ECE Dept., Union College, Schenectady, NY 12308. email: traverc@union.edu

2004 Design Automation Conference Graduate Scholarships

Each year the Design Automation Conference sponsors several $24,000 scholarships to support graduate research and study in Design Automation (DA), with emphasis in "design and test automation of electronic and computer systems". Each scholarship is awarded directly to a university for the Faculty Investigator to expend in direct support of one or more DA graduate students.

The criteria for granting such a scholarship expanded in 1996 to include financial need. The criteria are: the academic credentials of the student(s); the quality and applicability of the proposed research; the impact of the award on the DA program at the institution; and financial need. Preference is given to institutions that are trying to establish new DA research programs.

Profs. Jiang Hu, Texas A&M Univ., College Station, TX
Student: Chin-Ngai Sze
Integrated Placement and Skew Optimization for Variation Tolerant and Low Power Clock Network

Prof. Adrian Nunez-Aldana, Syracuse Univ., Syracuse, NY
Students: Amit Varde, Chandrasekar Rajagopal
Synthesis of High-Performance Digital Circuits

Information on next year's DAC scholarship award program will be available on the DAC web page: http://www.dac.com.