# Realizable Parasitic Reduction Using Generalized $Y$ - $\Delta$ Transformation 

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#### Abstract

We propose a realizable RCLK-in-RCLK-out parasitic reduction technique. The method employs generalized $Y$ - $\Delta$ transformation. In our method, admittances are kept in their original rational forms of $s$, and their orders are reduced by truncating high-order terms. Therefore reduced admittances match the low-order terms in exact admittances. First-order realization of admittances is guaranteed, and higher-order realization is achieved by template optimization using Geometric Programming. The algorithm uniquely uses common-factor identification and cancelation operations to make $Y$ - $\Delta$ transformation numerically stable. The experiment shows that our method can achieve higher reduction ratio than TICER and comparable simulation results with PRIMA.


Categories \& Subject Descriptors: B.7.2 Simulation, B.8.2 Performance Analysis \& Design Aids, G.2.2 Graph Theory.
General Terms: Algorithms, Performance, Design.
Keywords: Y- $\Delta$ Transformation, model order reduction, parasitic reduction.

## 1. Introduction

With ever increasing design complexity huge amount of extracted interconnect data size has pushed the capacity of existing timing/noise analysis and transistor level simulation tools to the limits. Recent work in the model order reduction has been focused on generating stable and passive macromodels [1]-[6]. However, these models cannot directly be fed into a general simulator. Integrating realizable reduction techniques into design flow of real applications shows more advantanges. For example, RC-in-RC-out like reduction technique has been used widely in the extraction and transistor level simulation stage.

Liao [7] proposes a method to realize reduced RC macromodels from progressively merged sub-circuits. Sheehan [8] presents an RC-in-RC-out reduction scheme named TICER, in which internal nodes with extremely large and small time constants are eliminated using Gauss eliminations. A nice property of the methods is that reduced RC circuits can be plugged back into the system and simulated using any general simulator. At the same time they preserve the zero and first order of moments. But these methods only work for RC networks.
In another aspect, topological analysis [9] is an approach to calculating driving-point admittances using Cramer's rule in $s$-domain. The determinant of an admittance matrix of a passive network without mutual inductances is equal to the sum of all the tree admittance products of the network. Hence the method avoids cancelations in

[^0]the expansion of determinants. However, enumerating all the trees in a large network is very difficult.
Recently Ismail proposes DTT to approximate transfer functions in tree-structured RLC networks by direct transfer-function truncations [10]. The transfer functions are kept in the rational form so that low-order moments are matched implicitly. Since a truncated transfer function may not be a positive real function, the method is not compatible with general simulators either.
Unsatisfied with these limitations, we devise a new RCLK-in-RCLK-out reduction method based on $Y$ - $\Delta$ transformation [11] [12] for general RCLK-VJ ${ }^{1}$ linear networks. The idea is that, given such a network, we perform Y- $\Delta$ transformation on all internal nodes in an efficient order. Y- $\Delta$ admittances in each transformation are kept in the rational form, and admittances whose order is higher than a threshold $\beta$ will be truncated. Unlike topological analysis and other symbolic approaches, the new reduction method only evaluates the first $\beta$ terms in the denominator and numerator of $\Delta$ admittances. ${ }^{2}$ Discarded high-order terms, however, do not affect the preservation of the low-order terms, i.e., the low-order terms are precisely the first $\beta+1$ terms in exact admittances.

The main contribution of this paper:

1) Realizability of reduced RCLK network is achieved via Y- $\Delta$ transformation and Geometric Optimization;
2) Fidelity of low-order terms in $\Delta$ admittances are kept, and the orders of $\Delta$ admittances are reduced to no more than $\beta$;
3) First $\beta+1$ moments of exact admittances are matched implicitly by reduced $\mathrm{Y}-\Delta$ admittances, including the zero order moment for DC correctness;
4) Two kinds of common-factor effects are first discovered in $\mathrm{Y}-\Delta$ transformation. The findings lead to essential numerical improvement to $\mathrm{Y}-\Delta$ transformation.
The remaining of the paper is organized as follows. Generalized $\mathrm{Y}-\Delta$ transformation formula are given in Section 2. Common-factor effects are illustrated and solved in Section 3. In section 4, we present the RLC realization method using Geometric Programming. Section 5 reviews the proposed reduction algorithm. Section 6 shows examples and experimental results.

## 2. Generalized Y- $\Delta$ Transformation

First we clarify the assumptions and notations we will use throughout the paper. We confine our discussion to linear RCLK-J networks. We assume that all current sources are shunt to ground and all storage elements have no initial conditions. ${ }^{3}$

- $n_{k}$ denotes the $k$-th labeled node, where $k$ starts from 0 . Nodes are eliminated in the order labeled;

[^1]- when the $k$-th node is eliminated, the network will be updated accordingly. We label the network (graph) before the elimination as $G_{k}\left(V_{k}, E_{k}\right)$, and the network (graph) after as $G_{k+1}\left(V_{k+1}, E_{k+1}\right)$;
- $\left(n_{i}, n_{j}\right)$ denotes the branch between $n_{i}$ and $n_{j}, Y_{i, j}$ the admittance of $\left(n_{i}, n_{j}\right), I_{i}$ the current source impinging on $n_{i}$, and $\Gamma_{i}$ the neighbor set of $n_{i}$. A superscript ( $k$ ) on these notations stands for "in $G_{k}$ ";
- The first neighbor of $n_{i}$ is the node in $\Gamma_{i}$ with the smallest label.

Our Y- $\Delta$ transformation is generalized in the following three ways:

1) it identifies and cancels common factors present in $\Delta$ admittances to assure numerical stability;
2) it covers circuits of general topology in contrast to DTT, which is tree-based. Aside from RLC, it also covers mutual K elements and current sources, which call for more sophisticated source transformation because of common-factor effects.
3) it eliminates nodes efficiently by dynamically choosing the one with the minimum degree. In TICER, however, the degree of quick and slow nodes is not a concern in its ordering scheme.
We cover K-element transformation in the next sub-section, followed by Y- $\Delta$ transformation formula. Common-factor effects will be covered in the next section.

### 2.1. Conversion of Mutual $K$ Branch

RLC can be handled directly by Y- $\Delta$ transformation because they have well-known admittance forms in $s$-domain. Mutual inductors, on the contrary, have no simple admittance form. Because the branch voltage $v_{1}$ of a mutual inductor relys on current variations in more than branches including itself

$$
V_{1}=L_{11} I_{1} s+L_{12} I_{2} s+\cdots .
$$

Including mutual inductors is difficult, because we are not able to eliminate a node if one of its incident branch inductively couples with more than one branch.

Alternatively, with K elements, inductive coupling can be modeled in such a way that variation of a branch current replies on multiple coupling branch voltages. $I-V$ relationship of mutual K elements is given by

$$
\begin{equation*}
\frac{K_{11}}{s} V_{1}+\frac{K_{12}}{s} V_{2}+\cdots=I_{1}, \tag{1}
\end{equation*}
$$

where $V_{i}$ 's are branch voltages.
In order for us to invoke Y- $\Delta$ transformation on arbitrary nodes, we still have to change mutual-K elements to self-K elements. In


Fig. 1. Conversion on mutual K in $s$-domain: (a)given mutual K ; (b) converted self K .

Fig. 1(a), the KCL equations for the four nodes in terms of $V_{1}$ and $V_{2}$ can be written as

$$
\begin{align*}
\frac{K_{11}}{s} V_{1}+\frac{K_{12}}{s} V_{2} & =I_{1} \\
\frac{K_{12}}{s} V_{1}+\frac{K_{22}}{s} V_{2} & =I_{2} \tag{2}
\end{align*}
$$

One can check that the KCL equations for the four nodes in Fig. 1(b) are exactly the same as (2), so that (b) is equivalent to (a). However, (b) has only self-K elements. Although some values in (b) are negative, the circuit is still passive because K-based method guarantees the extracted K matrix to be positive definite.

### 2.2. Generalized Formula for $Y-\Delta$ Transformation

The generalized $\mathrm{Y}-\Delta$ transformation formula for linear RCLK-VJ networks are given below.
Theorem 1 ( $Y$ - $\Delta$ Formula): Suppose $n_{k}$ in $G_{k}$ is the node being eliminated. $\forall n_{i}, n_{j} \in n_{k}$ 's neighbors $\Gamma_{k}^{(k)}$, if branch $\left(n_{i}, n_{j}\right)$ is not in $G_{k}$, then it will be added into $G_{k+1}$ after $n_{k}$ is eliminated; otherwise a new admittance will be added on to the branch in $G_{k+1}$. Admittance $Y_{i j}^{(k+1)}$ for $\left(n_{i}, n_{j}\right)$ is calculated as

$$
\begin{equation*}
Y_{i j}^{(k+1)}(s)=Y_{i j}^{(k)}(s)+\frac{Y_{i k}^{(k)}(s) \times Y_{j k}^{(k)}(s)}{\sum_{l} Y_{l k}^{(k)}(s)}, \quad \forall n_{l} \in \Gamma_{k}^{(k)} \tag{3}
\end{equation*}
$$

If $I_{k}^{(k)} \neq 0$,

$$
\begin{equation*}
I_{i}^{(k+1)}(s)=I_{i}^{(k)}(s)+\frac{Y_{i k}^{(k)}(s)}{\sum_{l} Y_{l k}^{(k)}(s)} I_{k}^{(k)}(s), \quad \forall n_{l}, n_{i} \in \Gamma_{k}^{(k)} \tag{4}
\end{equation*}
$$

For admittances and current sources not mentioned above, they will be inherited from $G_{k}$ to $G_{k+1}$.
The theorem states that when we perform Y- $\Delta$ transformation on $n_{k}$, neighbors of $n_{k}$ in $G_{k}$ will become pairwise adjacent in $G_{k+1}$. And if $n_{k}$ in $G_{k}$ has a current source, then each of its neighbors will have a current source in $G_{k+1}$. It can be proven by solving for $v_{k}$ from the KCL of $n_{k}$ in terms of other voltage variables and substituting the solution for $v_{k}$ in other KCL equations. The proposed transformation is different from Gauss elimination, because we calculate $Y_{i j}^{(k+1)}$ in (3) only up to the term of order $\beta$. Since computation of higher-order terms is skipped, we get an approximation of $Y_{i j}^{(k+1)}$ whose numerator and denominator are equal to the first $\beta$ terms in $Y_{i j}^{(k+1)}$ 's numerator and denominator, respectively. Stable admittances and transfer functions can be obtained from $\Delta$ admittances because of this nice property [14]. This further treatment is not in DTT.
Corollary 1: If all RLC elements in a linear network are positive, $Y_{i j}^{(k+1)}$ in (3) is a rational function of $s$

$$
\begin{equation*}
Y_{i j}^{(k+1)}(s)=\frac{A_{i j}^{(k+1)}}{B_{i j}^{(k+1)}}=\frac{\sum_{p=0}^{m} a_{p} s^{p}}{\sum_{q=0}^{n} b_{q} s^{q}}, \tag{5}
\end{equation*}
$$

and $a_{p}, b_{q}$ in (5) are non-negative.
Corollary. 1 holds because no subtraction is introduced in (3). Note that all the coefficients of powers of $s$ in (5) are computed numerically.

## 3. Common-FActor Effects

Common factors are introduced into the numerator and denominator of new admittance and current source in (3) and (4). They are harmful because 1) they cause exponential growth of the magnitude of coefficients in the numerators and denominators; 2) they create fake zeros/poles that hamper pole/zero approximation. Unfortunately, common-factor effects are present even though we perform truncations. First we go through an example to show when these common factors are generated and what they are composed of. Then we will give theorems for their existence. The impact of common-factor effects on $\mathrm{Y}-\Delta$ admittances is discussed in the experiments.

(a): $G_{0}$

(b): $G_{1}$

(c): $G_{2}$

Fig. 2. A numerical example showing common factor existence: (a) $n_{0}$ is to be eliminated; (b) $n_{1}$ is to be eliminated; (c) $n_{0}$ and $n_{1}$ eliminated.

### 3.1. Example on Common-Factor Effects

A second-order circuit is given in Fig. 2(a). We want to apply Y- $\Delta$ transformation on $n_{0}$ and $n_{1}$ orderly. First we eliminate $n_{0}$ using (3) in Fig. 2(a). After the transformation, we have six admittances in (b):

$$
\begin{array}{lll}
Y_{12}^{(1)}=\frac{2 s}{\omega} & Y_{14}^{(1)}=\frac{8 s^{2}}{\omega} & Y_{24}^{(1)}=\frac{4}{\omega} \\
Y_{13}^{(1)}=\frac{6 s^{2}}{\omega} & Y_{23}^{(1)}=\frac{3}{\omega} & Y_{34}^{(1)}=\frac{12 s}{\omega}
\end{array}
$$

where $\omega \equiv 1+7 s+2 s^{2}$. Note that $\omega$ is the common denominator of the new admittances in (6).

Next we eliminate $n_{1}$ in (b). The admittance in (c) is computed again using (3). for instance,

$$
\begin{equation*}
Y_{23}^{(2)}=Y_{23}^{(1)}+\frac{Y_{12}^{(1)} \times Y_{13}^{(1)}}{Y_{12}^{(1)}+Y_{13}^{(1)}+Y_{14}^{(1)}} \tag{7}
\end{equation*}
$$

Insert (6) into (7),

$$
\begin{equation*}
Y_{23}^{(2)}=\frac{6 s+42 s^{2}+12 s^{3}}{\omega\left(2 s+14 s^{2}\right)} \tag{8}
\end{equation*}
$$

Note that we have considered $\omega$ as a common denominator, so that the result in (8) is simplified. Without identifying this common denominator, we would have

$$
\begin{equation*}
Y_{23}^{(2)}=\frac{6 s \omega^{2}+18 s^{2} \omega^{2}+24 s^{2} \omega^{2}+12 s^{3} \omega^{2}}{\omega\left(2 s \omega^{2}+6 s^{2} \omega^{2}+8 s^{2} \omega^{2}\right)} \tag{9}
\end{equation*}
$$

$\omega$ in (6) is called "type-I common factor", when it is shared explicitly among denominators of admittances incident to the node being eliminated, e.g., $n_{1}$ in Fig. 2(b). Note that the existence of type-I common factor is not straightforward in the case that $n_{1}$ is also adjacent to nodes besides $n_{0}$ 's neighbors when $n_{1}$ is eliminated, and in the case that other nodes are eliminated before $n_{1}$ and after $n_{0}$, when admittance merging happens. In the two cases, $\omega$ is generally just a factor shared by the denominators of some of the admittances incident to $n_{1}$.

To show the other kind of form of common factor $\omega$, we go back to look at (8). Interestingly, $\omega$ is present in not only the denominator, but also the numerator, because $6 s+42 s^{2}+12 s^{3}$ can be factorized as $6 s\left(1+7 s+2 s^{2}\right)=6 s \omega$. Therefore, $\omega$ is common factor shared by the denominator and numerator of $Y_{23}^{(2)}$. We compute $Y_{24}^{(2)}$ and $Y_{34}^{(2)}$ in the same way, and it turns out that $\omega$ is present in their numerators and denominators as well. Because the presence of $\omega$ is implicit in the numerators, we name it as "type-II common factor".

In summary, we have found out that Type-I common factor emerges explicitly in the denominator of every new admittance immediately after $n_{0}$ is eliminated. Type-II postpones until $n_{1}$ - the first neighbor of $n_{0}$ is eliminated, when type-II emerges implicitly in both the numerator and denominator of every new admittance induced by $n_{1}$. Both types of common factors need to be treated for numerical stability concerns. Sub-section 3.2 to 3.5 clarify the composition of common factors, and their existence in $\Delta$ admittances and current
sources for general linear network reduction using the proposed Y- $\Delta$ transformation. We will make the theorems easy to follow. Rigorous proofs can be found in [13].

### 3.2. Composition of Common Factor

Theorem 2: Suppose $n_{k}$ is to be eliminated, $n_{k}$ has $m$ neighbors in $G_{k}$, and the admittances of branches incident to $n_{k}$ are denoted as $\frac{A_{1}}{B_{1}}, \frac{A_{2}}{B_{2}}, \cdots, \frac{A_{m}}{B_{m}}$. Type-I and type-II common factors associated with $n_{k}$ are equal to $\omega_{k}$, which is defined as:

$$
\begin{equation*}
\omega_{k}=\frac{\sum_{i=1}^{m}\left(A_{i} \prod_{j=1, j \neq i}^{m} B_{j}\right)}{W_{k}}, \quad \text { where } \quad W_{k}=\prod_{i=0}^{k-1} \omega_{i}^{p_{i}} \tag{10}
\end{equation*}
$$

$p_{i}$ is the number of denominators in $\left\{B_{1}, B_{2}, \cdots, B_{m}\right\}$ with factor $\omega_{i}$.

Th. 2 defines the composition of type-I and type-II common factors ${ }^{4}$ in a recursive way, based on Th .1 . It is derived from the denominator of the new admittance ${ }^{5}$ in (3), and we have simplified it by considering type-I common factors in denominators $B_{1}, \cdots, B_{m}$. Each node has a unique $\omega$ which emerges at different times in different forms: it appears explicitly first in denominators as a typeI common factor, and it appears implicitly later in numerators and explicitly in denominators as a type-II common factor. $\omega$ 's are atomic common factors. $W_{k}$ defined in (10) are composed of one or more different atomic common factors.

### 3.3. Existence of Type-I Common Factor

A Type-I common factor associated with any node $n_{k}$ appears immediately after it is eliminated. We figure out that $\omega_{k}$ in (10) defines a factor that the denominator of every new admittance will have after $n_{k}$ is eliminated. Even when admittance merging happens, i.e., $Y_{i j}^{(k)} \neq 0$, the factor is still with the denominator of the resultant admittance $Y_{i j}^{(k+1)}$. Because when two admittances are merged, their denominators are multiplied together. So that a factor in any denominator before the merging is still a factor in the new denominator after it. We formally state the observation in Th. 3.

Theorem 3 (Existence of Type-I Common Factor): After $n_{k}$ in $G_{k}$ is eliminated, $\forall n_{i}, n_{j} \in n_{k}$ 's neighbors $\Gamma_{k}^{(k)}$, the denominator of new admittance $Y_{i j}^{(k+1)}$ in $G_{k+1}$ will have a factor $\omega_{k}$ associated with $n_{k}$.

### 3.4. Existence of Type-II Common Factor

A Type-II common factor associated with any node $n_{k}$ appears in both numerators and denominators of new admittances after $n_{k}$ 's first neighbor is eliminated. Identifying the existence of type-II common factors is very difficult, because they appear in numerators implicitly. The proof of their existence is a key step towards an effective and efficient $\mathrm{Y}-\Delta$ transformation for our reduction purpose.

Theorem 4 (Existence of Type-II Common Factor): Suppose $n_{f}$ is the first neighbor of $n_{k}$ in $G_{k}$. After $n_{k}$ and $n_{f}$ are eliminated, $\forall n_{i}, n_{j} \neq n_{f} \in n_{k}$ 's neighbor set, the numerator and denominator of new admittance $Y_{i j}^{(f+1)}$ in $G_{f+1}$ has a common factor $\omega_{k}$ associated with $n_{k}$.

Theorem 5 (Recursive Existence of Type-II Common Factor): Suppose $n_{f}$ is the first neighbor of $n_{k}$ in $G_{k}$, and $\omega_{k}$ is associated with $n_{k}$. Th. 4 holds no matter how many nodes are eliminated and how many type-II common factors are cancelled in $\Delta$ admittances between $n_{k}$ 's and $n_{f}$ 's eliminations.

Similar to mathematical induction, Th. 4 lays the foundation of the existence of type-II common factors, and Th. 5 affirms that it is safe to cancel type-II common factors at any time after they appear.

[^2]
### 3.5. Existence of Common Factor in Current Source Transformation

Current source transformation is performed along with Y- $\Delta$ transformation. Common-factor effects in the latter occur in the former as well. We summarize them in Th. 6.

Theorem 6: Suppose $n_{f}$ is the first neighbor of $n_{k}$ in $G_{k}$, and $\omega_{k}$ is associated with $n_{k}$.

1) If $n_{k}$ has a current source $I_{k}^{(k)}$, then after $n_{k}$ is eliminated, $\forall n_{i} \in n_{k}$ 's neighbors $\Gamma_{k}^{(k)}$, the denominator of new current source $I_{i}^{(k+1)}$ in $G_{k+1}$ will have a factor $\omega_{k}$;
2) After $n_{k}$ and $n_{f}$ are eliminated, $\forall n_{i} \in n_{k}$ 's neighbors $\Gamma_{k}^{(k)}$ in $G_{f+1}$, the numerator and denominator of new current source $I_{i}^{(f+1)}$ has a common factor $\omega_{k}$. The statement holds no matter how many nodes are eliminated and how many type-II common factors are cancelled in Y- $\Delta$ admittances and transformed current sources between $n_{k}$ 's and $n_{f}$ 's eliminations.

## 4. $\Delta$ Admittance Realization

After eliminating all internal nodes using Y- $\Delta$ transformation, we realize each $\Delta$ branch by calling for a positive real function approximation. Since all the coefficients in $\Delta$ admittances are nonnegative, first order realization is guaranteed. Therefore Elmore delays of original networks are preserved. Higher order realization of $\Delta$ admittances ${ }^{6}$ are achieved by choosing a passive template structure with the same order and do the approximation with the constraints that each element needs to be positive and the moments are kept approximately the same.

### 4.1. First Order Realization

For any second-order $\Delta$ admittance of the form

$$
\begin{equation*}
Y_{i j}(s)=\frac{a_{0}+a_{1} s}{b_{0}+b_{1} s} \tag{11}
\end{equation*}
$$

it is seen from Corollary 1 that

$$
a_{0}, a_{1}, b_{0}, b_{1} \geq 0
$$

If $a_{0} b_{1} \leq a_{1} b_{0}, Y_{i j}(s)$ in (11) can be realized using circuit in Fig. 3(a), where

$$
\begin{equation*}
R_{1}=\frac{b_{1}}{a_{1}} \quad R_{2}=\frac{a_{1} b_{0}-a_{0} b_{1}}{a_{0} a_{1}} \quad C=\frac{a_{1}^{2}}{a_{1} b_{0}-a_{0} b_{1}} \tag{12}
\end{equation*}
$$

otherwise it can be realized using circuit in (b), where

$$
\begin{gathered}
R_{1}-\frac{b_{0}}{a_{0}} \quad R_{2}=\frac{a_{0} b_{1}-a_{1} b_{0}}{a_{0} a_{1}} \quad L=\frac{a_{0} b_{1}-a_{1} b_{0}}{a_{0}^{2}} . \\
R_{\text {(a) }}^{R_{1}} \quad R_{2}
\end{gathered}
$$

Fig. 3. First Order Realization: (a) RC configuration; (b) RL configuration
Consider RC networks with each branch consisting of a conductance $g_{i}$ and capacitance $c_{i}$ in parallel. Some elements may be absent, in which case the corresponding $g_{i}$ or $c_{i}$ is zero. For such a network with two branches, the $\Delta$ admittance after the center node is eliminated is

$$
Y_{12}(s)=\frac{g_{1} g_{2}+\left(g_{1} c_{2}+g_{2} c_{1}\right) s}{\left(g_{1}+g_{2}\right)+\left(c_{1}+c_{2}\right) s}
$$

It can be seen that

$$
g_{1} g_{2}\left(c_{1}+c_{2}\right) \leq\left(g_{1} c_{2}+g_{2} c_{1}\right)\left(g_{1}+g_{2}\right)
$$

[^3]so that $Y_{12}$ can be realized using RC model shown in Fig. 3(a). It can be proven by induction that any $\Delta$ admittance reduced from any RC network can be realized by the RC model.

### 4.2. Template Realization

To achieve high-order approximation to $\Delta$ admittances, we devise high-order templates borrowed from circuit structures in admittance realization theory. For example, the circuit shown in Fig. 4 is a structure used in Brune's synthesis procedure [15]. When serialparallel withdrawal does not work for a realizable admittance, one can always invoke Brune's procedure to realize it using a similar structure. The method, however, can not be applied directly to $\Delta$ admittances, because these admittances are truncated so that they may not be realizable. But since the $\Delta$ admittances preserve low-order terms of exact input admittances, which are positive real functions, we can expect that the $\Delta$ admittances are positive and real within some moderate frequency range. This inspires us to use the general Brune's admittance structure as templates to approximate the $\Delta$ admittances, with the constraint that each element needs to be positive and the objective that the coefficients of the $\Delta$ admittances are matched as much as possible.

In Fig. 4, the shunt RLC box repeats the same circuit topology from the left, but elements may have different values. It is there because Brune's realization process may have multiple cycles. In each cycle, it uses the network on the left to the RLC box to reduce the order of the input admittance $Y(s)$ by two, until the RLC box can be realized by a conductance. In Brune's realized networks, $L_{1}$ is allowed to be either positive or negative, and $L_{3}$ always has an opposite sign to $L_{1}$. All other elements in Fig. 4 are positive. Admittance in the form of Fig. 4 is a positive real function of $s$, and the T inductor series can be replaced by an ideal transformer with positive primary and secondary indactances.


Fig. 4. Brune's Admittance Structure
We formulate the realization problem in Geometric Programming [16]. Suppose $Y(s)$ is a $\Delta$ admittance in the form of

$$
Y(s)=\frac{\sum_{0}^{\beta} a_{i} s^{i}}{\sum_{0}^{\beta} b_{i} s^{i}}
$$

Here $a_{i}$ and $b_{i}$ are positive real numbers. We build a $\beta$-th order Brune's admittance $\tilde{Y}(s)$ in Fig. 4, $\tilde{Y}(s)$ can be written as

$$
\begin{equation*}
\tilde{Y}(s)=\frac{\sum_{i=0}^{\beta} \tilde{a}_{i} s^{i}}{\sum_{i=0}^{\beta} \tilde{b}_{i} s^{i}} \tag{14}
\end{equation*}
$$

$\tilde{a}_{i}$ and $\tilde{b}_{i}$ are given by

$$
\begin{equation*}
\tilde{a}_{i}=\sum_{j=1}^{x\left(\tilde{a}_{i}\right)} p_{i j}, \quad \text { and } \quad \tilde{b}_{i}=\sum_{j=1}^{x\left(\tilde{b}_{i}\right)} q_{i j} \tag{15}
\end{equation*}
$$

where

$$
\begin{equation*}
p_{i j}=e_{i j} t_{1}^{\alpha_{i j_{1}}} t_{2}^{\alpha_{i j_{2}}} \ldots t_{\nu}^{\alpha_{i j_{\nu}}} \quad e_{i j}>0 \tag{16}
\end{equation*}
$$

and

$$
\begin{equation*}
q_{i j}=f_{i j} t_{1}^{\beta_{i j_{1}}} t_{2}^{\beta_{i j_{2}}} \ldots t_{\nu}^{\beta_{i j_{\nu}}} \quad f_{i j}>0 \tag{17}
\end{equation*}
$$

In (16) and (17), $t_{1}, t_{2}, \ldots, t_{\nu}$ are element variables in the template $\tilde{Y}(s), x\left(\tilde{a}_{i}\right)$ and $x\left(\tilde{b}_{i}\right)$ are the numbers of $p_{i j}$ and $q_{i j}$ in $\tilde{b}_{i}$ and
$\tilde{a}_{i}$, respectively. Note that they are not variables. Once a template is determined, $x\left(\tilde{a}_{i}\right)$ and $x\left(\tilde{b}_{i}\right)$ are known. We formulate the Geometric Programming problem as follows:
Objective function:

$$
\begin{equation*}
\min \left(\sum_{i=0}^{\beta} \sum_{j=0}^{x\left(\tilde{b}_{j}\right)} \frac{1}{p_{i j}}+\sum_{i=0}^{\beta} \sum_{j=0}^{x\left(\tilde{a}_{j}\right)} \frac{1}{q_{i j}}\right) \tag{18}
\end{equation*}
$$

subject to

$$
\begin{gather*}
t_{j}>0, \quad j=1,2, \ldots \nu  \tag{19}\\
\tilde{a}_{i} \leq a_{i}, \tilde{b}_{i} \leq b_{i} \quad i=0,2, \ldots \beta \tag{20}
\end{gather*}
$$

For example, given a 2 nd-order $\Delta$ admittance

$$
\begin{equation*}
Y(s)=\frac{a_{0}+a_{1} s+a_{2} s^{2}}{b_{0}+b_{1} s+b_{2} s^{2}} \tag{21}
\end{equation*}
$$

we substitute $R_{1}$ for $R$ and $R_{2}$ for the RLC box in Fig. 4 and use the resultant network as a template. Hence the input admittance $Y(s)$ of the template is

$$
\begin{equation*}
\tilde{Y}(s)=\frac{L R+\left(L_{1}^{2}+C L R_{1} R_{2}\right) s+\left(C L_{2}^{2} R_{1}+C L^{2} R_{2}\right) s^{2}}{L+C L R_{2} s+C L_{2}^{2} s^{2}} \tag{22}
\end{equation*}
$$

where $R \equiv R_{1}+R_{2}$ and $L \equiv L_{1}+L_{2}$. In this case, $p_{i j}$ and $q_{i j}$ in (18) are product terms in the numerator and denominator of (22) respectively; $t_{j}$ in (19) is elements in the template; $\tilde{a}_{i}$ and $\tilde{b}_{i}$ are symbolic coefficients of powers of $s$ in (22), and $a_{i}$ and $b_{i}$ are numerical coefficients in (21). The objective function tries to minimize the reciprocals of $p_{i j}$ and $q_{i j}$. On the other hand under the constraints that these terms can not be greater than the numerical coefficients in (21). When the objective function is minimized and the inequalities in the constraints all become equalities, then the coefficients in (21) are matched, and so are the moments.

In summary, first-order realization of $\Delta$ admittances is guaranteed, and high-order realization is accomplished by template optimization. The new realization procedure works effectively for templates of order ten or less in our experiments. And the merit of the method is that templates are realized and preserve electrical properties at the port simultaneously.

## 5. Reduction Flow

| PROCEDURE reduction_flow( $c k t$ ) |  |
| :---: | :---: |
| 1 | Transform voltage sources to current sources; |
| 2 | Decouple any floating current source in $c k t$; |
| 3 | WHILE ( $\exists$ internal node in ckt) DO |
| 4 | Pick node $n_{k}$ with the minimum degree; |
| 5 | IF $I_{k} \neq 0$ THEN |
| 6 | CALL current_source_transformation for $I_{k}$; |
| 7 | CALL Y- $\Delta_{-}$transformation to eliminate $n_{k}$; |
| 8 | Remove common factors from new admittances; |
| 9 | Remove $n_{k}$ and its incident branches from ckt; |
| 10 | Realize all $\Delta$ admittances. |

The proposed Y- $\Delta$ transformation is more efficient than LU decomposition in SPICE in two ways. First, it invokes LU decomposition only once in $s$-domain. But SPICE does it repeatedly due to varying time steps for stiff circuits. Secondly, the algorithm allows dynamical memory de-allocation, as branches of nodes eliminated are no longer needed and can be freed. As a result, the memory requirement grows up in the middle of the reduction process and goes down until it terminates. Memory requirement in SPICE grows monotonically until LU decomposition is completed. This also means less non-zero fill-ins, which result in faster decomposition.

## 6. Examples

We use a few examples in order to show the superiority of proposed realizable parasitic reduction in terms of orders of reduced models and reduction efficiency. The first example is a high-performance clock distribution circuit in a real design case. The circuit with 78564 nodes is a mixture of RC trees and meshes. Our method can reduce it to a simple RLC circuit with four nodes only. Circuit given in Fig. 5(b) is such a realization. In this case, if we use TICER to achieve


Fig. 5. Comparison of two reduced circuits
the same reduction ratio $^{7}$, the reduced circuit is given in Fig. 5(a). Both methods realize each admittance using passive elements. But none of them in (a) matches first-order moments. In (b) the two shunt admittances to ground match to the first order, and the floating one matches to the second order. In Fig. 6, we use a reduced circuit with higher order branch admittance to generate the waveform. The waveform for TICER-reduced circuit (Fig. 5(a)) is also given in the figure for comparison. We find out that the waveform of $\mathrm{Y}-\Delta$ are closer to the SPICE result without reduction.


Fig. 6. Comparison of responses of a RC network
The response of a 62 -node RLC network with 7 ports is plotted in Fig. 7. The response evaluated from the reduced network is very close to PRIMA's curve, and both of them are very close to SPICE output. Table I summarize the reduction ratio versus reduction efficiency. It

| reduction ratio | Reduction Time(sec) |  |
| :---: | ---: | ---: |
|  | TICER | $\mathrm{Y}-\Delta$ |
| $20 \%$ | 5.96 | 6.58 |
| $40 \%$ | 23.58 | 23.79 |
| $60 \%$ | 56.37 | 25.42 |
| $80 \%$ | 249.33 | 27.76 |
| $99 \%$ | 1634.78 | 46.56 |

TABLE I

## EfFICIENCY COMPARISON

is clear that minimizing non-zero fill-ins is a good ordering scheme in Y- $\Delta$ transformation-based reduction techniques. For instance, TICER eliminates nodes by picking those with extreme time constants first. Because it does not consider the degrees of nodes when eliminating them, this ordering scheme is inferior in terms of efficiency when

[^4]

Fig. 7. Comparison of responses of a RLC network
high reduction ratio is preferred. Although one can specify a limit on reduction ratio in TICER, the complexity may be passed to the following simulation stage because the sparsity of circuits has been tampered already. A compromise of the two ordering schemes is also promising.


Fig. 8. Illustration of Common-Factor Effects
Fig. 8 compares the magnitude growth of coefficients in one branch admittance. The two ends of the branch are ports so that other admittances were merged onto it. The figure plots the coefficients collected from different powers of $s$. In the case when common factors are not canceled, they grow wildly along with reduction percentage, so that most of the actual significant bits are lost due to finite-precision computation. In fact, the higher the order is, the faster coefficients grow. For instance, the figure shows that the 2ndorder coefficient is smaller than the 1st-order one at low reduction ratio, but becomes larger at higher ratio. Because coefficients grow at different rate, the problem can not be solved by scaling. It is shown by the line with square dot that $\mathrm{Y}-\Delta$ becomes practically useful only after common-factor cancelations are invoked.

As a special case of general Y- $\Delta$ transformation, DTT method does not raise common-factor question because they are tree-based. If nodes in a tree are eliminated in a bottom-up fashion, no non-zero fill-ins will be introduced. Therefore common factors do not exist in reduction of tree-structured circuits.

Fig. 9 plots locations of poles on complex plane. These poles are approximated for a power-ground design extracted at board-level with RLC elements. We leave only one current source (port) in the circuit because PRIMA's accuracy heavily depends on the number of ports. We find out that PRIMA does not return any complex poles. And although we have observed voltage oscillation in a wide frequency range, unfortunately SPICE is not able to do pole analysis on this stiff circuit either. However some low frequency poles are evaluated using $\mathrm{Y}-\Delta$ transformation. The example shows the contribution of common-


Fig. 9. Pole approximation
factor cancelations to numerical stability of Y- $\Delta$ transformation from another aspect.

## 7. Conclusions and Future Work

We have proposed a realizable parasitic RCLK reduction method based on generalized Y- $\Delta$ transformation technique. Since the method is RCLK-in-RCLK-out, it is compatible with general simulators such as SPICE. The algorithm employs common-factor identification and cancelation operations to make Y- $\Delta$ transformation numerically stable. First-order realization of $\Delta$ admittances is guaranteed, and high-order realization is achieved by template optimization using Geometric Programming.

The algorithm can be easily extended to multi-port reduction. One can either stop Y- $\Delta$ reduction at any point and realize branches, or call for $\Delta$-Y transformation, which is equivalent to LU's back solving. Like the forward Y- $\Delta$ transformation, the backward $\Delta-\mathrm{Y}$ also has the two types of common factors to cancel out.

## ACKNOWLEDGMENTS

The authors would like to thank Prof. E.S. Kuh for his advise on this work. They would also thank all the reviewers for their constructive comments and careful proofreadings.

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[^0]:    This work was supported in part under grants from NSF project number MIP9987678 and the California MICRO program.

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    DAC 2003, June 2-6, 2003, Anaheim, CA, USA.
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[^1]:    ${ }^{1}$ A RLCK-VJ network contains resistors, capacitors, inductors, K elements, independent voltage and current sources only.
    ${ }^{2} \mathrm{Y}$ and $\Delta$ admittances are referring to inputs and outputs of a $\mathrm{Y}-\Delta$ transformation.
    ${ }^{3}$ Voltage sources and floating current sources can be converted to shunt current sources using source transformation, and initial conditions can be simply modeled as constant current or voltage sources in $s$-domain.

[^2]:    ${ }^{4}$ Common factor associated with a node changes according to different $\beta$ (order of truncation). But the common-factor effects are persistent, $\forall \beta>0$.
    ${ }^{5}$ It refers to the admittance on the right in (3) before the merging.

[^3]:    ${ }^{6} \Delta$ admittances can be changed to stable admittances before the realization is called.

[^4]:    ${ }^{7}$ The original TICER algorithm uses time constant of each node as an error control mechanism, therefore such a high reduction ratio may not be allowed in the algorithm. The point of our comparison is that Y- $\Delta$ achieves a better reduction ratio, yet the result is close enough to SPICE.

