Losses in Multilevel Crossover in VLSI Interconnects

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Abstract

The radiation and surface wave losses may give rise to electromagnetic interference (EMI) problems in high speed VLSI interconnects. Over and above there will be dielectric and conductor losses. These losses have been evaluated for multilevel interconnects by finite difference time domain (FDTD) technique. The crosstalk between lines in the same level as well as in different levels and propagation delays are also found.

1. Introduction

Today's high speed VLSI interconnects are having multilevel structures and designers feel concerned about losses induced by planar lines and discontinuities. Over and above the conductor and dielectric losses, the planar lines with discontinuities exhibit losses due to surface waves and space waves. These losses degrade the signal distribution and may lead to electromagnetic interference (EMI) problem and similar other unwanted effects. Several literatures are available where losses in microstrip lines including radiation and surface wave losses from discontinuities are found [1]-[7]. But no such information for multilevel structures is available. This paper aims at evaluating the conductor loss, dielectric loss, radiation loss and surface wave loss in a multiconductor multilevel structure. Multilevel crossover structure has been analyzed in [8] and equivalent circuit has been found. But the model does not account for radiation and surface waves. To account for all these full-wave effects, finite difference time domain (FDTD) technique [9] has been used in this paper. All the frequency dependent full-wave behaviors like dispersion, cross talk, radiation and surface waves are automatically accounted for in FDTD method. Surface impedance boundary condition (SIBC) [10] is used for incorporation of conductor loss. Dielectric loss is taken into account by taking the nonzero conductivity of the substrate. Near field to far field transformation (NFFF) [11] is used for the computation of radiated fields. FDTD method produces the TDR directly and no tedious parameter extraction or Fourier transform is required. From the voltages at different ports the propagation delays are calculated.

Crossover structures with and without bend, (shown in fig. 1) are analyzed in this paper. For truncation of the FDTD computation domain uniaxial perfectly matched layer (UPML) boundary [12] is used. Non-uniform grid FDTD scheme [13] is used as it saves memory and computer time.

2. Formulation

2.1. Uniaxial perfectly matched layer (UPML) boundary

Within the FDTD computation domain the fields are updated using Yee algorithm [9]. The computation domain is truncated by UPML boundary. In the UPML regions the fields are updated in two steps and non-split field formulation is used. Within the UPML regions the two-step update equations for E^z field component are obtained following the method illustrated in [12] as

$$D_{z_{i,j,k+\frac{1}{2}}}^{n+\frac{1}{2}} = \left(\frac{A_1}{A_2}\right) D_{z_{i,j,k+\frac{1}{2}}}^{n-\frac{1}{2}} + \left(\frac{1}{A_2}\right) (A_3 - A_4)$$
(1)

$$E_{z_{i,j,k+\frac{1}{2}}}^{n+\frac{1}{2}} = \left(\frac{C_1}{C_2}\right) E_{z_{i,j,k+\frac{1}{2}}}^{n-\frac{1}{2}} + \left(\frac{1}{C_2\epsilon_0\epsilon_r}\right) (C_3 - C_4)$$
(2)

where,

$$C_{1} = \kappa_{y} - \frac{\sigma_{y}}{2\epsilon_{0}\epsilon_{r}}$$

$$C_{2} = \kappa_{y} + \frac{\sigma_{y}}{2\epsilon_{0}\epsilon_{r}}$$

$$C_{3} = \left(\kappa_{z} + \frac{\sigma_{z}\Delta t}{2\epsilon_{0}\epsilon_{r}}\right) D_{z_{i,j,k+\frac{1}{2}}}^{n+\frac{1}{2}}$$

$$C_{4} = \left(\kappa_{z} - \frac{\sigma_{z}\Delta t}{2\epsilon_{0}\epsilon_{r}}\right) D_{z_{i,j,k+\frac{1}{2}}}^{n-\frac{1}{2}}$$

When $\kappa_x = \kappa_y = \kappa_z = 1$ then (1) and (2) take the form given in [12]. Gedney has suggested the use of $\kappa_i > 1$; (i = x, y or z) for better absorption of evanescent waves. To take advantage of this fact the expressions given by Gedney have been modified by incorporating κ_i . Similar twostep update equations can be found for other field components. The conductivity σ , permittivity ϵ and permeability μ are scaled within the UPML region in the normal direction while in the transverse directions they are kept same as the corresponding values in the computation domain. In the x direction, when this is normal to the UPML interface, σ_x and $\epsilon_x = \mu_x = \kappa_x$ are varied following the equations

$$\sigma_x(x) = \sigma_{x_{max}} \left(\frac{x}{d}\right)^m \tag{3}$$

$$\kappa_x(x) = 1 + \kappa_{x_{max}} \left(\frac{x}{d}\right)^m \tag{4}$$

where, d is the depth of the UPML region and m is the polynomial scaling factor. $\sigma_{x_{max}}$ and $\kappa_{x_{max}}$ are the maximum values of σ_x and $\epsilon_x = \mu_x = \kappa_x$ respectively at the outer face of the UPML boundary. The optimum value of $\sigma_{x_{max}}$ is given in [12] as

$$\sigma_{x_{max}} = \frac{m+1}{150\pi\sqrt{\epsilon_{eff}}} \tag{5}$$

where, ϵ_{eff} is the effective dielectric constant of the microstrip. The equations for other normal directions are similar to (1)-(5). In the rest of the paper the subscripts indicating normal directions are omitted and it is assumed that they are in any of the three directions, whichever is appropriate. The optimum values of κ_{max} are found following [14].

2.2. Surface impedance boundary condition (SIBC)

For incorporation of conductor loss in FDTD, SIBC is used. For implementation of SIBC the method given in [10] is adopted in this paper. Due to the frequency dependent nature of surface impedance, convolution is required in time domain. The surface impedance in the Laplace domain is

$$Z_c(s) = \eta \sqrt{\frac{s/a}{1+s/a}} \tag{6}$$

where, $a = \sigma/\epsilon$ and $\eta = \sqrt{\mu/\epsilon}$; σ is the conductivity of strip and ground plane conductors and the substrate have permittivity ϵ and permeability μ . Defining s' = s/a the normalized surface impedance is

$$Z_n(s') = Z_c(as')/\eta = \sqrt{s'/(1+s')}$$
(7)

The normalized surface impedance $Z_n(s')$ is approximated by first order rational functions as

$$Z_n(s') = 1 - \sum_{i=1}^{N} c_i / (s' + \omega_i)$$
(8)

where, N is the number of first order rational function used in the approximation. The values of c_i and ω_i are found by least square method. It is shown in [10] that with N = 8 the maximum relative error in $Z_n(s')$ is 0.0016 %. As s' is independent of material and geometrical parameters, the values of c_i and ω_i have to be computed once only and those values can be used for all simulations. In time domain the tangential electric and magnetic fields at the conductordielectric interface are related by

$$E_t(t) = Z_c(t) * [\hat{n} \times H_t(t)]$$
(9)

where, * denotes convolution. $Z_c(t)$ can be found by inverse Laplace transform of (8). Assuming the waves to be piecewise linear in time and implementing recursive convolution, the update equation for tangential electric fields are

$$E_t(n\Delta t) = \eta[\hat{n} \times H_t(n\Delta t)] - \sum_{i=1}^N F_i(n\Delta t)$$
(10)

where,

$$F_{i}(n\Delta t) = p_{i1}[\hat{n} \times H_{t}(n\Delta t)] + p_{i2}[\hat{n} \times H_{t}\{(n-1)\Delta t\}$$
$$+ p_{i3}F_{i}\{(n-1)\Delta t\}$$
$$p_{i1} = \frac{\eta c_{i}}{\omega_{i}} \left[1 + \frac{(e^{-q} - 1)}{q}\right]$$
$$p_{i2} = \frac{\eta c_{i}}{\omega_{i}} \left[\frac{1}{q} - e^{-q}\left(1 + \frac{1}{q}\right)\right]$$
$$p_{i3} = e^{-q}$$
$$q = a\omega_{i}\Delta t$$



Figure 1. Multilevel crossover. (a) 3-D view. SiO_2 and conductors are shown in gray and black respectively. Five SiO_2 layers (ϵ_r =3.9) are 1 μ m thick and the copper conductor ($\sigma = 4.1 \times 10^7 Sm^{-1}$) strips are of width 1 μ m and thickness 1 μ m. (b) Crossover without bend. (c) Crossover with bend. In (b) and (c) numbers within circle indicate port number.

2.3. Near field to far field transform (NFFF) and the radiated fields

The far field will contain only $\hat{\theta}$ and $\hat{\phi}$ components. For evaluation of radiated far field, a surface called near field to far field surface is taken which is inside the computation domain and encompasses the structure. The six faces of the near field to far field surface are divided into patches by the FDTD grid. The tangential electric and magnetic fields at the center of each patch are found by taking average of adjacent fields. The scheme for computation of far field is detailed in [11]. The time domain far fields are Fourier transformed to obtain frequency domain far fields $E_{\theta}(\omega)$ and $E_{\phi}(\omega)$. Integrating these fields the radiated power is found as

$$R(\omega) = \frac{\pi r^2}{\eta} \int_{0}^{2\pi} \int_{0}^{\pi} \left[\left| E_{\theta}(\omega) \right|^2 + \left| E_{\phi}(\omega) \right|^2 \right] \sin(\theta) d\theta \, d\phi \quad (11)$$

where, $\eta = 120\pi$ is the free space wave impedance.

3. Numerical results

Crossovers, with and without bend, are analyzed in this paper. The structures are shown in fig. 1. The two-level crossover has two conductors in each level. The conductors are of Copper (conductivity $\sigma = 4.1 \times 10^7 Sm^{-1}$) with width 1 μ m and thickness 1 μ m. The five SiO_2 layers are

of thickness 1 μ m and ϵ_r =3.9 and covered by two ground conductors (copper) of thickness 1 μ m on both top and bottom. This structure is taken from [15]. The line lengths and separations are shown in fig. 1. The excitation pulse is trapezoidal with rise-time and fall-time $\tau_r = \tau_f = 380$ fs and on-time τ_{on} =380 fs. The TDR are shown in figs. 2 and 3 for crossovers without bend and with bend respectively. A major portion of the incident power is transmitted through the excited line and reaches port 2. The propagation delays for the signal to reach from port 1 to port 2 are 1.2 ps and 0.2 ps respectively for with and without bend case. There is considerable coupling between the lines in the same level as well as between lines in different levels. These coupled signal amplitudes are nearly 10 % of that of the incident pulse. Due to presence of bend, reflection is observed in V_1 for the crossover with bend structure. This reflection is not present in the crossover without bend structure. Due to the bend, a significant crosstalk is observed in V_3 after around 2 ps.

The conductor and dielectric losses are shown in fig. 4. For computation of conductor and dielectric losses the structure taken has only one line in level 2 and no line in level 1. For computation of dielectric loss the conductivity of SiO_2 is taken to be $\sigma_d = 1.0 \times 10^{-4} Sm^{-1}$ while the conductivity of conducting lines are set to a very high value $(\sigma_c = 1.0 \times 10^{70} Sm^{-1})$. For evaluation of conductor loss, $\sigma_d = 0$ and $\sigma_c = 4.1 \times 10^7 Sm^{-1}$. It can be seen that at all frequencies the conductor loss is higher compared to dielectric loss. This higher conductor loss is as expected [1].



Figure 2. Voltages at different ports of the crossover without bend. Excitation is given at port 1 with a trapezoidal pulse of $\tau_r = \tau_f = \tau_{on}$ =380 fs. V_i indicates voltage at port i. Upper graph (V_1 to V_4) shows voltages in level 2 and the lower graph (V_5 to V_8) shows voltages in level 1.

For comparison purpose, the conductor loss for microstrip on alumina (ϵ_r =9.9 and height h=0.64 mm) with strip width w = 0.04 mm is plotted in the inset of fig. 4 along with the result given in [1]. This shows good match between our result and the result of [1].



Figure 3. Voltages at different ports of the crossover with bend. Excitation is given at port 1 with a trapezoidal pulse of $\tau_r = \tau_f = \tau_{on}$ =380 fs. V_i indicates voltage at port i. Upper graph (V_1 to V_4) shows voltages in level 2 and the lower graph (V_5 to V_8) shows voltages in level 1.

For the computation of radiation loss, the crossover with bend structure is taken. The top conducting ground plane is removed so that the structure can radiate. The simulation has been performed for zero conductor and dielectric losses. The structure is excited by Gaussian pulse with



Figure 4. Conductor and dielectric losses for a straight conductor placed on level 2 and no conductor in level 1. The structure has five SiO_2 layers (ϵ_r =3.9, $\sigma_d = 1.0 \times 10^{-4}Sm^{-1}$) of thickness 1 μ m. The conducting strip is of copper ($\sigma_c = 4.1 \times 10^7 Sm^{-1}$) of width 1 μ m and thickness 1 μ m. Inset: Comparison of conductor loss for a single microstrip line on alumina (ϵ_r =9.9 and height h=0.64 mm) with strip width w=0.04 mm with the result of [1].

 τ_r =380 fs. The radiated power as a function of frequency is plotted in fig. 5. The S-parameters are also computed for the crossover with bend that have zero conductor and dielectric losses. From1 $-\sum |S_{ij}|^2$; i = 1,2,...,8 and j = 1; the total loss is found. As there is no conductor and dielectric losses, this total loss is due to radiation and surface wave losses. Thus FDTD computed normalized radiated power when subtracted from the total loss gives surface wave loss. The total loss and surface wave loss are also shown in fig. 5. At low frequencies the surface wave loss is higher and both radiation and surface wave losses increases with frequencies. But beyond 91 GHz radiation loss becomes higher compared to surface wave loss.

4. Conclusion

Different losses are found in this paper for multilevel crossover with and without bend. Conductor loss is higher compared to dielectric loss for these structures. For crossover with bend there is considerable radiation loss and this loss increases with frequency. There is also problem of surface waves. These radiation and surface waves may give rise to EMI problems. Pulse responses of multilevel crossovers have been computed directly and thus no Fourier transform or tedious parameter extraction is required.



Figure 5. Total loss, radiation loss and surface wave loss for the crossover with bend. Conductor and dielectric losses are assumed zero.

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