

# Push-Up Scheduling: Optimal Polynomial-Time Resource Constrained Scheduling for Multi-Dimensional Applications\*

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*Abstract— Multi-dimensional computing applications, such as image processing and fluid dynamics, usually contain repetitive groups of operations represented by nested loops. The optimization of such loops, considering processing resource constraints, is required to improve their computational time. This study presents a new technique, called push-up scheduling, able to achieve the shortest possible schedule length in polynomial time. Such technique transforms a multi-dimensional data flow graph representing the problem, while assigning the loop operations to a schedule table in such a way to occupy, legally, any empty spot. The algorithm runs in  $O(n|E|)$  time, where  $n$  is the number of dimensions of the problem, and  $|E|$  is the number of edges in the graph.*

## 1 Introduction

In the study of implementing a solution for simulating partial differential equations, our research group was challenged by the need of obtaining an optimized execution time for each simulation point, under the restriction of the number of computational resources available. The group decision was to improve the total execution time by reducing the time spent in the computation of each point to its shortest possible value. The creation of a new scheduling algorithm was required to achieve such a goal, since most of the existing scheduling methods do not consider the multi-dimensional (MD) characteristics of the problem and are not able to achieve an optimal schedule.

Some recent research has been conducted in the scheduling of MD applications, such as the affine-by-statement technique [1] and the index shift method [3]. However, these methods do not consider resource constrained designs. The MD rotation “heuristic” technique, proposed in [4], can possibly obtain a shorter schedule length at each iteration of the algorithm. However, the optimality of the results depends upon an user input parameter that determines the number of iterations to be executed.

Considering that the most time-consuming sections of MD applications consist of loops of repetitive operations, we focus in their optimization. The loops are modeled by cyclic data flow graphs, usually called *MD data flow graphs* (MDFGs). In our algorithm, the loop body, is restructured

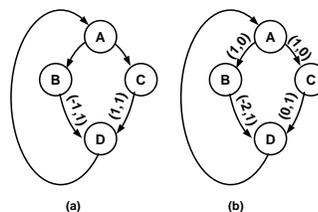


Fig. 1: (a) MDFG representing a section of a wave digital filter (b) MDFG after retiming A and D

by transformations applied to the MDFG equivalent to that loop. The schedule length is associated with the number of control steps, i.e., the clock cycles of the circuit design, required to execute all operations in the loop body. Our algorithm achieves the minimum possible schedule length in a resource constrained environment by applying a MD retiming technique, named *push-up scheduling*. At every new control step, nodes are selected to be assigned to functional units, and *pushed-up* to earlier control steps if the required functional unit is available. The *push-up* operation will activate an implicit MD retiming if necessary in order to do the earliest assignment.

Let’s examine a simple problem represented by the MDFG in Figure 1(a). Nodes *B* and *C* are multiplications, while *A* and *D* are additions. Let’s assume our target hardware system has only one multiplier and one adder, both able to execute in one time unit. The application of a traditional list scheduling method produces a schedule table of length four, as shown in figure 2(a). By applying a MD retiming  $r(A) = r(D) = (1, 0)$  to the graph, we obtain a new one, shown in figure 1(b). It is clear that, in this transformed graph, nodes *A* and *D* still need to be executed sequentially, while *B* and *C* are part of a different iteration and can be scheduled in parallel with *A*, *D*.

## 2 Background

An *MD data flow graph* (MDFG)  $G$  consists of a tuple  $(V, E, d, t)$ , where  $V$  is the set of computation nodes,  $E$  represents the set of dependence edges,  $d$  is a function representing the MD delays between two nodes, and  $t$  is a function representing the computation time of each node. An *iteration* is equivalent to the execution of each node in  $V$

\*This work was supported in part by the NSF CAREER grant MIP 95-01006, and by the William D. Mensch, Jr. Fellowship.

CS	Adder	Mult.
1	D	-
2	A	-
3	-	B
4	-	C

(a)

CS	Adder	Mult.
1	D	B
2	A	C

(b)

Fig. 2: (a) Initial schedule (b) Optimized schedule

exactly once. An iteration is associated with a static schedule, that is repeatedly executed for the loop. The *earliest starting time* for the execution of node  $u$ ,  $ES(u)$ , is the first control step following the end of the execution of all nodes predecessors of  $u$  by a zero-delay edge. This can be represented as:  $ES(u) = \max\{1, ES(v_i) + t(v_i)\}$  for all  $v_i$  preceding  $u$  by an edge  $e_i$  such that  $d(e_i) = (0, 0, \dots, 0)$

A functional unit  $fu$  is available at control step  $cs$  if no node has been assigned to  $fu$  at such control step. Such data is recorded by the availability function  $AVAIL(fu)$  that returns the first control step where  $fu$  is available.

An *MD retiming*  $r$  redistributes MD delays in an MDFG  $G$ , creating a new MDFG  $G_r = (V, E, d_r, t)$ . A retiming vector  $r(u)$  applied to a node  $u \in G$  represents the offset between the original iteration containing  $u$ , and the one after retiming. Such vector represents MD delays pushed into the edges  $u \rightarrow v$ , and subtracted from the edges  $w \rightarrow u$ , where  $u, v, w \in G$ . The *chained MD retiming* technique [5] is one of the possible methods able to compute a legal MD retiming for some MDFG. It is characterized by important properties revisited below:

1. A legal MD retiming  $r$  of a node in an MDFG  $G$ , with all its incoming edges having non-zero delays, is any vector orthogonal to a schedule vector  $s$  that realizes  $G$ .
2. If  $r$  is a MD retiming function orthogonal to a schedule vector  $s$  that realizes  $G = (V, E, d, t)$ , and  $u \in V$ , then  $(k \times r)(u)$  is also a legal MD retiming.
3. The chained MD retiming algorithm transforms  $G$  to  $G_r$ , such that  $G_r$  is realizable and fully parallel.

The algorithm responsible to compute the chained retiming is able to produce a fully parallel MDFG.

### 3 The Push-Up Scheduling Technique

Let us begin the discussion on this technique by tracing the scheduling process of the operations in the MDFG of figure 1. Consider a target processor that has only two functional units, one multiplier and one adder, able to execute in a same amount of time, hereafter designated one control step. Let us start assigning the operations to the functional units. At the first control step of our schedule, only the addition represented by node  $D$  is ready for execution. We then say that  $D$  is a *schedulable node* if it satisfies one of the conditions below:

1.  $u$  has no incoming edges
2. all incoming edges of  $u$  have a non-zero MD delay
3. all the predecessors of  $u$ , connected to  $u$  by a zero-delay edge, have been scheduled to earlier control steps.

The existence of MD delays in the incoming edges of a node implies that the required input data has been produced

CS	Mult.	Adder
1	-	D

(a)

CS	Mult.	Adder
1	-	D
2	-	A

(b)

CS	Mult.	Adder
1	B	D
2	-	A
3	B	-

(c)

CS	Mult.	Adder
1	B	D
2	C	A
3	C	-

(d)

Fig. 3: Push-up scheduling sequence

in some previous iteration and are available from some storage mechanism. In our example,  $D$  is then assigned to the adder at control step 1, as shown in figure 3. Node  $A$  is assigned to the adder at control step 2.  $B$  and  $C$  become schedulable nodes at control step 3, according to scheduling condition 3. However, both nodes require the multiplier that is also available at control steps 1 and 2. In order to schedule these nodes to those control steps, it is necessary to change the graph in such a way that both nodes,  $B$  and  $C$ , can satisfy scheduling conditions 1 or 2.

The technique used to do such transformation is the MD retiming. It implies in pushing MD delays from the incoming edges of  $A$  to its outgoing edges. However, the incoming edges of  $A$  have zero delays. In order to bypass this new problem, we need to propagate the retiming to node  $D$  in a similar fashion as done in the *chained MD retiming*. In this simple example, node  $D$  is retimed by  $(1, 0)$  and subsequently, the same retiming is applied to node  $A$ , leaving the number of delays in edge  $D \rightarrow A$  unchanged. The resulting graph, however, will allow to schedule nodes  $B$  and  $C$  at control steps 1 and 2, respectively. We may express the need for a MD retiming by the lemma below:

**Lemma 3.1** *Given an MDFG  $G = (V, E, d, t)$  and an edge  $e = u \rightarrow v$ , such that  $v$  can be scheduled to  $ES(v)$  and  $d(e) = (0, 0, \dots, 0)$ , then a MD retiming of  $u$  is required if  $ES(v) > AVAIL(fu_v)$  where  $fu_v$  is any functional unit able to execute the operation represented by  $v$ .*

We need to be sure that all nodes in the graph are correctly retimed such that delays are placed in the required edges. In order to develop an efficient way to accomplish such a task, we define a MD delay counting function  $MC$ .

**Definition 3.1** Let  $G = (V, E, d, t)$  be an MDFG,  $v$  a node in  $V$ , and  $X$  a set of edges in  $E$  that require an extra MD delay. The function  $MC(v)$  gives the upper bound on the number of extra non-zero delays required by  $X$  along any path  $p = a_0 \xrightarrow{e_1} a_1 \xrightarrow{e_2} \dots \xrightarrow{e_{k-1}} a_{k-1} \xrightarrow{e_k} v, k \geq 1$  with  $d(e_i) = (0, \dots, 0), 1 \leq i \leq k$ .

Figure 4 shows an example of how the function  $MC$  works. Assume that the graph in figure 4(a) is to be transformed to the one shown in figure 4(b). Let us also assume that nodes  $C, D$  and  $F$  require their incoming edges to become non-zero delay edges. In this situation, the value of the function  $MC$  for nodes  $A, B$  and  $E$  is zero. For nodes  $C$  and  $F$ ,  $MC(C) = MC(F) = 1$ , which implies that the paths  $A \rightarrow B \rightarrow C$  and  $E \rightarrow F$  need one non-zero

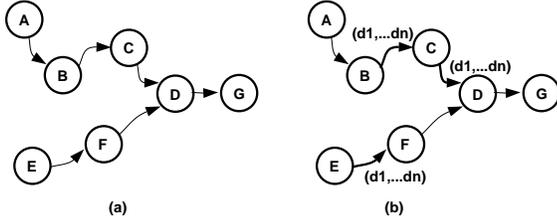


Fig. 4: Example showing the function MC with a value  $MC(D)=2$

delay. Node  $D$  is associated with the value  $MC(D) = 2$  due to the number of additional non-zero delays on path  $A \rightarrow B \rightarrow C \rightarrow D$ .

For now we assume that the values of  $MC$  are given. It is important for us to know how many times to retime each node in the MDFG in order to place the extra delays in the right edges. The theorem below shows how to compute an MD retiming function such that we can accomplish that goal.

**Theorem 3.2** *Given an MDFG  $G = (V, E, d, t)$ , a legal MD retiming  $r$ , a set of extra delays  $X$  and the function  $MC$  computed for the nodes in  $G$  with respect to  $X$ , if the MD retiming  $r(u) = (\max\{MC\} - MC(u)) * r$  is applied to every  $u \in V$  creating an MDFG  $G_r = (V, E, d_r, t)$ , then  $X \subseteq \{e \in E \mid d(e) = (0, \dots, 0) \wedge d_r(e) \neq (0, \dots, 0)\}$  and  $d_r(e) \neq (0, \dots, 0)$  if  $d(e) \neq (0, \dots, 0)$ .*

Let us re-examine our initial example in figure 1. If we traverse the graph starting from the initial schedulable nodes, we must begin by scheduling node  $D$ . Node  $A$  is assigned to the adder at control step 2. When trying to schedule nodes  $B$  and  $C$ , we are already at control step 3, i.e.,  $ES(B) = ES(C) = 3$ . However, the multiplier is available at earlier control steps, i.e.,  $AVAIL(fu_B) = AVAIL(fu_C) = 1$ . Selecting to schedule  $B$  at control step 1, implies  $ES(B) > AVAIL(fu_B)$  and according to lemma 3.1,  $A$  must be retimed. This implies an extra delay on the edge  $A \rightarrow B$ , changing  $MC(B)$  to 1. Similarly, when  $C$  is scheduled, we obtain  $MC(C) = 1$ .

In order to obtain a transformed graph equivalent to the new schedule, we select the schedule vector  $s = (0, 1)$  and consequently, the MD retiming vector  $r = (1, 0)$ . The application of a MD retiming according to theorem 3.2, considering that the maximum value for  $MC$  is 1, will result  $r(A) = (1 - MC(A)) * (1, 0) = (1, 0)$ , and  $r(D) = (1 - MC(D)) * (1, 0) = (1, 0)$ , while  $r(C) = r(D) = (0, 0)$ , producing the retimed graph shown in figure 1(b). The algorithm OPTIMUS, for OPTimal Multi-dimensional Scheduling, combining MD retiming techniques and the MD delay counting procedure is described below.

**Algorithm OPTIMUS**( $G = (V, E, d, t)$ )

Choose  $s = (s_1, s_2, \dots, s_n)$  such that  $s \cdot d(e) > 0$  for any  $e \in E$   
 Choose  $r$  such that  $r \perp s$   
 $ES(\forall u \in V) \leftarrow 0$

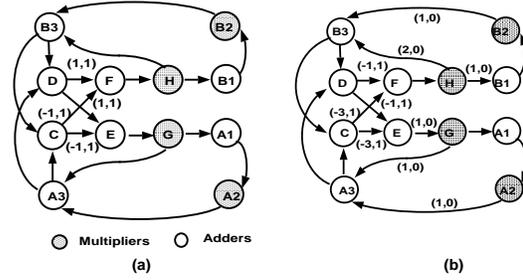


Fig. 5: (a) Wave Digital Filter MDFG (b) Final MDFG

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MC( $\forall u \in V$ )  $\leftarrow$  0
MCmax  $\leftarrow$  0
QueueV  $\leftarrow$   $\phi$ 
/* remove original edges with non-zero delays */
 $\forall e \in E, E \leftarrow E - \{e, s.t. d(e) \neq (0, \dots, 0)\}$ 
/* queue schedulable nodes */
QueueV  $\leftarrow$  QueueV  $\cup$   $\{u \in V, s.t. INDEGREE(u) = 0\}$ 
while QueueV  $\neq$   $\phi$ 
  GET(u, QueueV)
  /* check if u needs an extra non-zero delay */
  if AVAIL(fu) < ES(u)
    /* adjust the MC(u) value */
    MC(u)  $\leftarrow$  MC(u) + 1
    MCmax  $\leftarrow$  max{MC(u), MCmax}
  endif
  /* adjust ES(u) and schedule on the first possible control step*/
  ES(u)  $\leftarrow$  AVAIL(fu)
  ASSIGN(u) to fu at control step ES(u)
  /* propagate the values to successor nodes of u */
   $\forall v$  such that  $u \rightarrow v$ 
    INDEGREE(v)  $\leftarrow$  INDEGREE(v) - 1
    ES(v)  $\leftarrow$  max{ES(v), ES(u) + t(u)}
    /* assume this edge does not require a new delay */
    MC(v)  $\leftarrow$  max{MC(v), MC(u)}
    /* check for new schedulable nodes */
    if INDEGREE(v) = 0
      QueueV  $\leftarrow$  QueueV  $\cup$   $\{v\}$ 
    endif
endwhile
/* compute the MD retiming */
 $\forall u \in V, r(u) = (MCmax - MC(u)) * r$ 

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## 4 Experiments

In this section we present results of the application of our method to different problems. Because of the space limit, only one case is presented in detail, an MDFG representing a wave digital filter, designed to compute a solution for a Partial Differential Equations problem, a transmission line problem, based in the Fettweis method [2]. After applying the Fettweis transformations we obtain the MDFG in figure 5(a). The case of one adder and one multiplier available is trivial. Therefore, we assume that there are two two-input adders and one multiplier available and these devices require one time unit to complete.

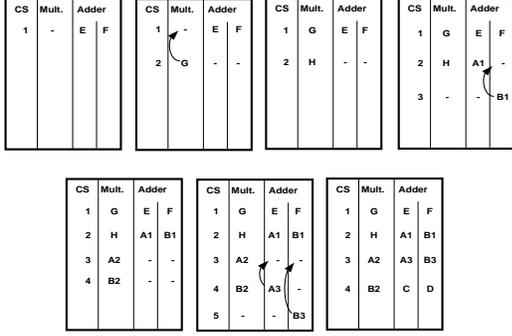


Fig. 6: Sequence of the push-up scheduling

Using the OPTIMUS algorithm, a schedule vector  $(0, 1)$  is selected with an MD retiming vector  $(1, 0)$ . The initial sequence of the scheduling algorithm, assigns nodes  $E$  and  $F$  to the adders in control step 1.  $H$  and  $G$  become schedulable nodes at control step 2. If  $G$  gets the chance to be assigned first, it must be pushed-up to control step 1, where the multiplier is still available. That implies that node  $E$  must be retimed, therefore,  $MC(G) = 1$ . At the end of the algorithm, we have  $MC(E) = MC(F) = MC(H) = 0$ ,  $MC(G) = MC(A1) = MC(A2) = MC(B1) = MC(B2) = 1$ , and  $MC(C) = MC(D) = MC(A3) = MC(B3) = 2$ , with a maximum  $MC$  value of 2. The resulting retiming function is  $r(E) = r(F) = r(H) = (2, 0)$ ,  $r(G) = r(A1) = r(A2) = r(B1) = r(B2) = (1, 0)$ , and  $r(C) = r(D) = r(A3) = r(B3) = (0, 0)$ . Figure 6 shows the changes in the schedule table, while figure 5(b) shows the retimed MDFG. The final schedule length of 4 control steps represents a significant reduction on the total computation time if compared to the 7 control steps required by a list scheduling algorithm.

Table 1 summarizes the comparison between our results and other methods, showing the achieved execution time and the complexity of the algorithm involved<sup>1 2</sup>. The row *list sch.* is based on the original design characteristics, using a traditional list scheduling method, while *OPTIMUS* shows the data for our proposed method, the row *rot.* shows the requirements imposed by the rotation scheduling method [4]. The row *affine-by-st.* presents results that could be obtained by modifying *affine-by-statement* methods developed for systolic arrays [1, 3], and finally, methods focused on fine-grain parallelism that also depend on the selection of a new schedule, such as the *reindexing technique* [6] are presented in row *fine-grain*. We notice that when the fully parallel solution was achieved, the complexity of the algorithms becomes one of the distinguishing elements on this comparison.

Table 2 shows a comparison of different practical experiments among those methods able to find a solution, without the need of finding a fully parallel graph. In order to provide a fair comparison based on the time complexity of

<sup>1</sup> ILP was used to represent methods with complexity equivalent to an Integer Linear Programming solution, or eventually to an LP algorithm.

<sup>2</sup>  $U$  was used, in the rotation heuristic, to represent an user input defining the number of iterations of the algorithm.

Method	schedule length	complexity
list scheduling	7	$O( E )$
affine-by-st.	4	ILP
fine-grain	4	ILP
rotation	4 (possibly optimal)	$O(nU E )$
<b>OPTIMUS</b>	<b>4 (always optimal)</b>	$O(n E )$

Table 1: Summary of results for the wave digital filter

Comparison with other results - final schedule length			
Test case	list sch.	rot.	<b>OPTIMUS</b>
transmission line	7	6	<b>4</b>
Floyd-Steimberg	5	4	<b>3</b>
Fwd-substitution	5	4	<b>3</b>
Toeplitz Hyp. Cholesky	4	3	<b>2</b>
DFT	5	4	<b>4</b>

Table 2: Summary of results for several problems

the techniques been reported, the value  $U = 1$  was adopted for the MD rotation scheduling. The problems reported on table 2 are the transmission line described earlier, and four other cases, where, for simplification, the target machine was assumed to be a parallel system with 3 general purpose functional units. Such problems are the Floyd-Steimberg, and the forward substitution algorithms, the Toeplitz Hyperbolic Cholesky solver, and a discrete Fourier transform (DFT).

Our experiments always achieved optimal results. From the tables, we notice that fully-parallel solutions are also able to produce such results, however, using non-efficient solutions such as ILP algorithms or introducing excessive number of delays. These results demonstrate the high efficiency of the push-up scheduling technique as well as the optimality of its results.

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