

# Message from the Chairs

On behalf of Codes+ISSS 2006 Organizing and Program Committees, we would like to welcome you to the 4<sup>th</sup> *IEEE/ACM International Conference on Hardware/Software Co-Design and System Synthesis*.

Codes+ISSS is the premier event in design of embedded systems hardware, software, and tools. This year we continued building upon the reputation of a high-quality conference by enforcing a rigorous reviewing process, selecting the best submitted papers, and assembling a very exciting program of keynotes, tutorials, special sessions, panels, and regular papers.

We received 183 full paper submissions and selected 46 papers for publication, an acceptance rate of 25%. The Program Committee carefully reviewed all papers, with an average of over 5 reviews per paper. The Committee then met face to face for detailed discussions on the papers. Selections were not based on quotas for each topic area, but instead, papers from one topic area were compared to other topic areas in order to select the absolute best set of papers overall. We hope you will find this year's papers to represent some of the most important advances in theory and practice of system-level design and related areas.

Codes+ISSS program is uniquely complemented by tutorials and workshops. This year's tutorials cover state-of-the-art topics such as UML for SoC Design and Automotive Electronics, and like last year, tutorials are free to all registered attendees. CODES+ISSS hosts two full-fledged workshops, namely ESTIMedia and a UML hands-on workshop. ESTIMedia has been very successful in previous years and its special emphasis on multimedia-related topics effectively complements the main Codes+ISSS program. The hands-on workshop on UML is a first of its kind in Codes+ISSS and gives attendees the opportunity to learn UML in practice.

After a successful debut last year, *Embedded Systems Week (ES Week)* takes place again, this time expanded with the addition of CASES (a leading conference on architecture and compilers). Codes+ISSS, EMSOFT and CASES are collocated with all sessions in parallel, and registrants to one conference can attend sessions in all three conferences. Moreover, the technical programs of the three conferences, including keynotes, special sessions, panels, tutorials and workshops, have been carefully planned to avoid major overlaps and complement each other. *ES Week* is uniquely positioned to offer the best technical content for attendees in the areas of embedded systems design, tools, software, architecture and compilers.

Codes+ISSS owes its high technical quality to the dedicated members of the Steering Committee, Organizing Committee, Technical Committee and numerous reviewers who contributed their time and expertise to ensure the quality of the conference. We thank them all full heartedly. We would also like to thank our sponsoring societies, IEEE Circuits and Systems Society (CAS) and Computer Society (CS) and the Council of Electronic Design Automation (CEDA), the ACM SigDA, SigBED and SigMICRO. Moreover, we would like to thank our several industrial contributors, without which the conference would not be possible. The excellent level of industrial support demonstrates that Codes+ISSS is able to bridge both academia and industry interests. Finally, thanks are due to all the people who submitted their work to the conference and to the attendees whose active participation is relied upon for lively and interesting technical exchanges.

This year the conference follows its worldwide rotation and takes place in Seoul, Korea, known for hosting the 1988 Olympic Games and the 2002 World Cup Soccer Championship. Seoul represents a unique merger of high-technology services and infrastructure with a historic city of over 600 years as the capital of Korea. The conference location promises to inspire the minds as well as the hearts of attendees. We hope you enjoy this year's Codes+ISSS and we are looking forward to your future participation next year in Austria!

**Reinaldo Bergamaschi**  
*General Chair*

**Soonhoi Ha and Kiyong Choi**  
*Technical Program Co-Chairs*