## Panel

# Are Current ESL Tools Meeting the Requirements of Advanced Embedded Systems?

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## Abstract

Electronic System Level (ESL) tools are becoming more and more important in order to bridge the well-known productivity design gap. This panel brings together specialists from industry and ESL tool houses to discuss whether current ESL tools available are usable and meeting the requirements of the industry. In particular, we would like to know to what degree currently available tools from major ESL tool houses are used for designing what kind of systems in industry, and for what purpose such as analysis, design space exploration, or synthesis, etc. these tools are currently used. Also, we would like to discuss to what degree existing tools can help in reducing design time, and finally, what the industry sees the most challenging features of tools currently not yet available. On the other side, we would like to know the opinion of the ESL tool houses what they see the most important offerings of ESL tools currently available as well as the most challenging points and problems why ESL hasn't really taken off so far. Could this either be an industry issue, a methodology issue, a design issue or a tool issue?

**Panelists**

Industrial representatives:

Soo Kwan Eo, *Samsung, Korea*

Akio Miyoshi, *IBM, Japan*

Pierre Paulin, *ST Microelectronics, Canada*

Masami Aihara, *Toshiba, Japan*

Krisztian Flautner, *ARM, UK*

CAD vendor representatives:

Joachim Kunkel, *Synopsys, USA*

Athanasios (A.K.) Kalekos, *CoWare, USA*

Mike Meredith, *Forte Design Systems, USA*

Shawn McCloud, *Mentor Graphics, USA*

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