# A Small-Step Operational Semantics for GP 2

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The operational semantics of a programming language is said to be small-step if each transition step is an atomic computation step in the language. A semantics with this property faithfully corresponds to the implementation of the language. The previous semantics of the graph programming language GP 2 is not fully small-step because the loop and branching commands are defined in big-step style. In this paper, we present a truly small-step operational semantics for GP 2 which, in particular, accurately models diverging computations. To obtain small-step definitions of all commands, we equip the transition relation with a stack of host graphs and associated operations. We prove that the new semantics is non-blocking in that every computation either diverges or eventually produces a result graph or the failure state. We also show the finite nondeterminism property, viz. that each configuration has only a finite number of direct successors. The previous semantics of GP 2 is neither non-blocking nor does it have the finite nondeterminism property. We also show that, for a program and a graph that terminate, both semantics are equivalent, and that the old semantics can be simulated with the new one.

#### 1 Introduction

GP 2 is a nondeterministic programming language based on graph transformation rules. The previous semantics of GP 2 is defined by both small-step and big-step inference rules [17]. An operational semantics is *small-step* if atomic computation steps in the language correspond to transition steps, meaning that the language can be implemented by translating the transition steps into corresponding code. In this paper, we present a truly small-step operational semantics for GP 2 which, in particular, accurately models diverging computations.

While the previous semantics (Figure 3) has small-step elements, the branching and loop constructs are not small-step. This can lead to the semantic transition sequence *blocking* or *getting stuck* [14], i.e. reaching a configuration which is neither a graph nor the failure state, such that no inference rule is applicable.

To illustrate this situation, consider the program P = try (r1!) then skip else skip, with the rule  $r1 : {}_{1}O \Rightarrow {}_{1}O$  O, applied to the host graph O. The statement r1! means that the rule r1 is called until it is no longer applicable. The try statement attempts to evaluate r1! but will neither branch to the then nor the else part because the loop r1! diverges on O. In the previous semantics, try statements are handled with the following inference rules:

$$[\operatorname{try}_1'] \ \frac{\langle C,G\rangle \leadsto^+ H}{\langle \operatorname{try} C \ \operatorname{then} P \ \operatorname{else} Q,G\rangle \leadsto \langle P,H\rangle} \quad [\operatorname{try}_2'] \ \frac{\langle C,G\rangle \leadsto^+ \ \operatorname{fail}}{\langle \operatorname{try} C \ \operatorname{then} P \ \operatorname{else} Q,G\rangle \leadsto \langle Q,G\rangle}$$

The premises of these inference rules are that the conditional part C of a try statement applied to host graph G results in either a graph H or failure, which determines whether P or Q is called. If  $\langle C, G \rangle$  diverges (does not terminate) however, neither rule applies. Since there are no other try rules, the transition sequence gets stuck.

The new semantics we introduce in this paper handles try statements with the following rules:

$$\begin{split} & [\text{try}_1] \ \langle \text{try} \ C \ \text{then} \ P \ \text{else} \ Q, S \rangle \rightarrow \langle \text{TRY}(C, P, Q), \ \text{push}(\text{top}(S), S) \rangle \\ & [\text{try}_2] \ \frac{\langle C, S \rangle \rightarrow \langle C', S' \rangle}{\langle \text{TRY}(C, P, Q), S \rangle \rightarrow \langle \text{TRY}(C', P, Q), S' \rangle} \quad \text{[try}_3] \ \frac{\langle C, S \rangle \rightarrow S'}{\langle \text{TRY}(C, P, Q), S \rangle \rightarrow \langle P, \text{pop}2(S') \rangle} \\ & [\text{try}_4] \ \frac{\langle C, S \rangle \rightarrow \text{fail}}{\langle \text{TRY}(C, P, Q), S \rangle \rightarrow \langle Q, \text{pop}(S) \rangle} \end{aligned}$$

Here S and S' are stacks of graphs. The rule  $[try_1]$  duplicates the top of the stack, and the TRY construct signals that the copy operation has happened. Repeated applications of the inference rule  $[try_2]$  model the evaluation of the condition in a small-step fashion. If the condition loops,  $[try_2]$  can be applied indefinitely, and we get an infinite transition sequence.

Intuitively, P should loop, which is what happens in the implementation of GP2. In the previous semantics however, P gets stuck because r1! diverges, which means that we cannot apply either of the inference rules  $[try'_1]$  or  $[try'_2]$  to resolve the try statement.

The previous semantics tries to remedy this issue in the *semantic function* which associates to a program P and host graph G the set [P]G of all possible outcomes of the execution of P on G. These outcomes can be a graph, the element fail, or  $\bot$  which represents an infinite transition sequence. The previous semantic function uses  $\bot$  as an outcome if the transition sequence gets stuck. However, there are problems with this approach.

Consider the program  $P = try Loop then skip else skip, where Loop = <math>\{r1,r2\}!$ , r1 is as previously defined, and  $r2 : \mathbb{O} \Rightarrow \emptyset$ . The command  $\{r1,r2\}$  is a *rule set call*, meaning that rules r1 and r2 are selected nondeterministically. When P is executed on the host graph  $\mathbb{O}$ , an application of r2 causes the loop to terminate since it removes the marked node which is necessary for either rule to be applicable. Hence r1 may be applied a number of times, and then r2 is applied once. But it should also be possible that r2 is never called, resulting in a diverging computation. Hence the set of outcomes we want is  $\{\bot,\emptyset,\mathsf{O},\mathsf{OO},\mathsf{OO},\ldots\}$ . According to the previous semantics, however, the execution of P on  $\mathbb{O}$  cannot get stuck since Loop *can* always transition to a graph; and by the rules  $[try'_1]$  and  $[try'_2]$ , the execution cannot diverge either. So  $\bot \notin [P]\mathbb{O} = \{\emptyset,\mathsf{O},\mathsf{OO},\mathsf{OO},\ldots\}$ . Now, the new semantics indeed corresponds exactly to our intuition of the operational behaviour of GP2 programs. Moreover, we conjecture that the implementation is sound with respect to the new semantics, in that the behaviour of the implementation is covered by the new semantics.

This may also lead to two programs being semantically equivalent, even though they should not be. Programs P and P' are semantically equivalent if [P] = [P'], i.e. they have the same outcomes for all host graphs. Consider the program  $P = \text{try }(\{r3,r2\}!)$  then skip else skip, where  $r3: _1O \Rightarrow _1O$ . It can diverge but is semantically equivalent to Q = try r2 then skip else skip since the previous semantics cannot detect that divergence. For instance,  $[P]O = [Q]O = \{\emptyset\}$ , but [P]O should include  $\bot$ .

The aforementioned issues can also happen with if statements, which work similarly to try statements, except that the changes the condition made to the host graph are reversed, even if the evaluation of the condition succeeds. Nested loops such as Loop! can get stuck as well since their inference rules also assume that the loop body either results in a graph or fails.

Diverging computations not being modelled properly entails an undesirable property, namely *infinite nondeterminism*, i.e. there can be infinitely many configurations reachable in a single transition step. Consider the program P = try Loop then skip else skip, where Loop =  $\{r1,r2\}$ !, and the rules are as previously defined. We have  $[\text{Loop}] \bigcirc = \{\emptyset, O, OO, OOO, \dots, \bot\}$ . In a transition sequence starting with  $\langle P, \bigcirc \rangle$ , since the try statement is resolved within a single step, it only takes one step to transition to either of the graphs in the set  $\{\emptyset, O, OO, OOO, \dots\}$ , of which there are infinitely many.

The semantics we introduce in this paper is truly small-step and as such, it accurately models looping computations with diverging transition sequences. When starting with a valid GP2 program, it cannot get stuck, which is a property we call *non-blocking*. As a consequence of the small-step approach, we get *finite nondeterminism*, meaning we can only reach a finite number of configurations within a single transition step.

In Section 2, we give a brief overview of the rule-based graph programming language GP 2 along with the previous semantics. We propose the new semantics in Section 3 and give examples of transition sequences. In Section 4 we prove several properties of the new semantics, including non-blocking as well as finite nondeterminism, and define the semantic function along with semantic equivalence. In Section 5, we compare the new and previous semantics by showing the new semantic function is an extension of the previous one, and that they are equivalent excluding divergence.

### 2 The Graph Programming Language GP 2

This section provides a brief introduction to GP 2 [16], a nondeterministic graph programming language based on transformation rules. We show the abstract syntax of GP 2 programs below, and refer to [3] for the full syntax. The language is implemented by a compiler generating C code [4].

GP 2 programs transform input graphs into output graphs, where graphs are labelled and directed and may contain parallel edges and loops.

The principal programming construct in GP2 are conditional graph transformation rules labelled with expressions. For example, Figure 1 shows a program recognising graphs that contain cycles and the declaration of its rules. The rule delete which has three formal parameters, a left-hand graph and a right-hand graph which are specified graphically, and a textual condition starting with the keyword where. The small numbers attached to nodes are identifiers, all other text in the graphs are labels.

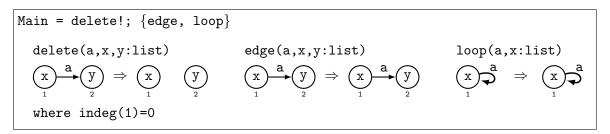


Figure 1: GP 2 program recognising cyclic graphs

GP 2 labels consist of an expression and an optional mark (explained below). Expressions are of type int, char, string, atom or list, where atom is the union of int and string, and list is the type of a (possibly empty) list of atoms. Lists of length one are equated with their entries and hence every expression can be considered as a list.

The concatenation of two lists x and y is written x:y, the empty list is denoted by empty. Character strings are enclosed in double quotes. Composite arithmetic expressions such as n \* n must not occur in the left-hand graph, and all variables occurring in the right-hand graph or the condition must also occur in the left-hand graph.

Besides carrying list expressions, nodes and edges can be *marked*. For example, one of the nodes in rule r1 in the introduction is marked by a grey shading. Marks are convenient to highlight items in

```
Program
                       Declaration { Declaration }
Declaration
                       MainDecl | ProcedureDecl | RuleDecl
                  ::=
MainDecl
                       Main '=' CommandSeq
                       ProcedureID '=' [ '[' LocalDecl ']' ] CommandSeq
ProcedureDecl
                  ::=
LocalDecl
                       (RuleDecl | ProcedureDecl ) { LocalDecl }
                       Command {';'Command}
CommandSeq
Command
                       Block
                  ::=
                       if Block then Block [else Block]
                       try Block [then Block] [else Block]
Block
                       '(' CommandSeq ')' ['!']
                  ::=
                        SimpleCommand
                         Block or Block
SimpleCommand
                 ::=
                       RuleSetCall ['!']
                         ProcedureCall ['!']
                         break
                         skip
                         fail
RuleSetCall
                       RuleID | '{' [ RuleID { ',' RuleID } ] '}'
                       ProcedureID
ProcedureCall
                  ::=
```

Figure 2: GP 2 Program Syntax

input or output graphs, and to record visited items during a graph traversal. For instance, a graph can be checked for connectedness by propagating marks along edges as long as possible and subsequently testing whether any unmarked nodes remain. Note that conventional graph algorithms are often described by using marks as a visual aid [7].

Additionally, nodes in rules and host graphs can be *rooted*. If such a node appears in the left-hand side of a rule, it can only be matched with a root node in the host graph. Their use restricts matching to the neighbourhoods of root nodes, which can greatly increase efficiency [6].

We do not elaborate any further on features such as marks or roots because the GP2 semantics does not depend on them.

Rules operate on *host graphs* which are labelled with constant values (lists containing integer and string constants). Applying a rule  $L \Rightarrow R$  to a host graph G works roughly as follows: (1) Replace the variables in L and R with constant values and evaluate the expressions in L and R, to obtain an instantiated rule  $\hat{L} \Rightarrow \hat{R}$ . (2) Choose a subgraph S of G isomorphic to  $\hat{L}$  such that the dangling condition and the rule's application condition are satisfied (see below). (3) Replace S with  $\hat{R}$  as follows: numbered nodes stay in place (possibly relabelled), edges and unnumbered nodes of  $\hat{L}$  are deleted, and edges and unnumbered nodes of  $\hat{R}$  are inserted.

In this construction, the *dangling condition* requires that nodes in S corresponding to unnumbered nodes in  $\hat{L}$  (which should be deleted) must not be incident with edges outside S. The rule's application condition is evaluated after variables have been replaced with the corresponding values of  $\hat{L}$ , and node identifiers of L with the corresponding identifiers of L. For example, the term indeg(1)=0 in the condition of delete in Figure 1 forbids the node L to have incoming edges, where L is the node in L corresponding to 1.

(a) Inference rules for core commands

$$\begin{array}{lll} [\operatorname{or}'_1] & \langle P\operatorname{or} Q,G\rangle \leadsto \langle P,G\rangle & [\operatorname{or}'_2] & \langle P\operatorname{or} Q,G\rangle \leadsto \langle Q,G\rangle \\ [\operatorname{skip}'] & \langle \operatorname{skip},G\rangle \leadsto G & [\operatorname{fail}'] & \langle \operatorname{fail},G\rangle \leadsto \operatorname{fail} \\ [\operatorname{if}'_3] & \langle \operatorname{if} C \operatorname{then} P,G\rangle \leadsto \langle \operatorname{if} C \operatorname{then} P \operatorname{else} \operatorname{skip},G\rangle \\ [\operatorname{try}'_3] & \langle \operatorname{try} C \operatorname{then} P,G\rangle \leadsto \langle \operatorname{try} C \operatorname{then} P \operatorname{else} \operatorname{skip},G\rangle \\ [\operatorname{try}'_4] & \langle \operatorname{try} C \operatorname{else} P,G\rangle \leadsto \langle \operatorname{try} C \operatorname{then} \operatorname{skip} \operatorname{else} P,G\rangle \\ [\operatorname{try}'_5] & \langle \operatorname{try} C,G\rangle \leadsto \langle \operatorname{try} C \operatorname{then} \operatorname{skip} \operatorname{else} \operatorname{skip},G\rangle \\ \end{array}$$

(b) Inference rules for derived commands

Figure 3: Previous GP 2 Semantics

Formally, GP2 is based on a form of attributed graph transformation according to the so-called double-pushout approach [11, 9]. The grammar in Figure 2 gives the abstract syntax of GP2 programs. A program consists of declarations of conditional rules and procedures, and exactly one declaration of a main command sequence. The category RuleID refers to declarations of conditional rules in RuleDecl (whose syntax is omitted). Procedures must be non-recursive, they can be seen as macros with local declarations.

The call of a rule set  $\{r_1, \ldots, r_n\}$  nondeterministically applies one of the rules whose left-hand graph matches a subgraph of the host graph such that the dangling condition and the rule's application condition are satisfied. The call *fails* if none of the rules is applicable to the host graph.

The command if C then P else Q is executed on a host graph G by first executing C on G. If this results in a graph, P is executed on the original graph G; otherwise, if C fails, Q is executed on G. The try command has a similar effect, except that P is executed on the result of C's execution in case C succeeds.

The loop command P! executes the body P repeatedly until it fails. When this is the case, P! terminates with the graph on which the body was entered for the last time. The break command inside a loop terminates that loop with the current graph and transfers control to the command following the loop.

A program P or Q non-deterministically chooses to execute either P or Q, which can be simulated by a rule-set call and the other commands [16]. The commands skip and fail can also be expressed by the other commands: skip is equivalent to an application of the rule  $\emptyset \Rightarrow \emptyset$  (where  $\emptyset$  is the empty graph) and fail is equivalent to an application of  $\{\}$  (the empty rule set).

Like Plotkin's structural operational semantics [15], the previous GP 2 semantics is given by inference rules. The rules in Figure 3 define the transition relation → over the following set:

$$(ComSeq \times \mathscr{G}) \times ((ComSeq \times \mathscr{G}) \cup \mathscr{G} \cup \{fail\}).$$

Here  $\mathscr{G}$  is the set of all GP 2 host graphs and ComSeq is the set of command sequences as defined in the syntax (Figure 2), where we assume that procedure IDs have been eliminated by macro expansion. This means that procedure IDs have been replaced with their defining command sequence, and name clashes arising from local declarations have been resolved by renaming.

The element fail represents the program resulting in a failure state. The inference rules contain universally quantified variables, namely host graphs G and H, command sequences in ComSeq C, P, P', and Q, and rule set call R. The transitive closure of  $\rightsquigarrow$  is denoted by  $\rightsquigarrow$ <sup>+</sup>, and the reflexive transitive closure by  $\rightsquigarrow$ <sup>\*</sup>.

In general, the execution of a program on a host graph may result in another graph, fail, or diverge. Also, executions can get *stuck* in that they reach a non-terminal configuration (neither a graph nor fail) to which no inference rule is applicable. Let  $\mathscr G$  be the set of all host graphs and  $\mathscr G^\oplus = \mathscr G \cup \{\bot, \mathrm{fail}\}$ . These outcomes are described by the semantic function  $[\_]: \mathrm{ComSeq} \to (\mathscr G \to 2^{\mathscr G^\oplus})$  which, for a command sequence P and a host graph G, is defined as

$$[P]G = \{X \in \mathcal{G} \cup \{\text{fail}\} \mid \langle P, G \rangle \leadsto^+ X\} \cup \{\bot \mid P \text{ can diverge or get stuck from } G\}.$$

By divergence we mean non-termination, that is the existence of an infinite transition sequence starting in  $\langle P, G \rangle$ .

## 3 The Small-Step Semantics

In this section, we introduce an improved semantics defined by inference rules, and give examples of transition sequences.

Due to additional constructs, the new semantics needs to distinguish between command sequences that are valid GP 2 programs, and command sequences that are intermediary. The former are members of CommandSeq from the syntax in Figure 2, and are called *command sequences*. They have to satisfy the context conditions specified in Appendix A.6 of Bak's thesis [3]. The following condition is particularly relevant to this paper: "A break must be enclosed within a loop. If a break is in the condition of a branching statement, the enclosing loop must be within the same condition." This constraint is not specific to graph programs: Java, C, and Python have similar restrictions on the use of break statements.

We define extended command sequences (set ExtComSeq) to be command sequences with additional auxiliary constructs ITE and TRY. They do not follow context conditions since we may want a break outside of a loop in an intermediary transition step. The ITE and TRY statements serve to advance the command sequence in the condition in a small-step fashion, as well as to maintain the stack of host graphs. When we enter an ITE or TRY statement, the top of the stack (and current host graph) is duplicated in order to keep a backup. When exiting these statements we either pop the top, modified graph, or the second graph on the stack which is the unmodified backup copy depending on the outcome of the condition. The stack structure is needed because if and try statements may be nested. Whenever we enter an ITE or TRY construct, we push a graph, and whenever we exit one, we pop a graph. This ensures that the stack always contains enough graphs to pop and that the current host graph is always on top.

The rules in Figure 4 inductively define a transition relation  $\rightarrow$  over the following set:

$$(ExtComSeq \times \mathscr{S}) \times ((ExtComSeq \times \mathscr{S}) \cup \mathscr{S} \cup \{fail\}),$$

where  $\mathscr{S}$  is the set of all stacks of GP 2 host graphs (explained below). We call an element of the set  $(ExtComSeq \times \mathscr{S}) \cup \mathscr{S} \cup \{fail\}$  an *extended configuration*, whereas  $(CommandSeq \times \mathscr{S}) \cup \mathscr{S} \cup \{fail\}$  is the set of *configurations*. A configuration (or extended configuration) C is *terminal* if C = fail or C = S for some graph stack S.

The set  $\mathscr S$  is the set of all non-empty stacks of GP2 host graphs where the top element is the current host graph, and where the other elements are backup copies to revert to or discard after the resolution of conditions of branching statements. Such a stack  $S = [G_1, G_2, G_3, \ldots, G_n]$  is a finite ordered list of GP2 host graphs with unary operations  $top(S) = G_1, pop(S) = [G_2, G_3, \ldots, G_n]$  and  $pop(S) = [G_1, G_3, \ldots, G_n]$ , as well as the binary operation  $push(G, S) = [G, G_1, G_2, \ldots, G_n]$ , where G is a GP2 host graph.

Most of the inference rules in Figure 4 have a horizontal bar. These rules consist of a *premise* above the bar and a *conclusion* below. The conclusion defines a transition step provided that the premise holds. A rule without a bar is called an *axiom* and can be applied to a configuration without any precondition.

There are several universally quantified meta-variables within the inference rules. P, P', Q, Q', C, and C' stand for extended command sequences in ExtComSeq, S stands for a graph stack in  $\mathcal{S}$ , G represents a host graph, and R represents a rule set. We denote the transitive closure of  $\rightarrow$  by  $\rightarrow^+$ , and the reflexive transitive closure by  $\rightarrow^*$ .

The inference rules inductively define the transition relation  $\rightarrow$ . The rules [call<sub>1</sub>] and [call<sub>2</sub>] are base cases. Their premises are GP2 derivations. Which of the two premises is satisfied depends on whether

<sup>&</sup>lt;sup>1</sup>By branching statement, we mean an if, try, ITE, or TRY statement.

$$[\operatorname{call}_{1}] \frac{\operatorname{top}(S) \Rightarrow_{R} G}{\langle R, S \rangle \to \operatorname{push}(G, \operatorname{pop}(S))}$$
 
$$[\operatorname{call}_{2}] \frac{\operatorname{top}(S) \not \Rightarrow_{R}}{\langle R, S \rangle \to \operatorname{fail}}$$
 
$$[\operatorname{seq}_{1}] \frac{\langle P, S \rangle \to \langle P', S' \rangle}{\langle P; Q, S \rangle \to \langle P'; Q, S' \rangle}$$
 
$$[\operatorname{seq}_{2}] \frac{\langle P, S \rangle \to S'}{\langle P; Q, S \rangle \to \langle Q, S' \rangle}$$
 
$$[\operatorname{seq}_{3}] \frac{\langle P, S \rangle \to \operatorname{fail}}{\langle P; Q, S \rangle \to \operatorname{fail}}$$
 
$$[\operatorname{break}] \langle \operatorname{break}; P, S \rangle \to \langle \operatorname{break}, S \rangle$$
 
$$[\operatorname{alap}_{1}] \langle P!, S \rangle \to \langle \operatorname{try} P \operatorname{then} P! \operatorname{else} \operatorname{skip}, S \rangle \quad [\operatorname{alap}_{2}] \langle \operatorname{TRY}(\operatorname{break}, P!, \operatorname{skip}), S \rangle \to \operatorname{pop2}(S)$$
 
$$[\operatorname{if}_{1}] \langle \operatorname{if} C \operatorname{then} P \operatorname{else} Q, S \rangle \to \langle \operatorname{ITE}(C, P, Q), \operatorname{push}(\operatorname{top}(S), S) \rangle$$
 
$$[\operatorname{try}_{1}] \langle \operatorname{try} C \operatorname{then} P \operatorname{else} Q, S \rangle \to \langle \operatorname{TRY}(C, P, Q), \operatorname{push}(\operatorname{top}(S), S) \rangle$$
 
$$[\operatorname{if}_{2}] \frac{\langle C, S \rangle \to \langle C', S' \rangle}{\langle \operatorname{ITE}(C, P, Q), S \rangle \to \langle \operatorname{ITE}(C', P, Q), S' \rangle}$$
 
$$[\operatorname{try}_{2}] \frac{\langle C, S \rangle \to \langle C', S' \rangle}{\langle \operatorname{TRY}(C, P, Q), S \rangle \to \langle \operatorname{TRY}(C', P, Q), S' \rangle}$$
 
$$[\operatorname{try}_{3}] \frac{\langle C, S \rangle \to S'}{\langle \operatorname{TRY}(C, P, Q), S \rangle \to \langle P, \operatorname{pop2}(S') \rangle}$$
 
$$[\operatorname{try}_{4}] \frac{\langle C, S \rangle \to \operatorname{fail}}{\langle \operatorname{TRY}(C, P, Q), S \rangle \to \langle Q, \operatorname{pop}(S) \rangle}$$
 
$$[\operatorname{try}_{4}] \frac{\langle C, S \rangle \to \operatorname{fail}}{\langle \operatorname{TRY}(C, P, Q), S \rangle \to \langle Q, \operatorname{pop}(S) \rangle}$$

(a) Inference rules for core commands

$$\begin{array}{lll} [\operatorname{or}_1] & \langle P\operatorname{or} Q,S\rangle \to \langle P,S\rangle & [\operatorname{or}_2] & \langle P\operatorname{or} Q,S\rangle \to \langle Q,S\rangle \\ [\operatorname{skip}] & \langle \operatorname{skip},S\rangle \to S & [\operatorname{fail}] & \langle \operatorname{fail},S\rangle \to \operatorname{fail} \\ [\operatorname{if}_5] & \langle \operatorname{if} C \operatorname{then} P,S\rangle \to \langle \operatorname{if} C \operatorname{then} P \operatorname{else} \operatorname{skip},S\rangle \\ [\operatorname{try}_5] & \langle \operatorname{try} C \operatorname{then} P,S\rangle \to \langle \operatorname{try} C \operatorname{then} P \operatorname{else} \operatorname{skip},S\rangle \\ [\operatorname{try}_6] & \langle \operatorname{try} C \operatorname{else} P,S\rangle \to \langle \operatorname{try} C \operatorname{then} \operatorname{skip} \operatorname{else} P,S\rangle \\ [\operatorname{try}_7] & \langle \operatorname{try} C,S\rangle \to \langle \operatorname{try} C \operatorname{then} \operatorname{skip} \operatorname{else} \operatorname{skip},S\rangle \\ \end{array}$$

Figure 4: Improved GP 2 Semantics

(b) Inference rules for derived commands

 $top(S) \Rightarrow_R G$  or  $top(S) \not\Rightarrow_R$ , i.e. whether a rule in the rule set can be applied to the current host graph or not. The if and try statements are modelled by the  $[if_i]$  and  $[try_i]$  rules.

Sequential composition of commands is covered by [seq<sub>1</sub>], [seq<sub>2</sub>], and [seq<sub>3</sub>], covering the cases of whether the first command called on a host graph results in a configuration, a graph stack, or fail.

Loops are semantically described as a try statement in [alap<sub>1</sub>]. Calling a command sequence as long as possible is modelled by trying to apply the command sequence, and if it succeeds, keep applying it as long as possible. Breaking from a loop is handled by [break], which makes sure commands following the break are discarded, and [alap<sub>2</sub>], which terminates the loop if there is an isolated break in the TRY condition.

Figure 4a shows the inference rules for the core commands of GP 2, while Figure 4b gives the inference rules for derived commands such as or, skip, and fail, as well as some if and try statements with omitted then and else clauses. These commands are referred to as *derived* commands because they can be defined by the core commands (see [16] for the case of the previous semantics).

Let us look at a couple of examples of transition sequences in Figure 5, the first to illustrate loops, and the second to illustrate if and try statements. For each transition, we note the applied inference rule as a subscript. If the conclusion of [rule<sub>1</sub>] is used as a premise for [rule<sub>2</sub>], we denote it by  $\frac{[rule_1]}{[rule_2]}$ .

**Example 3.1.** Consider the program P=r! and the rule  $r: {}_{1}O \rightarrow O \Rightarrow {}_{1}O$ . Let us examine a transition sequence of P applied to the graph  $O \rightarrow O \rightarrow O$ , as seen in Figure 5a.

We start by applying [alap<sub>1</sub>] which turns the loop into a try statement. Unlike in the previous semantics, we model a loop by trying to apply its body, and if it is successful, we call the loop again.

The inference rule  $[try_1]$  transforms the try statement into the auxiliary TRY construct, which advances the program in a small-step fashion, unlike the previous semantics. There is a similar ITE construct which models if statements. The top of the graph stack is duplicated since the changes made by the condition of the try may be discarded.

We then apply r to the current host graph (top of the stack) so  $[call_1]$  can be applied. This serves as a premise for  $[try_3]$ , which ends the TRY statement, pops the second element of the stack, and moves on to the then part which is the original loop.

We repeat this process until r is no longer applicable to the host graph. At this point, [call<sub>2</sub>] serves as the premise for [try<sub>4</sub>] which exits the TRY statement. This time, the condition results in fail, so we move on to the else part which is skip and the loop terminates.

Now consider program P'=try(if (r1;r1) then (r1;r1)) and the rule r1:  $_1O\rightarrow O\Rightarrow_1O$ . A transition sequence of P' applied to host graph  $O\rightarrow O$  can be found in Figure 5b.

Since the try statement does not have a then or else part, we first apply [try7], which adds skip as both the then and else parts.

The inference rule  $[try_1]$  turns the try statement into the auxiliary TRY statement and duplicates the top of the stack. For most of the remaining transition sequence, we apply  $[try_2]$  under various premises to advance the condition.

Since the if has no else part,  $[if_5]$  completes it with a skip. The if statement is then turned into the auxiliary ITE statement, duplicating the top of the stack once again.

The rule r1 is applied to the host graph which advances the concatenation with [seq<sub>2</sub>], the ITE with [if<sub>2</sub>], and the TRY with [try<sub>2</sub>]. Calling r1 a second time resolves the ITE, and the top of the stack is popped since changes made by the conditions of if statements are reversed.

We keep applying the condition of the TRY, until we resolve it with [try<sub>3</sub>]. This time the second graph on the stack is popped since changes made by the condition of a try that did not result in fail are

```
⟨r!, [O→O→O]⟩
  \rightarrow_{[alap_1]} \quad \langle \texttt{try r then r! else skip}, \lceil \texttt{O} \textbf{+} \texttt{O} \textbf{+} \texttt{O} \rceil \rangle
  \rightarrow {}_{[try_1]} \quad \langle TRY (r, r!, skip), [O \rightarrow O \rightarrow O, O \rightarrow O \rightarrow O] \rangle
 \to \underline{[\operatorname{call_1}]} \quad \langle \mathtt{r!}, [\mathsf{O} \textcolor{red}{\blacktriangleright} \mathsf{O}] \rangle
         [try<sub>3</sub>]
                       \langle \text{try r then r! else skip}, [O\rightarrow O] \rangle
  \rightarrow [alap<sub>1</sub>]
                       \langle TRY (r, r!, skip), [O\rightarrow O, O\rightarrow O] \rangle
  \rightarrow [try<sub>1</sub>]
                       \langle r!, [O] \rangle
  \rightarrow \frac{[call_1]}{[try_3]}
                       \langle \text{try r then r! else skip}, [O] \rangle
  \rightarrow [alap<sub>1</sub>]
                       \langle TRY(r, r!, skip), [O, O] \rangle
  \rightarrow [try<sub>1</sub>]
                        \langle \text{skip}, [O] \rangle
  \to \tfrac{[call_2]}{[try_4]}
                        [O]
  \rightarrow [skip]
                                             (a) Transition sequence of program P applied to graph O→O→O.
\langle try(if(r1;r1) then(r1;r1)), [O \leftarrow O \rightarrow O] \rangle
                   \langle \text{try (if (r1;r1) then (r1;r1)) then skip else skip, } [O \leftarrow O \rightarrow O] \rangle
\rightarrow_{[try_7]}
                   \langle TRY(if (r1;r1) then (r1;r1), skip, skip), [O \leftarrow O \rightarrow O, O \leftarrow O \rightarrow O] \rangle
\rightarrow_{[try_1]}
                   \langle TRY(if (r1;r1) then (r1;r1) else skip, skip, skip), [O \leftarrow O \rightarrow O, O \leftarrow O \rightarrow O] \rangle
\to_{\underbrace{[if_5]}_{[try_2]}}
\to_{\underbrace{[if_1]}{[try_2]}}
                    \langle TRY(ITE(\texttt{r1};\texttt{r1},\texttt{r1};\texttt{r1},\texttt{skip}),\texttt{skip},\texttt{skip}), [\texttt{O} \bullet \bullet \texttt{O} \bullet \texttt{O}, \texttt{O} \bullet \bullet \texttt{O}, \texttt{O} \bullet \bullet \texttt{O}] \rangle
\begin{array}{c} \rightarrow_{[call_1]} \\ \underline{[seq_2]} \\ \underline{[if_2]} \\ \underline{[try_2]} \end{array}
                    \langle TRY(ITE(r1, r1; r1, skip), skip, skip), [O\rightarrowO, O\rightarrowO, O\rightarrowO] \rangle
\xrightarrow{\underbrace{[call_1]}_{\underbrace{[if_3]}_{}}}
                    \langle TRY(r1;r1, skip, skip), [O \leftarrow O \rightarrow O, O \leftarrow O \rightarrow O] \rangle
                   \langle TRY(r1, skip, skip), [O\rightarrow O, O\rightarrow O] \rangle
 \rightarrow_{[\operatorname{call}_1]}
                   \langle \text{skip}, [O] \rangle
 \rightarrow_{[\operatorname{call}_1]}
                   [0]
\rightarrow_{[skip]}
                                            (b) Transition sequence of program P' applied to graph O←O→O.
```

Figure 5: Examples of transition sequences

preserved.

### 4 Properties of the Semantics

In this section, we show that the semantics is non-blocking, i.e. if a transition sequence ends in an extended configuration, we can always apply an inference rule (Proposition 4.3). Note that we can only guarantee the non-blocking property for extended configurations that are part of a transition sequence originating in a valid GP 2 program. We call those *reachable* extended configurations. This is reasonable because there can be no other types of configurations in a transition sequence modelling a GP 2 program.

Furthermore, we will describe the outcomes of a transition sequence starting with a valid GP 2 program (Proposition 4.5), and show that we have finite nondeterminism (Proposition 4.7), i.e. there are only finitely many one-step transitions starting from any configuration, and what it means for the semantic function.

Let us first look at a lemma that guarantees we can make a transition step from extended configurations that do not contain a break, which is the first step towards showing the non-blocking property.

**Lemma 4.1** (Progress from Extended Configurations). Let  $\langle P, S \rangle$  be an extended configuration. Then one of the following applies:

- $\langle P, S \rangle \rightarrow \langle P', S' \rangle$  for some extended configuration  $\langle P', S' \rangle$ .
- $\langle P, S \rangle \to S'$  for some graph stack  $S' \in \mathcal{S}$ .
- $\langle P, S \rangle \rightarrow \text{fail}$ .
- P is not a command sequence and contains a break.

*Proof.* We shall prove this lemma by going through what *P* could be according to the syntax and the semantics.

Case 1: P is a rule set call. Then either  $top(S) \Rightarrow_P G$  or  $top(S) \not\Rightarrow_P$ . So either [call<sub>1</sub>] or [call<sub>2</sub>] can be applied.

Case 2: P is a loop. If P is a loop,  $[alap_1]$  can be applied.

Case 3: P is fail, skip or an or statement. Then [fail], [skip], or [or<sub>1</sub>] can be applied respectively.

Case 4: P is of the form if  $P_1$  then  $P_2$  else  $P_3$  or try  $P_1$  then  $P_2$  else  $P_3$ . Then  $[if_1]$  or  $[try_1]$  can be applied. If any then-clause or else-clause is omitted as specified by the syntax,  $[if_5]$ ,  $[try_5]$ ,  $[try_6]$ , or  $[try_7]$  can be applied.

Case 5: P is of the form  $ITE(P_1, P_2, P_3)$  or  $TRY(P_1, P_2, P_3)$ . If P contains a break, the fourth point of the lemma is satisfied, as containing ITE or TRY statements makes P not a command sequence. So for the remainder of this case, assume P does not contain a break. If  $P_1$  is a sequential composition, let  $P_1 = P_1'$ ;  $P_1''$  where  $P_1'$  is not a sequential composition. Otherwise let  $P_1 = P_1'$ . We shall show the lemma's statement by induction on how many ITE or TRY statements are nested in  $P_1'$  via the first sequential component of the condition.

- For the base case, assume  $P'_1$  is not an ITE or TRY statement. Then  $P'_1$  is not a sequential composition and covered by cases 1 to 4 ( $P'_1$  cannot be break since P contains no break).
- Now for the induction step, assume that  $P'_1$  is an ITE or TRY statement. Then  $P'_1$  does derive either a configuration  $\langle P'''_1, S' \rangle$ , a graph stack S' or fail by the induction hypothesis. Hence one of [if<sub>2</sub>], [if<sub>3</sub>], [if<sub>4</sub>], [try<sub>2</sub>], [try<sub>3</sub>], or [try<sub>4</sub>] can be applied to  $\langle P, S \rangle$ .

Case 6: P is a sequential composition. Then we can decompose P into  $P = P_1$ ;  $P_2$  where  $P_1$  is not a sequential composition. We can apply [seq<sub>1</sub>], [seq<sub>2</sub>], or [seq<sub>3</sub>] since  $\langle P_1, S \rangle \to \langle P'_1, S' \rangle$ ,  $\langle P_1, S \rangle \to S'$ , or  $\langle P_1, S \rangle \to$  fail respectively by cases 1 to 5.

Case 7: P contains a break.

If P is not a command sequence, the final point of the lemma is satisfied. Otherwise, P satisfies context conditions, meaning it must be enclosed within a loop, so either case 2 or one of the other previous cases is applicable.

Lemma 4.1 has a case where the extended command sequence contains a break. This is because for a transition sequence not to get stuck on a break, we need to start with a command sequence where the break is within a loop, which we cannot guarantee if we consider a single transition step like in Lemma 4.1. In order to deal with this case, we prove that we can construct a transition sequence that leads to a state with no break in the following lemma. However, we need to restrict it to extended configurations reachable from a valid GP2 program. We say that an extended configuration C is reachable if there is a configuration  $\langle P, [G] \rangle$  such that  $\langle P, [G] \rangle \to^* C$ . This will still allow us to work towards non-blocking, since we only care about transition sequences that start with valid GP2 programs.

**Lemma 4.2** (Removing the break Statement). Let  $\langle P, S \rangle$  be an extended configuration that is reachable and non-terminal. Suppose that *P* contains break. Then one of the following applies.

- There is an extended configuration  $\langle P', S' \rangle$  containing no break statement such that  $\langle P, S \rangle \rightarrow^* \langle P', S' \rangle$ .
- There is a graph stack S' such that  $\langle P, S \rangle \rightarrow^+ S'$ .
- $\langle P, S \rangle \rightarrow^+$  fail

*Proof.* First assume that  $\langle P, S \rangle$  satisfies context conditions, i.e. the break is contained within a loop, and if the break is in the condition of an if or try statement, the enclosing loop must be in the same condition.

We will apply various inference rules to construct a transition sequence starting in  $\langle P, S \rangle$ . Remember that whenever we apply such an inference rule, it results in either a non-terminal extended configuration, a graph stack, or fail. If it results in a graph stack or fail, the second or third point of the lemma is satisfied. So at each step of the transition sequence we construct, we only need to consider the case where an inference rule results in a non-terminal extended command sequence.

If there are multiple loops with break statements, they are either in different sequential composition components, or nested. So let us show this lemma by induction on nesting and sequential composition.

As a base case, assume P contains a single loop with a break, and want to show we can apply a sequence of inference rules that ultimately removes the break. So P is of the form  $Q_0$ ;  $Q_1$ !;  $Q_2$ , where  $Q_1$  contains a break, and neither  $Q_0$  nor  $Q_2$  do. (What follows also applies if P is of the form  $Q_1$ !;  $Q_2$ ,  $Q_0$ ;  $Q_1$ !, or  $Q_1$ !.) We can repeatedly apply Lemma 4.1 to transition to  $Q_1$ !;  $Q_2$ . Then we apply [alap<sub>1</sub>] followed by [try<sub>1</sub>] to get TRY( $Q_1$ ,  $Q_1$ !, skip). We can then use Lemma 4.1 repeatedly as a premise for [try<sub>2</sub>] until we get TRY( $Q_3$ ;  $Q_4$ ,  $Q_1$ !, skip), where  $Q_3$  contains break and is not a sequential composition. If  $Q_3$  is a break, we can apply [try<sub>2</sub>] under the premise of [break], followed by [alap<sub>2</sub>] to get rid of the break. We know  $Q_3$  cannot be a loop since we assumed  $Q_1$ ! is the enclosing loop of the break. So  $Q_3$  is either an or, if, or try statement. If it is an or statement, we can apply [or<sub>1</sub>] or [or<sub>2</sub>] to either remove the break or lead to TRY(break;  $Q_5$ ,  $Q_1$ !, skip). Similarly, if  $Q_3$  is an if or

try statement, the break must be in the then or else part due to context conditions, and we can use inference rules to either remove the break or lead to  $TRY(break; Q_5, Q_1!, skip)$ . We can now apply  $[try_2]$  under the premise of [break] to get  $TRY(break, Q_1!, skip)$ . To this, we can apply  $[alap_2]$ , which gets rid of the break.

For the induction step, let us first consider the case of nesting. Assume that P is of the form  $Q_0$ ;  $(Q_1; Q_2; Q_3)!$ ;  $Q_4$ , where  $Q_2$  satisfies the lemma statement, and either  $Q_1$  or  $Q_2$  contain a single break. We can use the same arguments as in the base case in addition to  $[seq_1]$  under the premise of the induction hypothesis to get rid of the break.

Now consider sequential composition. As an induction step, assume that P is of the form  $Q_0$ ;  $Q_1$ !;  $Q_2$ ;  $Q_3$ !;  $Q_4$ , where one of  $Q_1$  or  $Q_3$  satisfies the lemma statement, and the other contains a single break. Again, we can use the arguments from the base case as well as the induction hypothesis in conjunction with  $[seq_1]$  to remove the break.

Finally, assume that  $\langle P,S\rangle$  does not satisfy context conditions, i.e. either there is a break without an enclosing loop, or there is a break in the condition of a branching statement whose enclosing loop is not within that condition. Since  $\langle P,S\rangle$  is reachable, the latter cannot be the case: transitions steps cannot separate a break from its enclosing loop in a way that they stop being within the same condition of a branching statement (loops can only be removed by inference rules, they cannot be "moved"). So suppose there is a break without an enclosing loop. This must be because [alap<sub>1</sub>] is applied earlier in the transition sequence, so it must be within the condition of a try or TRY. So we can use the same arguments as earlier in the proof, except that we need not argue that some of the inference rule, such as [alap<sub>1</sub>] or [try<sub>1</sub>] need to be applied.

Now that we have Lemmata 4.1 and 4.2, we can prove that the non-blocking property holds.

**Proposition 4.3** (Non-Blocking Property). Let  $\langle P, S \rangle$  be an extended configuration that is reachable and non-terminal. Then there is a transition step  $\langle P, S \rangle \to C$  for some extended configuration C.

*Proof.* If P does not contain a break, this proposition follows from Lemma 4.1. Otherwise, it follows from Lemma 4.2.

Let us now introduce a lemma that makes various statements about the size of host graph stacks in order to ensure that the inference rules are well-defined. Since we defined stacks to be nonempty, we want to make sure that if a transition sequence starts with a nonempty stack, it cannot lead to an empty stack, which the following lemma shows. Furthermore, when a transition sequence terminates in a graph stack, we want that stack to only contain one host graph.

For this lemma, we want to start from a valid GP2 program, not extended command sequences in general (since they may contain auxiliary constructs like ITE and TRY). So we consider configurations in CommandSeq  $\times \mathscr{S}$ . These follow the context conditions on where the break statement can appear as specified in [3].

**Lemma 4.4** (Stack Size). Let  $\langle P, [G] \rangle$  be a configuration in CommandSeq  $\times \mathscr{S}$ .

- (a) If  $\langle P, [G] \rangle \to^* \langle P', S \rangle$ , where  $\langle P', S \rangle$  is an extended configuration, then  $|S| \ge 1$ .
- (b) If  $\langle P, [G] \rangle \to^+ S$ , where S is a graph stack, then |S| = 1.

*Proof.* The statement in (a), is satisfied for zero transition steps. So let us examine the inference rules that contain push, pop, and pop2. The rule  $[call_1]$  contains both push and pop, but preserves the size of the stack. The rules that push a graph onto the stack are  $[if_1]$  and  $[try_1]$  which are exactly the rules that

introduce an ITE or a TRY. The rules that pop a graph from the stack are [alap<sub>2</sub>], [if<sub>3</sub>], [if<sub>4</sub>], [try<sub>3</sub>], and [try<sub>4</sub>]. These are exactly rules that remove an ITE or TRY from the extended command sequence. Since  $\langle P, [G] \rangle$  contains no ITE or TRY statements and only one host graph, we have |S| = #(P') + 1, where # counts the combined number of ITE and TRY statements in an extended command sequence. Since |S| = #(P') + 1, we have  $|S| \ge 1$ .

Now in case (b), we can break down the transition sequence into  $\langle P, [G] \rangle \to^* \langle P', S' \rangle \to S$ . Like in the proof of (a), the formula |S'| = #(P') + 1 applies. Let us examine which inference rules can be applied in the final step of the transition. It can only be either [skip], [call\_1], or [alap\_2]. To apply [skip], P' must be skip and #(skip) = 0, so |S| = |S'| = 1. To apply [call\_1], P' must be rule set call, and hence cannot contain ITE or TRY, so |S| = |S'| = 1. To apply [alap\_2], P' must be of the form TRY(break, P''!, skip), where P'' is an extended command sequence. We know P'' cannot contain an ITE or TRY statement because they can only be nested in their first argument. Indeed, if an extended command sequence already starts with an ITE or TRY, no inference rule allows for said ITE or TRY statement to be nested within another one. So the only way to nest statements is via the rule [try\_2], which modifies the first argument. But the first argument of P' is break, which contains no ITE or TRY statements. So #(P') = 1 and |S'| = 2. Since we apply [alap\_2], we have S = pop2(S'), so |S| = |S'| - 1 = 1.

We also want to make sure that if we call pop2 on a stack to pop its second element, the stack does indeed contain at least two elements. More precisely, under the premise of Lemma 4.4, if  $\langle P, [G] \rangle \to^+ \langle P', \text{pop2}(S) \rangle$  (an extended configuration) or  $\langle P, [G] \rangle \to^+ \text{pop2}(S)$  (a graph stack), then  $|S| \geq 2$ . This follows directly from Lemma 4.4 since |pop2(S)| = |S| - 1.

Let us now use Lemmata 4.1, 4.2, and 4.4 is to describe what the possible outcomes of a transition sequence starting in a valid GP 2 program are.

**Theorem 4.5** (Outcomes of Transition Sequences). Let  $\langle P, [G] \rangle$  be a configuration. Then one of the following applies:

- There is an infinite transition sequence  $\langle P, [G] \rangle \to \langle P_1, S_1 \rangle \to \langle P_2, S_2 \rangle \to \dots$  where  $\langle P_i, S_i \rangle$  is an extended configuration for all  $i \geq 1$ .
- $\langle P, [G] \rangle \rightarrow^+ [G']$  for some host graph G'.
- $\langle P, [G] \rangle \rightarrow^+$  fail.

*Proof.* Lemma 4.4 guarantees that if a transition sequence starts in  $\langle P, [G] \rangle$  and ends in a stack, that stack only contains one graph. So for this proposition, it is enough to show that transition sequences end in a stack in the relevant cases.

In order to get rid of a potential break statement in P, we can apply Lemma 4.2 to  $\langle P, [G] \rangle$ . If we get a graph stack or fail, we fulfil the second or third case of this proposition. Otherwise we get an extended configuration  $\langle P', S \rangle$  that contains no break.

Since there is now no break in either  $\langle P, [G] \rangle$  or  $\langle P', S \rangle$ , we can apply the first, second, and third cases of Lemma 4.1 either indefinitely to get an infinite transition sequence, or until we get a graph stack or fail.

Now that we know the possible outcomes of a transition sequence, we can define the *semantic function*  $\llbracket \_ \rrbracket$ : CommandSeq  $\to$  ( $\mathscr{G} \to 2^{\mathscr{G}^{\oplus}}$ ), where  $\mathscr{G}$  is the set of host graphs,  $\llbracket \mathscr{G} \rrbracket$  the set of stacks consisting of exactly one host graph (which we can identify with single host graphs), and  $\mathscr{G}^{\oplus} = \llbracket \mathscr{G} \rrbracket \cup \{ \text{fail}, \bot \}$ . The symbol  $\bot$  is used to represent an infinite transition sequence, i.e. divergence. The function is defined as

$$\llbracket P \rrbracket G = \{ X \in [\mathscr{G}] \cup \{ \text{fail} \} \mid \langle P, G \rangle \rightarrow^+ X \} \cup \{ \bot \mid P \text{ can diverge from } G \}.$$

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This functions differs from the one presented in [17] and Section 2 since  $\perp$  is only used when P diverges, because we know by Proposition 4.3 that P cannot get stuck. We will show in Lemma 5.5 that every infinite or stuck transition sequence in the previous semantics corresponds to an infinite transition sequence in the new semantics.

Let us now examine the property of *finite nondeterminism* as specified by Apt in Section 4.1 of [1], i.e. the set of elements reachable from a configuration in one transition step is finite. A related concept is *bounded nondeterminism*, where the cardinality of the aforementioned set is finite and depends on the program only (and not on the size of the current state). An example for a language with bounded nondeterminism is Dijkstra's language of guarded commands [18]. Many references [8, 10, 18, 19] equate the concepts of finite and bounded nondeterminism and call it "bounded nondeterminism". However, rule-based languages generally have unbounded nondeterminism because they rely on nondeterministic rule matching. This also applies to GP 2 which the following example illustrates.

**Example 4.6.** Consider the rule  $r: {}_{1}O \leftarrow O \Rightarrow {}_{1}O$  and the *comb graph G*<sub>4</sub> as shown in Figure 6. There

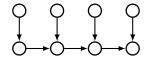


Figure 6: The comb graph  $G_4$ 

are four possible matches for the left-hand side of rule r in graph  $G_4$ , so applying the rule can result in four different non-isomorphic graphs, which is a finite amount. When applying r to comb graph  $G_k$ , we get k non-isomorphic graphs, which depends on the size of the host graph and hence is not bounded.  $\square$ 

We now show that GP 2 does have finite nondeterminism.

**Proposition 4.7** (Finite Nondeterminism). Let  $\gamma \in \text{ExtComSeq} \times \mathscr{S}$  be an extended configuration, and  $T_{\gamma} = \{ \gamma' \mid \gamma \to \gamma' \in \text{ExtCommSeq} \times \mathscr{S} \}$ . Then  $|T_{\gamma}|$  is finite.

*Proof.* The only inference rules that cause nondeterminism are  $[or_1]$ ,  $[or_2]$ , and  $[call_1]$ . If the rules  $[or_1]$  and  $[or_2]$  are applicable to  $\gamma$  then there are exactly two configurations reachable from  $\gamma$ . In  $[call_1]$ , the nondeterminism comes from several GP2 rules being called non-deterministically as part of a rule set, as well as from all the ways these rules can be matched in the host graph. Since rule sets and host graphs are finite, the number of configurations reachable from  $\gamma$  in one step via the inference rule  $[call_1]$  is finite as well.

Reynolds [18] defines this kind of nondeterminism using the semantic function instead of the set of configurations reachable in one step. The following corollary shows that this semantics fulfils that definition as well.

**Corollary 4.8.** Let  $P \in \text{CommandSeq}$  and  $G \in \mathscr{G}$  such that  $[\![P]\!]G$  is infinite. Then  $\bot \in [\![P]\!]G$ .

*Proof.* Let  $\gamma_0 = \langle P, [G] \rangle$ . Then  $T_{\gamma_0}^* = \{ \gamma | \gamma_0 \to^* \gamma \in \text{ExtCommSeq} \times \mathscr{S} \}$  is infinite as well since it contains all elements of  $[\![P]\!]G$  except perhaps fail or  $\bot$ . The set  $T_{\gamma_0}^*$  can be seen as a tree whose nodes are configurations and whose edges are defined by transition relations. Since  $T_{\gamma}$  is finite for all configurations  $\gamma$  by Proposition 4.7, each node in the tree only has finitely many adjacent nodes. By König's Lemma [12], the tree contains an infinite path. Since every node of the tree is reachable from the root  $\gamma_0$ , there is an infinite path starting from  $\gamma_0$ . By definition of the tree, this means there is an infinite transition sequence starting with  $\gamma_0$ . By definition of the semantic function, we can conclude that  $\bot \in [\![P]\!]G$ .

### 5 Comparison to the Previous Semantics

In this section, we show that the new semantics is a conservative extension of the previous one, i.e. their behaviour is equivalent on converging configurations, and if a configuration diverges in the previous semantics, it also diverges in the new one.

When we mention graph stacks in this section, we allow them to be empty. We use the notation  $[G_1, G_2, ..., G_k, S]$  (where  $G_i$  are graphs, S is a graph stack, and k > 0) to denote a stack whose top k elements are  $G_1, G_2, ..., G_k$ , and whose remaining elements are the elements of S.

**Lemma 5.1** (Simulating Finite Old Transition Sequences). Let  $P \in \text{CommandSeq}$ ,  $G \in \mathcal{G}$ , and  $X \in \{\langle P', G' \rangle, G', \text{fail} \}$ , where  $P' \in \text{CommandSeq}$  and  $G' \in \mathcal{G}$ . If  $\langle P, G \rangle \leadsto^* X$ , then, for any graph stack S, there is a transition sequence

- $\langle P, [G,S] \rangle \to^* \langle P', [G',S] \rangle$  if  $X = \langle P', G' \rangle$ .
- $\langle P, [G,S] \rangle \rightarrow^* [G']$  if X = G'.
- $\langle P, [G,S] \rangle \to^*$  fail if X =fail.

*Proof.* We shall prove this lemma by induction on the number of if, try, and ! statements in *P* combined.

If *P* has no if, try, or! statements, none of the [if], [try], and [alap] inference rules are applicable. All other rules behave identically in both semantics when identifying the tops graph of the stacks in the new rules with the graphs in the previous rules. Hence the base case is satisfied.

As the induction hypothesis, assume the lemma holds for P containing k if, try, or! statements. Now consider the case where P contains k+1 of them. Let  $\langle P_1, G_1 \rangle \leadsto \langle P_2, G_2 \rangle$  be a derivation step of  $\langle P, G \rangle \leadsto^* X$  that uses an [if], [try], or [alap] rule (possibly as a premise for another rule such as [seq\_1]). If such a step does not exist, the lemma holds by the same argument used in the base case. Consider the [if], [try], or [alap] rule [r] that relates to the if, try, or! statement enclosing all others resolved in the same step. Then no rule where [r] is a premise (or the premise of a premise) is an [if], [try], or [alap] rule. Since those are identical in both semantics, we only need to show that the part of P resolved by [r] is resolved in a way that fulfils the lemma statement.

If  $[r] = [if_1]$ , we have  $\langle if \ C$  then  $P_3$  else  $Q, G_3 \rangle \leadsto \langle P_3, G_3 \rangle$  under the premise of  $\langle C, G_3 \rangle \leadsto^+ H$ . Since P contains k+1 if, try, or ! statements, C contains at most k of them. So by the induction hypothesis, there is a transition sequence  $\langle C, [G_3, S] \rangle \to^* [H, S]$  (for any graph stack S). We can decompose this transition sequence into  $\langle C, [G_3, S] \rangle \to^l \langle C_4, S_4 \rangle \to [H, S]$  where  $l \geq 0$ , and  $S_4$  is a graph stack. This fulfills the premise of  $[if_2]$  l times, and then the premise of  $[if_3]$  once. So for any graph stack S', the premises are fulfilled by choosing  $S = [G_3, S']$ , and we have

$$\begin{split} \langle \text{if } C \text{ then } P_3 \text{ else } Q, [G_3,S'] \rangle & \rightarrow_{[\text{i}f_1]} \langle \text{ITE}(C,P_3,Q), [G_3,G_3,S'] \rangle \rightarrow_{[\text{i}f_2]}^l \langle \text{ITE}(C_4,P_3,Q), S_4 \rangle \\ & \rightarrow_{[\text{i}f_3]} \langle P_2, [G_3,S'] \rangle. \end{split}$$

If  $[r] = [if_2]$ , we have  $\langle if \ C$  then  $P_3$  else  $Q, G_3 \rangle \leadsto \langle Q, G_3 \rangle$  under the premise of  $\langle C, G_3 \rangle \leadsto^+$  fail. Again, we can use the induction hypothesis to conclude  $\langle C, [G_3, S] \rangle \to^*$  fail (for any graph stack S), which is a sequence of  $l \geq 0$  transitions  $\langle C, [G_3, S] \rangle \to^l \langle C_4, S_4 \rangle$  between extended configurations, followed by 1 transition  $\langle C_4, S_4 \rangle \to$  fail. This fulfills the premise of  $[if_2] \ l$  times, and then the premise of  $[if_4]$  once. So for any graph stack S', the premises are fulfilled by choosing  $S = [G_3, S']$ , and we have

$$\begin{split} \langle \text{if } C \text{ then } P_3 \text{ else } Q, [G_3,S'] \rangle \rightarrow_{[\text{if}_1]} \langle \text{ITE}(C,P_3,Q), [G_3,G_3,S'] \rangle \rightarrow_{[\text{if}_2]}^l \langle \text{ITE}(C_4,P_3,Q), S_4 \rangle \\ \rightarrow_{[\text{if}_4]} \langle Q, [G_3,S'] \rangle. \end{split}$$

If  $[r] = [try_1]$ , we can use the same arguments as when  $[r] = [if_1]$  to get a transition sequence  $\langle try \ C \ then \ P_3 \ else \ Q, \ [G_3, S'] \rangle \rightarrow^+ \langle P_3, \ [G_4, S'] \rangle$  for any graph stack S'.

If  $[r] = [try_2]$ , we can use the same arguments as when  $[r] = [if_2]$  to get a transition sequence  $\langle try \ C \ then \ P_3 \ else \ Q, \ [G_3, S'] \rangle \rightarrow^+ \langle Q, \ [G_3, S'] \rangle$  for any graph stack S'.

If  $[r] = [alap_1]$ , we have  $\langle P_3!, G_3 \rangle \leadsto \langle P_3!, H \rangle$  under the premise of  $\langle P_3, G_3 \rangle \leadsto^+ H$ . Since P contains k+1 if, try, or ! statements,  $P_3$  contains at most k of them. So by the induction hypothesis, we can conclude  $\langle P_3, [G_3, S] \rangle \to^* [H, S]$  for any graph stack S. We can decompose this transition sequence into  $\langle P_3, [G_3, S] \rangle \to^l \langle P_4, S_4 \rangle \to [H, S]$ , where  $l \geq 0$ , and  $S_4$  is a graph stack. These derivations fulfil the premise of  $[try_2]$  l times and then the premise of  $[try_3]$  once. So for any graph stack S', the premises are fulfilled by choosing  $S = [G_3, S']$ , and we have

$$\begin{split} \langle P_3!\,,\,[G_3,S']\rangle \to_{[\text{alap}_1]} \langle \text{try}\,P_3 \text{ then } P! \text{ else skip}, [G_3,S']\rangle \to_{[\text{try}_1]} \langle \text{TRY}(P_3,P_3!\,,\text{skip}),\,[G_3,G_3,S']\rangle \\ \to_{[\text{try}_2]}^l \langle \text{TRY}(P_4,P_3!\,,\text{skip}),\,S_4\rangle \to_{[\text{try}_3]} \langle P_3!\,,\,[H,S']\rangle. \end{split}$$

If  $[r] = [alap_2]$ , we have  $\langle P_3!, G_3 \rangle \leadsto G_3$  under the premise of  $\langle P_3, G_3 \rangle \leadsto^+$  fail. Again, we can use the induction hypothesis to conclude  $\langle P_3, [G_3, S] \rangle \to^*$  fail for any graph stack S. We can decompose this transition sequence into  $\langle P_3, [G_3, S] \rangle \to^l \langle P_4, [G_4, S] \rangle \to$  fail, where  $l \geq 0$ , and  $G_4$  is a graph. Let us argue that the graph stack right before the fail is  $[G_4, S]$ . The stack ends in S because  $P_3$  is a command sequence and hence does not contain ITE or TRY statements, which are the only constructs that could pop and hence modify S. Any if, try, and ! statements in  $P_3$  must resolve before the configuration that leads to fail in one step because fail is not reachable in one step from if, try, !, ITE, and TRY statements. Since they all resolved, any push has a corresponding pop, meaning the number of graphs in the stack before S remains 1. The derivations fulfil the premise of  $[try_2] l$  times and then the premise of  $[try_4]$  once. So for any graph stack S', the premises are fulfilled by choosing  $S = [G_3, S']$ , and we have

$$\begin{split} \langle P_3!, [G_3,S'] \rangle \rightarrow_{[\text{alap}_1]} \langle \text{try}\, P_3 \text{ then}\, P! \text{ else skip}, [G_3,S'] \rangle \rightarrow_{[\text{try}_1]} \langle \text{TRY}(P_3,P_3!,\text{skip}), [G_3,G_3,S'] \rangle \\ \rightarrow_{[\text{try}_2]}^{l} \langle \text{TRY}(P_4,P_3!,\text{skip}), [G_4,G_3,S'] \rangle \rightarrow_{[\text{try}_4]} \langle \text{skip}, [G_3,S'] \rangle \rightarrow_{[\text{skip}]} [G_3,S']. \end{split}$$

If  $[r] = [alap_3]$ , we have  $\langle P_3!, G_3 \rangle \leadsto H$  under the premise of  $\langle P_3, G_3 \rangle \leadsto^* \langle break, H \rangle$ . Again, we can use the induction hypothesis to conclude  $\langle P_3, [G_3, S] \rangle \to^* \langle break, [H, S] \rangle$  (for any graph stack S). Let  $l \geq 0$  be the number of steps in that transition sequence. These derivations fulfil the premise of  $[try_2]$  l times. So for any graph stack S', the premises are fulfilled by choosing  $S = [G_3, S']$ , and we have

$$\begin{array}{c} \langle P_3!\,, [G_3,S']\rangle \rightarrow_{[\mathrm{alap_1}]} \langle \mathtt{try}\,P_3\,\mathtt{then}\,P_3!\,\,\mathtt{else}\,\mathtt{skip}, [G_3,S']\rangle \rightarrow_{[\mathtt{try_1}]} \langle \mathtt{TRY}(P_3,P_3!\,,\mathtt{skip}), [G_3,G_3,S']\rangle \\ \rightarrow_{[\mathtt{try_2}]}^l \langle \mathtt{TRY}(\mathtt{break},P_3!\,,\mathtt{skip}), [H,G_3,S']\rangle \rightarrow_{[\mathtt{alap_2}]} [H,S']. \end{array} \qquad \Box$$

**Lemma 5.2** (Configurations That Get Stuck). Let  $\langle P,G\rangle$  be a configuration to which no inference rule of the previous semantics is applicable. Then P starts with an if, try, or! statement such that the condition (or the body in the case of!) diverges or gets stuck in the previous semantics.

*Proof.* For this proof, whenever we say a rule is applicable, we mean it is either applicable, or it can be used as a premise for a [seq] rule.

Let us first show that, if P does not start with an if, try, or ! statement, we can apply an inference rule to  $\langle P,G\rangle$ . If P starts with a rule set, that rule set is either applicable to the host graph or not, so either [call<sub>1</sub>] or [call<sub>2</sub>] can be applied. If P starts with a break, skip, or fail, then [break], [skip], or [fail] can be applied respectively. If P starts with an or statement, either [or<sub>1</sub>] or [or<sub>2</sub>] can be applied.

Now assume that P starts with an if, try, or! statement with a condition or body C. If  $\langle C, G \rangle$  neither converges nor gets stuck, there is a transition sequence  $\langle C, G \rangle \leadsto \text{fail}$ , or  $\langle C, G \rangle \leadsto H$  for some graph H. Hence one of  $[\text{if}_1]$ ,  $[\text{if}_2]$ ,  $[\text{try}_1]$ ,  $[\text{try}_2]$ ,  $[\text{alap}_1]$ , or  $[\text{alap}_2]$  is applicable.

**Lemma 5.3** (Loop-Free Command Sequences Do Not Get Stuck). For every loop-free command sequence P and graph G, no transition sequence starting with  $\langle P, G \rangle$  gets stuck in the previous semantics.

*Proof.* We prove this lemma by structural induction. For a base case, consider programs consisting of a single rule set R. Then on any graph G, R is either applicable or not. So to  $\langle R, G \rangle$  we can apply either [call<sub>1</sub>] or [call<sub>2</sub>], leading to a graph or fail. They are both transition sequences that end in a terminal state, and hence do not get stuck.

Another base case is skip or fail, which always lead to a graph or fail in a single step, and hence cannot lead to stuck transition sequences.

The break statement cannot be in P since context conditions require it to have an enclosing loop.

For the induction step, assume that every proper subprogram of *P* cannot get stuck, and show that *P* cannot get stuck either.

Assume  $P = P_1; P_2$ . By the induction hypothesis, for any graph G, no transition sequence starting with  $\langle P_1, G \rangle$  gets stuck, i.e. they can all be extended to either  $\langle P_1, G \rangle \leadsto^+$  fail or  $\langle P_1, G \rangle \leadsto^+ H$  for some graph H. This fulfils the premise of  $[\text{seq}_1]$  some number of times, and then the premise of either  $[\text{seq}_2]$  or  $[\text{seq}_3]$  once. So each transition sequence staring with  $\langle P, G \rangle$  must be of the form  $\langle P, G \rangle \leadsto^+_{[\text{seq}]}$  fail or  $\langle P, G \rangle \leadsto^+_{[\text{seq}]} H$ .

Assume P = if C then  $P_1$  else  $P_2$ . Then by induction hypothesis, for any graph G, no transition sequence starting with  $\langle C, G \rangle$  can get stuck, i.e. they can all be extended to either  $\langle C, G \rangle \leadsto^+$  fail or  $\langle C, G \rangle \leadsto^+ H$  for some graph H. This satisfies the premise of either  $[\text{if}_1]$  or  $[\text{if}_2]$ . So each transition sequence staring with  $\langle P, G \rangle$  must be of the form  $\langle P, G \rangle \leadsto_{[\text{if}_1]} \langle P_1, G \rangle$  or  $\langle P, G \rangle \leadsto_{[\text{if}_2]} \langle P_2, G \rangle$ . Any continuation of these sequences cannot get stuck because  $P_1$  and  $P_2$  satisfy the induction hypothesis.

The case P = try C then  $P_1$  else  $P_2$  is analogous to the previous one.

If P is an if or try with omitted then or else clauses, one of  $[if_3]$ ,  $[try_3]$ ,  $[try_4]$ , or  $[try_5]$  can be applied, and then the arguments used in the if and try cases can be applied.

Assume  $P = P_1$  or  $P_2$ . Then for any graph G, any transition sequence starting with  $\langle P, G \rangle$  starts with either  $\langle P, G \rangle \leadsto_{[or_1]} P_1$  or  $\langle P, G \rangle \leadsto_{[or_2]} P_2$ . Any continuation of these sequences cannot get stuck because  $P_1$  and  $P_2$  satisfy the induction hypothesis.

**Lemma 5.4** (Non-Nested Loops Do Not Get Stuck). For every loop-free command sequence P and graph G, no transition sequence starting with  $\langle P!, G \rangle$  gets stuck in the previous semantics.

*Proof.* It is enough to show that either  $\langle P,G\rangle \leadsto^+ H$ ,  $\langle P,G\rangle \leadsto^+$  fail, or  $\langle P,G\rangle \leadsto^* \langle \text{break},H\rangle$ . Because then, one of [alap<sub>1</sub>], [alap<sub>2</sub>], or [alap<sub>3</sub>] is applicable. And if [alap<sub>1</sub>] was applicable, we get  $\langle P!,G\rangle \leadsto \langle P!,H\rangle$ , the same arguments can be used on  $\langle P!,H\rangle$ , and hence on all its successors.

First of all, since P contains no !,  $\langle P,G \rangle$  cannot diverge. If P does not contain a break,  $\langle P,G \rangle$  cannot get stuck by Lemma 5.3. If P does contain a break, that is never called, the arguments of the proof of Lemma 5.3 still apply, and  $\langle P,G \rangle$  does not get stuck. If P contains a break that is called, that means there is a transition sequence  $\langle P,G \rangle \leadsto^* \langle \mathtt{break}, H \rangle$ , or  $\langle P,G \rangle \leadsto^* \langle \mathtt{break}; Q,H \rangle$ , to which we can apply [break] to get the former.

So either  $\langle P,G\rangle \leadsto^* \langle \text{break}, H\rangle$ , or  $\langle P,G\rangle$  neither diverges nor gets stuck, which means  $\langle P,G\rangle$  must resolve to either a graph or fail.

**Lemma 5.5** (Simulating Old Transition Sequences That Are Infinite or Stuck). Assume there is an infinite transition sequence  $\langle P, G \rangle \leadsto \ldots$ , or a stuck transition sequence  $\langle P, G \rangle \leadsto^* \langle P', G' \rangle$ . Then for any graph stack S, there is an infinite transition sequence  $\langle P, [G, S] \rangle \to \ldots$ 

*Proof.* First assume there is an infinite transition sequence  $\langle P,G\rangle \rightsquigarrow \langle P_1,G_1\rangle \rightsquigarrow \ldots$  To each step  $\langle P_i,G_i\rangle \rightsquigarrow \langle P_{i+1},G_{i+1}\rangle$  in that transition sequence, we can apply Lemma 5.1 to get a transition sequence  $\langle P_i,[G_i,S_i]\rangle \rightarrow^* \langle P_{i+1},[G_{i+1},S_{i+1}]\rangle$  for any graph stack  $S_i$  and some  $S_{i+1}$ . We can concatenate these into an infinite transition sequence.

Now assume there is a stuck transition sequence  $\langle P,G\rangle \rightsquigarrow^* \langle P',G'\rangle$ . We can apply Lemma 5.1 to each step in that sequence to get  $\langle P,[G,S]\rangle \rightarrow^* \langle P',[G',S]\rangle$  for each graph stack S.

We claim that for any command sequence P' such that  $\langle P', G' \rangle$  is stuck with respect to  $\leadsto$ , there is a diverging transition sequence  $\langle P', [G', S] \rangle \to \ldots$  for any graph stack S. This is enough to prove the Lemma. Let us show this by induction on the combined number of if, try, and ! statements in P'.

If there are no such statements, then  $\langle P',G'\rangle$  cannot get stuck by Lemma 5.2, so there has to be at least one, and P' starts with it. If that one statement is an if or a try, then  $\langle P',G'\rangle$  cannot get stuck by Lemma 5.3. If it is a! statement,  $\langle P',G'\rangle$  cannot get stuck by Lemma 5.4. So there have to be at least two if, try, or! statements.

So for our base case, assume P' contains exactly two if, try, or! statements. By Lemma 5.3, P' must contain a! statement. If the two statements are not nested, we can apply Lemmata 5.3 and/or 5.4 sequentially to conclude that  $\langle P', G' \rangle$  does not get stuck. So the two statements must be nested, and they must be the start of P' by Lemma 5.2. Assume the "inner" statement is not a! statement. Then the "outer" statement must be the! statement. By Lemma 5.4,  $\langle P', G' \rangle$  does not get stuck. So the "inner" statement must be a! statement Q!. The loop Q! cannot resolve to a graph or fail because then, the "outer" statement could be resolved and  $\langle P', G' \rangle$  would not be stuck. By Lemma 5.4, Q! cannot get stuck either. So Q! must diverge, and so must the condition or body C of the starting statement of P'. So there is an infinite transition sequence  $\langle C, G' \rangle \leadsto \ldots$ , and hence by Lemma 5.1 to each step in that transition sequence, we get  $\langle C, [G', S] \rangle \to \ldots$  for any graph stack S. This serves as a premise for [if<sub>2</sub>] or [try<sub>2</sub>], which we can use to get an infinite transition sequence  $\langle P', G' \rangle \to \ldots$ 

Now for the induction step, assume we get infinite transition sequences for programs with at most k if, try, and ! statements. Assume P' has k+1 such statements. By Lemma 5.2, since  $\langle P', G' \rangle$  is stuck, P' must start with one of those statements. So we can apply either  $[if_1]$ ,  $[try_1]$ , or  $[alap_1]$  followed by  $[try_1]$  to get  $\langle P', [G', S'] \rangle \to^+ \langle P'', [G', G', S'] \rangle$  for any graph stack S', where P'' starts with either ITE(C, Q, Q') or TRY(C, Q, Q'). Now C has at least one less if, try, or! statement than P', so we can apply the induction hypothesis to get an infinite transition sequence  $\langle C, [G', G', S'] \rangle \to \ldots$ , which can serve as premises for infinitely many applications of  $[if_2]$  or  $[try_2]$  (or these inference rules provide the premises for  $[seq_1]$ ). Hence we have an infinite transition sequence  $\langle P', [G', S'] \rangle \to^+ \langle P'', [G', G', S'] \rangle \to \ldots$ 

**Lemma 5.6** (Simulating Finite New Transition Sequences). Let  $P \in \text{CommandSeq}$ ,  $G \in \mathcal{G}$ , S a graph stack, and  $X \in \{\langle P', [G', S'] \rangle, [G', S'], \text{ fail}\}$ , where  $P' \in \text{CommandSeq}$ , S' is a graph stack, and  $S' \in \mathcal{G}$ . If  $S' \in \mathcal{G}$ , then there is a transition sequence

- $\langle P, G \rangle \leadsto^* \langle P', G' \rangle$  if  $X = \langle P', [G', S'] \rangle$ .
- $\langle P, G \rangle \leadsto^* G' \text{ if } X = [G', S'].$
- $\langle P, G \rangle \leadsto^* \text{ fail if } X = \text{fail.}$

*Proof.* Let us show this lemma by induction on the combined number of if, try, and ! statements in P. If P has no such statements, no step in  $\langle P, [G,S] \rangle \to^* X$  uses [if], [try], or [alap] rules. So no pushing or popping occurs and only the top of the stack is modified. The applied rules behave identically to those in the previous semantics when identifying the top of the stacks in the new rules with the host graphs in the previous rules. Hence the lemma is satisfied in the base case.

Now assume that the lemma holds for command sequences with k if, try, and ! statements, and assume P has k+1 of them. By the previous paragraph, we can simulate transition steps that do not involve [if], [try], or [alap] rules. So let  $\langle Q, [H,S'] \rangle$  be a configuration (we will handle the case where  $\langle Q, [H,S'] \rangle$  is an extended configuration, but not a configuration later) in the sequence  $\langle P, [G,S] \rangle \rightarrow^* X$  that uses such a rule to get the next state. Let [r] be that rule. It can only be [if<sub>1</sub>], [try<sub>1</sub>], or [alap<sub>1</sub>] since  $\langle Q, [H,S'] \rangle$  is a configuration and hence does not contain ITE or TRY.

If  $[r] = [if_1]$ , then we get  $\langle Q, [H,S'] \rangle \rightarrow \langle ITE(C,Q_1,Q_2);Q_3, [H,H,S'] \rangle \rightarrow^+ \langle Q_4;Q_3, [H,S'] \rangle$ , where  $Q_1$ ,  $Q_2$ , and  $Q_3$  are command sequences, and where  $Q_4 \in \{Q_1,Q_2\}$  (since this is part of a transition sequence that ends in X, a configuration, graph, or fail, we know that the if eventually resolves). So either  $\langle C, [H,H,S'] \rangle \rightarrow^* [H',H,S']$  or  $\langle C, [H,H,S'] \rangle \rightarrow^*$  fail. We can apply the induction hypothesis to C, which has at most k if, try, and ! statements, to get that either  $\langle C,H \rangle \rightsquigarrow^* H'$  or  $\langle C,H \rangle \rightsquigarrow^*$  fail. We can use this as a premise for either  $[if_1]$  or  $[if_2]$  to get  $\langle Q,H \rangle \rightsquigarrow \langle Q_4;Q_3,H \rangle$ , where  $Q_4 \in \{Q_1,Q_2\}$ , which simulates the sequence outlined at the beginning of this case.

If  $[r] = [try_1]$ , then we get either  $\langle Q, [H,S'] \rangle \rightarrow \langle TRY(C,Q_1,Q_2); Q_3, [H,H,S'] \rangle \rightarrow^+ \langle Q_1; Q_3, [H',S'] \rangle$  or  $\langle Q, [H,S'] \rangle \rightarrow \langle TRY(C,Q_1,Q_2); Q_3, [H,H,S'] \rangle \rightarrow^+ \langle Q_2; Q_3, [H,S'] \rangle$ , where  $Q_1, Q_2$ , and  $Q_3$  are command sequences, and H' some graph. We can use the same arguments as in the previous case to get a transition sequence  $\langle Q,H \rangle \rightsquigarrow \langle Q_1; Q_3,H' \rangle$  or  $\langle Q,H \rangle \rightsquigarrow \langle Q_2; Q_3,H \rangle$ .

If  $[r] = [alap_1]$ , we have  $Q = Q_1!$ ;  $Q_2$  so either  $\langle Q_1!$ ;  $Q_2, [H,S'] \rangle \rightarrow^* \langle Q_1!$ ;  $Q_2, [H',S'] \rangle$ ,  $\langle Q_1!$ ;  $Q_2, [H,S'] \rangle \rightarrow^* \langle Q_2, [H,S'] \rangle$ , or  $\langle Q_1!$ ;  $Q_2, [H,S'] \rangle \rightarrow^* \langle TRY(break, Q_1!, skip); Q_2, [H',H,S'] \rangle \rightarrow \langle Q_2, [H',S'] \rangle$ . We can conclude that either  $\langle Q_1, [H,S'] \rangle \rightarrow^* [H',S'], \langle Q_1, [H,S'] \rangle \rightarrow^*$  fail, or  $\langle Q_1, [H,S'] \rangle \rightarrow^*$  (break,  $\langle Q_1, H \rangle \rightarrow^* \langle P_1, P_2 \rangle$ ). By induction hypothesis, we get that either  $\langle Q_1, H \rangle \rightarrow^* H', \langle Q_1, H \rangle \rightarrow^*$  fail, or  $\langle Q_1, H \rangle \rightarrow^* \langle P_1, H \rangle \rightarrow^* \langle P_2, H \rangle$ . These can be used as premises of  $[alap_1]$ ,  $[alap_2]$ , or  $[alap_3]$  to conclude that either  $\langle Q_1!$ ;  $Q_2, H \rangle \rightarrow^* \langle Q_1!$ ;  $Q_2, H \rangle \rightarrow^* \langle Q_2, H \rangle$ .

Finally, if  $\langle Q, [H,S'] \rangle$  is an extended configuration, but not a configuration, Q must contain an ITE, TRY, or a break that does not satisfy the context conditions. All of these must originate in an if, try, or! statement, and are hence covered by the previous part of the proof.

Note that the first point of Lemma 5.6 only applies to transition sequences between command sequences (they do not contain TRY or ITE constructs). So diverging transition sequences in the new semantics where all command sequences contain ITE or TRY after some step cannot be simulated with diverging transition sequences in the previous semantics.

**Theorem 5.7.** Let  $P \in \text{CommandSeq}$  and  $G \in \mathcal{G}$ . Then

- (a)  $[P]G \subset \llbracket P \rrbracket G$  and
- (b)  $[P]G \setminus \{\bot\} = [P]G \setminus \{\bot\}.$

*Proof.* Lemma 5.1 guarantees that (a) holds for graphs and fail, and Lemma 5.5 guarantees that (a) holds for  $\bot$ . Furthermore, (b) follows from (a) and Lemma 5.6.

# 6 Conclusion

We have introduced a new operational semantics for the graph programming language GP 2. Unlike the previous semantics, this one is entirely small-step and non-blocking. As a consequence, it accurately models computations which intuitively should diverge (and do so in the implementation). In particular, the new semantic function correctly lists  $\bot$  as an outcome when there is a computation in which the

condition of a branching statement or the body of a loop diverges. We also obtain finite nondeterminism, meaning that for every configuration there are only finitely many choices for the next transition step.

Furthermore, we have shown that the new semantic function is an extension of the previous one, and that they are equivalent excluding divergence.

In future work, the new semantics should serve as a solid underpinning for setting up a time and space complexity theory for GP 2. Its small-step nature is crucial to defining atomic computation steps. Such a theory could possibly be automated akin to the resource analysis in [13].

Another aspect of the GP2 semantics is that it is orthogonal to the definition of the transformation rules. The inference rules that depend on the domain of graph transformation only need the definition of a rule application ([call<sub>1</sub>]) and the information when such an application fails ([call<sub>2</sub>]). Hence this semantics could be used as a foundation for GP2-like programming languages over other rule-based domains, such as string rewriting [5] or term rewriting [2].

**Acknowledgements.** We are grateful to the anonymous referees whose comments helped to improve the presentation of this paper.

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